AMD Athlon 64 X2 5000+ (rev. G2, DO) specifications

General information	
Type	CPU / Microprocessor
Market segment	Desktop
Family	AMD Athlon 64 X2
Model number	**5000+**
CPU part numbers	**ADO5000IAA5DO** is an OEM/tray microprocessor • **ADO5000DOBOX** is a boxed microprocessor with fan and heatsink
Stepping codes	AAA9G CAA9G CAAEG CAAIG NAA9G NAAEG NAAFG NAAIG NAAWG
Frequency	**5000+ (rated)** **2600 MHz (real)**
Bus speed	**One 1000 MHz 16-bit HyperTransport link (2 GT/s)**
Clock multiplier	**13**
Package	940-pin organic microPGA
Socket	**Socket AM2**
End-of-Life date	Last order date is end of 3rd quarter 2008 [1]
Architecture / Microarchitecture	
Microarchitecture	K8
Platform	Cartwheel
Processor core	Brisbane
Core stepping	BH-G2
CPUID	60FB2
Manufacturing process	0.065 micron silicon-on-insulator (SOI) technology 221 million transistors
Data width	**64 bit**
The number of CPU cores	**2**
The number of threads	**2**
Floating Point Unit	Integrated
Level 1 cache size	2 x 64 KB 2-way set associative instruction caches 2 x 64 KB 2-way set associative data caches
Level 2 cache size	**2 x 512 KB exclusive 16-way set associative caches**
Physical memory	1 TB
Virtual memory	256 TB
Multiprocessing	Uniprocessor
Extensions and Technologies	• MMX instructions • Extensions to MMX • 3DNow! technology • Extensions to 3DNow! • SSE / Streaming SIMD Extensions • SSE2 / Streaming SIMD Extensions 2 • SSE3 / Streaming SIMD Extensions 3 • AMD64 / AMD 64-bit technology • EVP / Enhanced Virus Protection • AMD-V / AMD Virtualization technology
Low power features	• Cool'n'Quiet technology • Halt state • Stop Grant state
Integrated peripherals / components	
Integrated graphics	None
Memory controller	The number of controllers: 1 Memory channels: 2 Supported memory: DDR2-800 Maximum memory bandwidth (GB/s): 12.8

Other peripherals	HyperTransport 2.0 technology
Electrical / Thermal parameters	
V core	1.325V / 1.35V / 1.375V
Maximum operating temperature	55°C - 68°C
Thermal Design Power	**65 Watt**

Notes on AMD Athlon 64 X2 5000+ (rev. G2, DO)

[1] - Information is not confirmed