

MOS FIELD EFFECT TRANSISTOR 2SK2826

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK2826 is N-Channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Super Low On-state Resistance $R_{DS(on)1} = 6.5 \text{ m}\Omega$ MAX. (Vgs = 10 V, ID = 35 A) $R_{DS(on)2} = 9.7 \text{ m}\Omega$ MAX. (Vgs = 4.0 V, ID = 35 A)
- Low Ciss : Ciss = 7200 pF TYP.
- Built-in Gate Protection Diode

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK2826	TO-220AB
2SK2826-S	TO-262
2SK2826-ZJ	TO-263
2SK2826-Z	TO-220SMD ^{Note}

★ Note TO-220SMD package is produced only in Japan.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	VDSS	60	V
Gate to Source Voltage (Vps = 0 V)	VGSS(AC)	±20	V
Gate to Source Voltage (Vps = 0 V)	VGSS(DC)	+20, -10	V
Drain Current (DC) (Tc = 25°C)	I _{D(DC)}	±70	Α
Drain Current (pulse) Note1	ID(pulse)	±280	Α
Total Power Dissipation (Tc = 25°C)	P _{T1}	100	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to + 150	°C
Single Avalanche Current Note2	las	70	Α
Single Avalanche Energy Note2	Eas	490	mJ

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1%

2. Starting Tch = 25°C, Vdd = 30 V, Rg = 25 Ω , Vgs = 20 \rightarrow 0 V

(TO-220AB)



(TO-262)



(TO-263, TO-220SMD)



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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

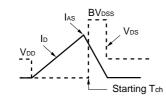


ELECTRICAL CHARACTERISTICS (TA = 25°C)

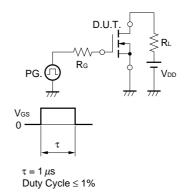
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	Vps = 60 V, Vgs = 0 V			10	μΑ
Gate Leakage Current	Igss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.0	1.5	2.0	٧
Forward Transfer Admittance	yfs	V _{DS} = 10 V, I _D = 35 A	20	94		S
Drain to Source On-state Resistance	RDS(on)1	V _G S = 10 V, I _D = 35 A		5.5	6.5	mΩ
	RDS(on)2	V _G S = 4.0 V, I _D = 35 A		7.0	9.7	mΩ
Input Capacitance	Ciss	Vps = 10 V		7200		pF
Output Capacitance	Coss	V _{GS} = 0 V		2000		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		700		pF
Turn-on Delay Time	td(on)	ID = 35 A		100		ns
Rise Time	tr	V _G S = 10 V		1200		ns
Turn-off Delay Time	t _{d(off)}	V _{DD} = 30 V		440		ns
Fall Time	t f	$R_G = 10 \Omega$		520		ns
Total Gate Charge	Q _G	ID = 70 A		150		nC
Gate to Source Charge	Qgs	V _{DD} = 48 V		20		nC
Gate to Drain Charge	Q _{GD}	V _{GS} = 10 V		40		nC
Body Diode Forward Voltage	VF(S-D)	IF = 70 A, VGS = 0 V		0.97		V
Reverse Recovery Time	trr	IF = 70 A, Vgs = 0 V		80		ns
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s$		250		nC

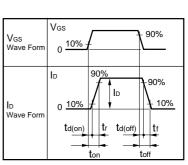
TEST CIRCUIT 1 AVALANCHE CAPABILITY

$R_{G} = 25 \Omega$ $P_{G} \longrightarrow 50 \Omega$ V_{DD} $R_{G} = 25 \Omega$ V_{DD} $R_{D} \longrightarrow V_{DD}$ $R_{D} \longrightarrow V_{DD}$



TEST CIRCUIT 2 SWITCHING TIME

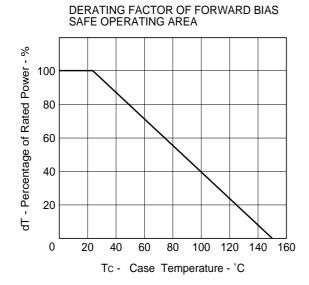


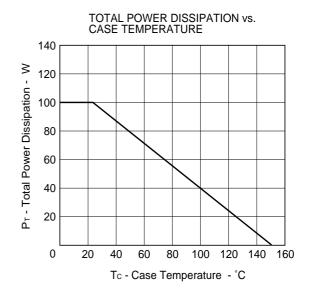


TEST CIRCUIT 3 GATE CHARGE

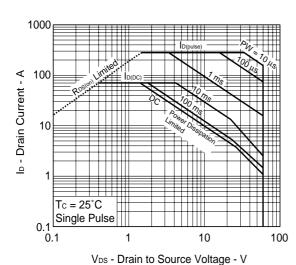


TYPICAL CHARACTERISTICS (TA = 25°C)

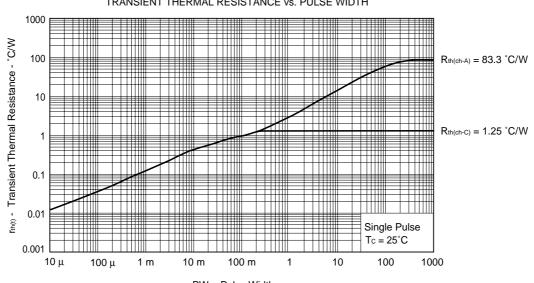




FORWARD BIAS SAFE OPERATING AREA



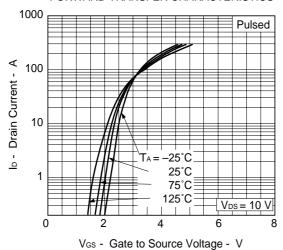
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



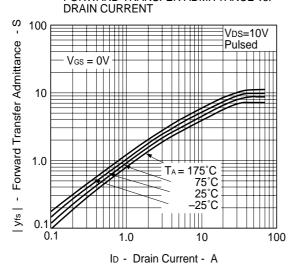
PW - Pulse Width - s Data Sheet D11273EJ3V0DS

3

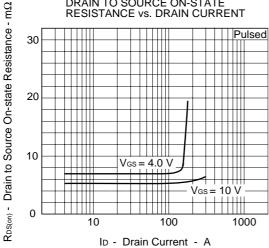
FORWARD TRANSFER CHARACTERISTICS



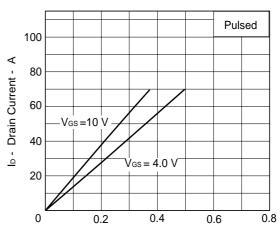
FORWARD TRANSFER ADMITTANCE vs.



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

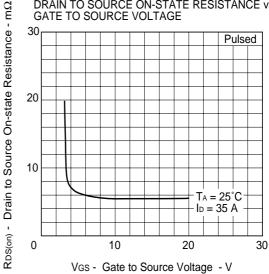


DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

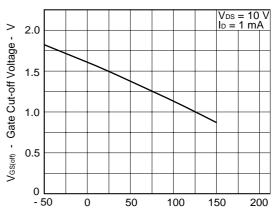


V_{DS} - Drain to Source Voltage - V

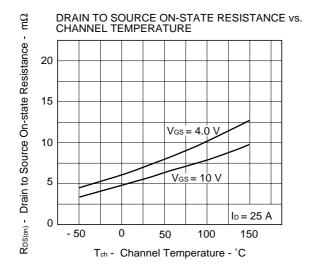
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

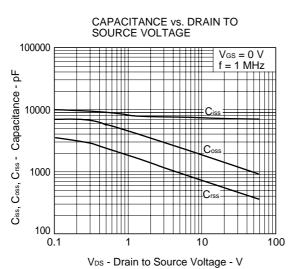


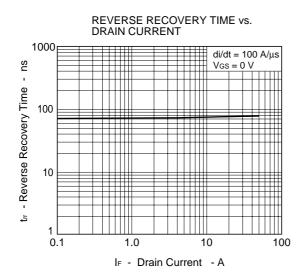
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



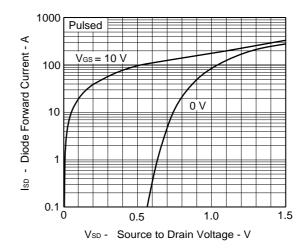
 T_{ch} - Channel Temperature - °C



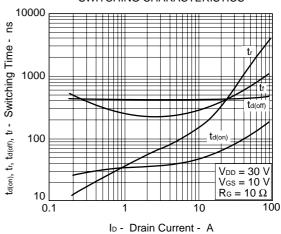


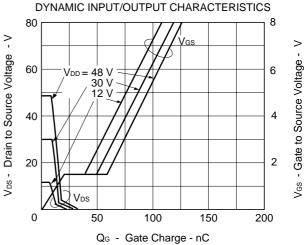




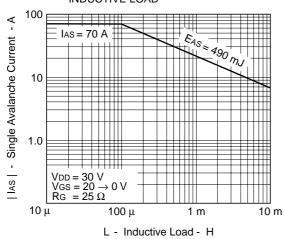


SWITCHING CHARACTERISTICS

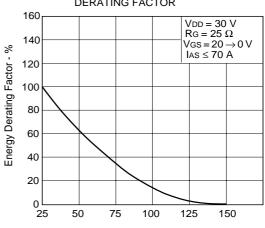




SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



SINGLE AVALANCHE ENERGY DERATING FACTOR

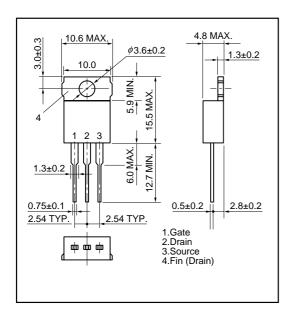


Starting Tch - Starting Channel Temperature - $^{\circ}$ C

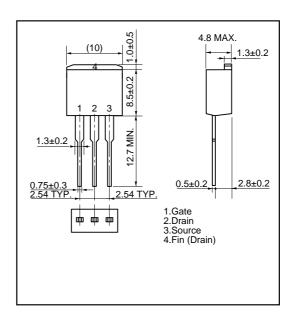


★ PACKAGE DRAWINGS (Unit: mm)

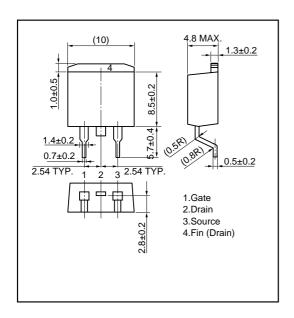
1) TO-220AB(MP-25)



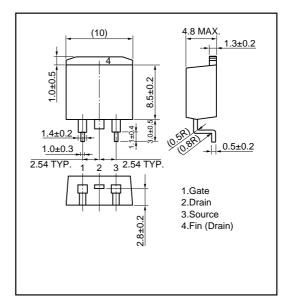
2) TO-262(MP-25 Fin Cut)



3) TO-263 (MP-25ZJ)

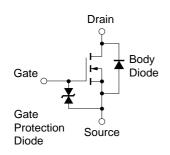


4) TO-220SMD(MP-25Z)^{Note}



Note This package is produced only in Japan.

EQUIVALENT CIRCUIT



Remark

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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