

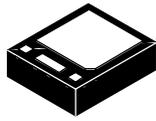
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®

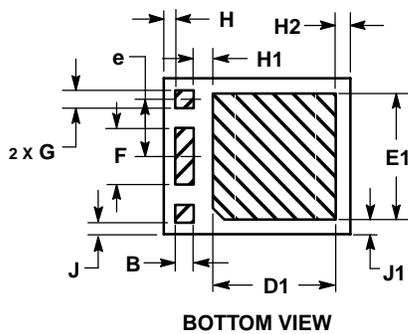
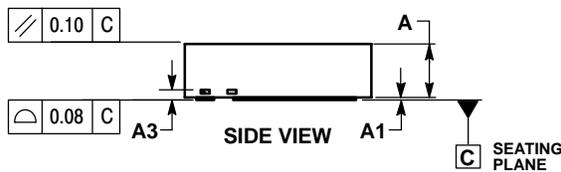
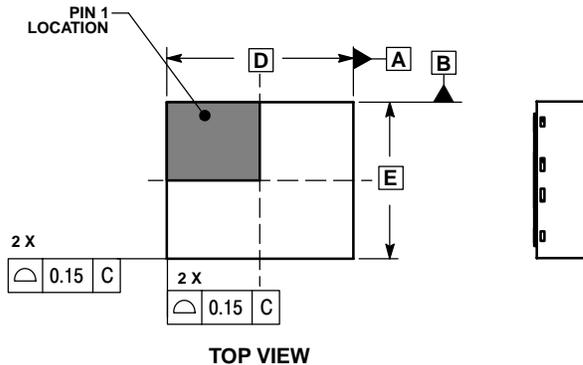


PLL4
CASE 508AA-01
ISSUE O

DATE 21 JUL 2004



SCALE 1:1



- NOTES:
1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
 2. DIMENSIONS IN MILLIMETERS.
 3. TOLERANCES: ± 0.10 MM.

DIM	MILLIMETERS	
	MIN	MAX
A	1.750	1.950
A1	0.000	0.050
A3	0.254	REF
B	0.500	0.700
D	6.200	BSC
D1	3.979	4.179
E	5.200	BSC
E1	4.087	4.287
e	1.905	BSC
F	1.860	1.880
G	0.500	0.700
H	0.379	REF
H1	0.635	REF
H2	0.507	REF
J	0.404	REF
J1	0.507	REF

GENERIC MARKING DIAGRAM*



- xxxxx = Specific Device Code
- A = Assembly Location
- Y = Year
- WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present.

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STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	PLL4, 6.2X5.2X1.85 MM PITCH	PAGE 1 OF 2

