

VIA Nano U3500 specifications

General information

Type	CPU / Microprocessor
Market segment	Mobile
Family	VIA Nano 3000 Series
Model number ?	U3500
Frequency ?	1000 MHz
Bus speed ?	800 MHz
Package	NanoBGA2
Socket	BGA400
Size	0.83" x 0.83" / 2.1cm x 2.1cm
Introduction date	November 3, 2009

Architecture / Microarchitecture

Microarchitecture	Isaiah
Manufacturing process	0.065 micron
Die	63.3mm ² (7.65mm x 8.275mm)
Data width	64 bit
The number of CPU cores	1
The number of threads	1
Floating Point Unit	Integrated
Level 1 cache size ?	64 KB instruction cache 64 KB data cache

Level 2 cache size [?](#) **1 MB 16-way set associative exclusive cache**

Features	<ul style="list-style-type: none">• MMX instructions• SSE / Streaming SIMD Extensions• SSE2 / Streaming SIMD Extensions 2• SSE3 / Streaming SIMD Extensions 3• SSSE3 / Supplemental Streaming SIMD Extensions 3• SSE4 / SSE4.1 + SSE4.2 / Streaming SIMD Extensions 4 ?• x86-64 / 64-bit extensions• Virtualization• Padlock Security Engine:<ul style="list-style-type: none">• AES encryption engine• Secure hash algorithm• NX Execute protection• Random Number Generators
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Low power features Adaptive PowerSaver technology

Integrated peripherals / components

Integrated graphics None

Electrical / Thermal parameters

Minimum power dissipation 0.1 Watt

Thermal Design Power [?](#) **5 Watt**

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