

ULTRA-SMALL, LOW-INPUT-VOLTAGE, LOW r_{ON} LOAD SWITCH WITH HYSTERESIS CONTROL INPUT

Check for Samples: [TPS22934](#)

FEATURES

- Integrated Single-Channel Load Switch
- Ultra Small CSP-4 package
0.9 mm x 0.9 mm, 0.5-mm Pitch, 0.5-mm Thick
- Input Voltage: 1.5 V to 3.6 V
- Ultra-Low ON Resistance
 - $r_{DS(ON)} = 63 \text{ m}\Omega$ at $V_{IN} = 3.6 \text{ V}$
 - $r_{DS(ON)} = 69 \text{ m}\Omega$ at $V_{IN} = 2.5 \text{ V}$
 - $r_{DS(ON)} = 78 \text{ m}\Omega$ at $V_{IN} = 1.8 \text{ V}$
 - $r_{DS(ON)} = 87 \text{ m}\Omega$ at $V_{IN} = 1.5 \text{ V}$
- 1-A Maximum Continuous Switch Current
- Integrated Hysteresis Enable Input (ON Pin)
Allows Easy Power Rail Sequencing
- Controlled Slew Rate Option: 26 μs at 3.6 V
- Quick Output Discharge Transistor
- ESD Performance Tested Per JESD 22
 - 3000-V Human-Body Model (A114-B, Class II)
 - 1000-V Charged-Device Model (C101)

APPLICATIONS

- Battery Powered Equipment
- Portable Industrial Equipment
- Portable Medical Equipment
- Portable Media Players
- Point Of Sales Terminal
- GPS Devices
- Digital Cameras
- Portable Instrumentation
- Smartphones

DESCRIPTION

The TPS22934 is a small, ultra low ON-resistance (r_{ON}) load switch with controlled turn on. The devices contain a P-channel MOSFETs that can operate over an input voltage range of 1.5 V to 3.6 V.

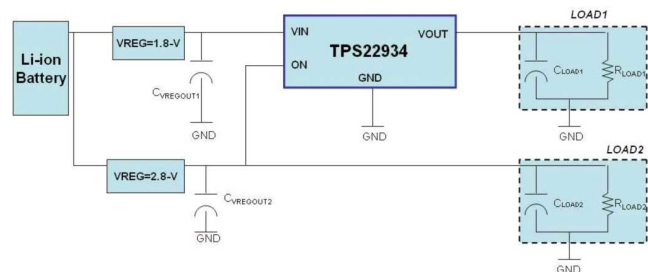
The switch is controlled by an on/off input (ON), which has built in hysteresis ($V_{TH+}(typ) = 2.35 \text{ V}$) allowing an easy use of TPS22934 in power-rail sequencing applications.

In TPS22934 a 35- Ω on-chip load resistor is added for output quick discharge when switch is turned off.

In TPS22934, the rise time of the device is internally controlled in order to avoid inrush current. TPS22934 feature a typical rise time of 26 μs with a 3.6-V input.

The TPS22934 is available in an ultra-small space-saving 4-pin CSP package and is characterized for operation over the free-air temperature range of -40°C to 85°C .

TYPICAL APPLICATION 1.8-V Power Rail Sequencing



FEATURE LIST

	r_{ON} (TYP) AT 3.6 V	SLEW RATE (TYP) AT 3.6 V	QUICK OUTPUT DISCHARGE	MAXIMUM OUTPUT CURRENT	ENABLE
TPS22934	63 m Ω	26 μs	Yes	1 A	Hysteresis Input $V_{TH+}(typ) = 2.35 \text{ V}$



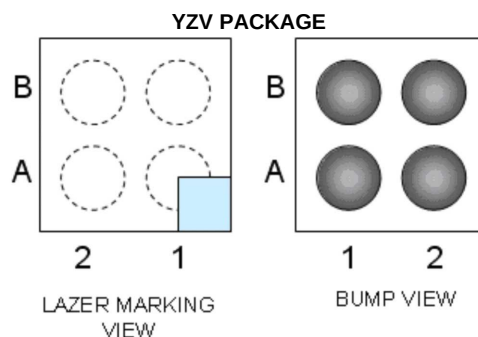
Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

ORDERING INFORMATION⁽¹⁾

T _A	PACKAGE ⁽²⁾		ORDERABLE PART NUMBER	TOP-SIDE MARKING ⁽³⁾
–40°C to 85°C	DSBGA – YZP (0.5-mm pitch)	Tape and reel	TPS22934YZVR	_ _ _ _ 6 2
		Tape and reel	TPS22934YZVT	

- (1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.
- (2) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.
- (3) The actual top-side marking has four preceding characters to denote year, month, sequence code, and the wafer fab/assembly site. Pin 1 identifier indicates solder-bump composition (1 = SnPb, ● = Pb-free).

+-----+	Y	= YEAR
	M	= MONTH
YMSN	S	= LOT SEQUENCE
62	4TH CHAR	= WAFER FAB/ASSEMBLY SITE CODE
●	LINE 2	= DEVICE NAME CODE
+-----+	●	= PIN ONE (FILLED CIRCLE)

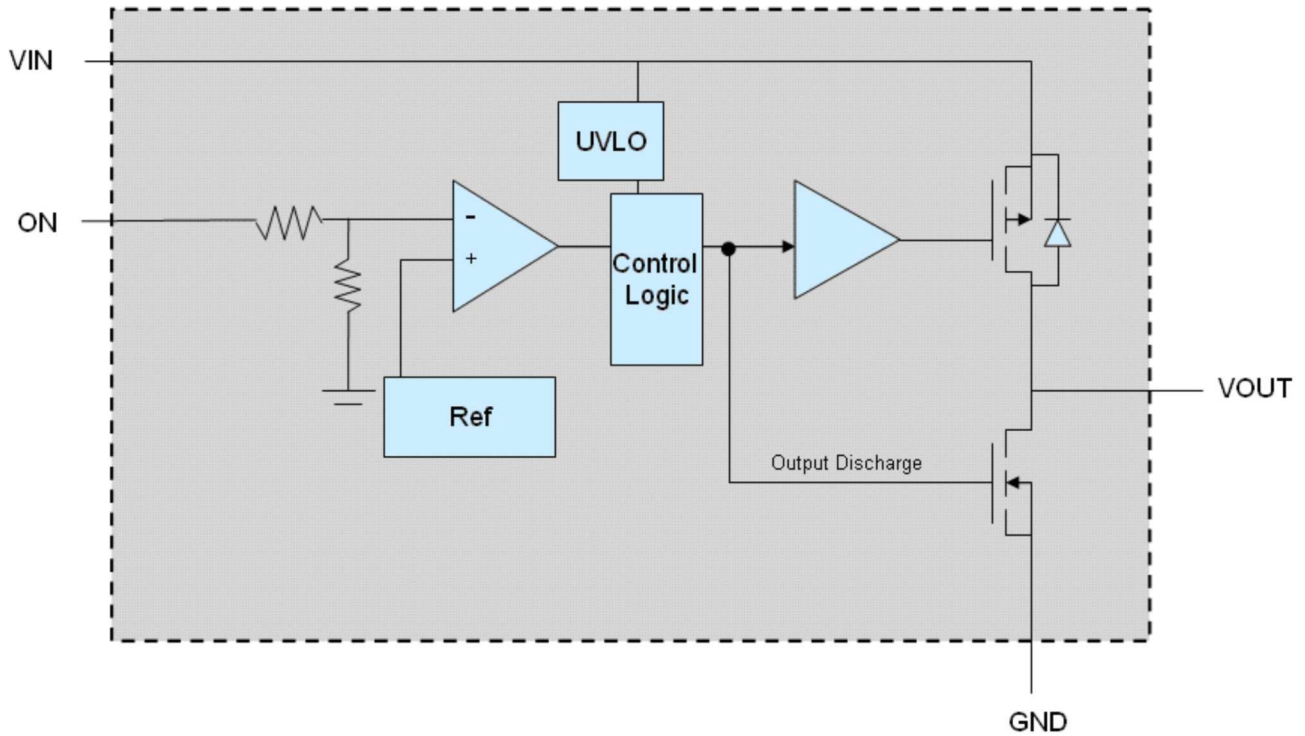
**TERMINALS ASSIGNMENTS (YZP PACKAGE)**

B	ON	GND
A	VIN	VOUT
	2	1

TERMINAL FUNCTIONS

NO.	NAME	DESCRIPTION
B1	GND	Ground
B2	ON	Switch control input, active high. Do not leave floating
A1	VOUT	Switch output
A2	VIN	Switch input, bypass this input with a ceramic capacitor to ground

BLOCK DIAGRAM



FUNCTION TABLE

ON (Control Signal)	VIN to VOUT	VOUT to GND
$V_{ON} < V_{TH-}$	OFF	ON
$V_{ON} > V_{TH+}$	ON	OFF

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

		MIN	MAX	UNIT
V _{IN}	Input voltage range	–0.3	4	V
V _{OUT}	Output voltage range		V _{IN} + 0.3	V
V _{ON}	Control input voltage range	–0.3	4	V
I _{MAX}	Maximum continuous switch current, T _A = –40°C to 85°C		1	A
I _{PLS}	Maximum pulsed switch current, 100-μs pulse, 2% duty cycle, T _A = –40°C to 85°C		1.4	A
T _A	Operating free-air temperature range	–40	85	°C
T _{stg}	Storage temperature range	–65	150	°C
ESD	Electrostatic discharge protection	Human-Body Model (HBM)		3000
		Charged-Device Model (CDM)		1000
				V

- (1) Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

DISSIPATION RATINGS

BOARD	PACKAGE	R _{θJC}	R _{θJA}	DERATING FACTOR ABOVE T _A = 25°C	T _A < 25°C	T _A = 70°C	T _A = 85°C
High-K ⁽¹⁾	YZV	28.18°C/W	120.62°C/W	–8.2904 mW/°C	829.04 mW	455.97 mW	331.61 mW

- (1) The JEDEC high-K (2s2p) board used to derive this data was a 3- × 3-inch, multilayer board with 1-ounce internal power and ground planes and 2-ounce copper traces on top and bottom of the board.

RECOMMENDED OPERATING CONDITIONS

		MIN	MAX	UNIT
V _{IN}	Input voltage	1.5	3.6	V
V _{ON}	Control input voltage	0	3.6	
V _{OUT}	Output voltage		V _{IN}	V
C _{IN}	Input capacitance	1 ⁽¹⁾		μF

- (1) See the *Input Capacitor* section in *Application Information*.

ELECTRICAL CHARACTERISTICS

 $V_{IN} = 1.5\text{ V to }3.6\text{ V}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	T_A	MIN	TYP ⁽¹⁾	MAX	UNIT
I_{IN} Quiescent current	$I_{OUT} = 0$, $V_{IN} = V_{ON} = 3.6\text{ V}$	Full		3.5	20	μA
$I_{IN(OFF)}$ OFF-state supply current	$V_{ON} = \text{GND}$, $V_{OUT} = 0$	Full		2.5	5	μA
r_{ON} ON-state resistance	$I_{OUT} = -200\text{ mA}$	$V_{IN} = 3.6\text{ V}$	25°C	63	77	$\text{m}\Omega$
			Full		80	
		$V_{IN} = 2.5\text{ V}$	25°C	69	85	
			Full		89	
		$V_{IN} = 1.8\text{ V}$	25°C	78	96	
			Full		100	
		$V_{IN} = 1.5\text{ V}$	25°C	87	107	
			Full		115	
r_{PD} Output pulldown resistance	$V_{IN} = 3.3\text{ V}$, $V_{ON} < V_{TH+}$, $I_{OUT} = 30\text{ mA}$	25°C		35	65	Ω
I_{ON} ON input bias current	$V_{ON} = 1.5\text{ V to }3.6\text{ V or GND}$	Full		0.7	1.5	μA
UVLO Undervoltage lockout	V_{IN} increasing	Full		0.8	1.05	V
	V_{IN} decreasing			0.7	0.95	
V_{TH+} Positive going ON voltage threshold	$V_{IN} = 1.5\text{ V to }3.6\text{ V}$	Full		2.1	2.35	V
V_{TH-} Negative going ON voltage threshold	$V_{IN} = 1.5\text{ V to }3.6\text{ V}$	Full		1.3	1.45	V
ΔV_{TH} Hysteresis ($V_{TH+} - V_{TH-}$)	$V_{IN} = 1.5\text{ V to }3.6\text{ V}$	Full		0.7	0.9	V

(1) Typical values are at $V_{IN} = 3.3\text{ V}$ and $T_A = 25^\circ\text{C}$.

SWITCHING CHARACTERISTICS

$V_{IN} = 3.6\text{ V}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{ON} Turn-ON time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		33		μs
t_{OFF} Turn-OFF time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		17		μs
t_r V_{OUT} rise time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		26		μs
t_f V_{OUT} fall time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		7.5		μs

SWITCHING CHARACTERISTICS

$V_{IN} = 2.5\text{ V}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{ON} Turn-ON time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		42		μs
t_{OFF} Turn-OFF time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		17		μs
t_r V_{OUT} rise time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		31		μs
t_f V_{OUT} fall time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		8		μs

SWITCHING CHARACTERISTICS

$V_{IN} = 1.8\text{ V}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{ON} Turn-ON time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		54		μs
t_{OFF} Turn-OFF time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		15		μs
t_r V_{OUT} rise time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		37		μs
t_f V_{OUT} fall time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		10		μs

SWITCHING CHARACTERISTICS

$V_{IN} = 1.5\text{ V}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{ON} Turn-ON time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		64		μs
t_{OFF} Turn-OFF time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		14		μs
t_r V_{OUT} rise time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		42		μs
t_f V_{OUT} fall time	$R_L = 500\ \Omega$, $C_L = 0.1\ \mu\text{F}$		12		μs

PARAMETER MEASUREMENT INFORMATION

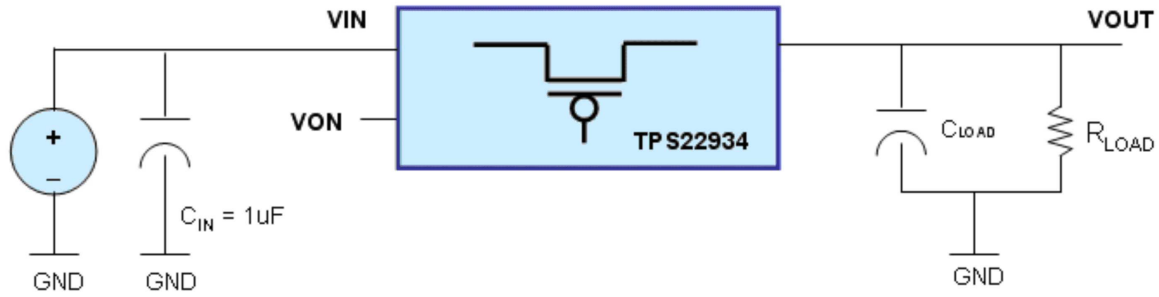


Figure 1 : Test Circuit

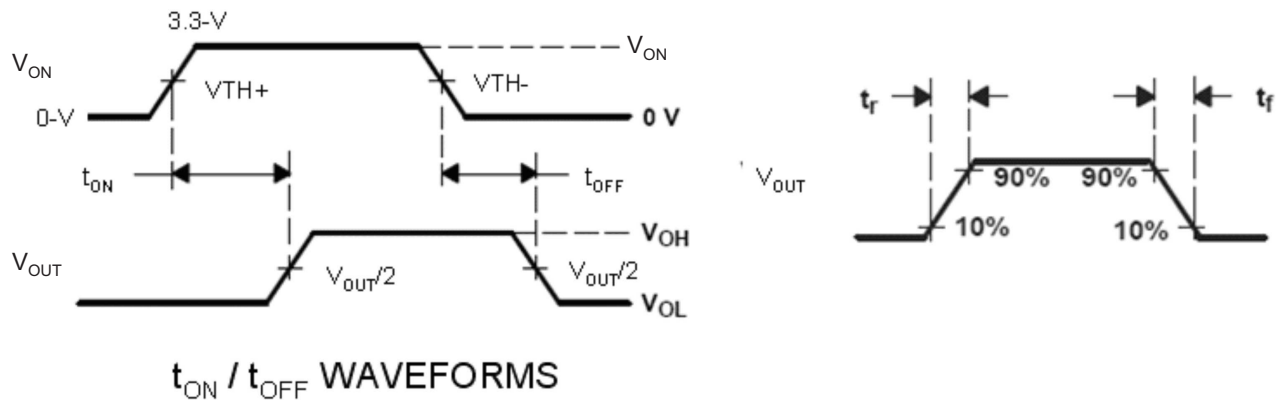
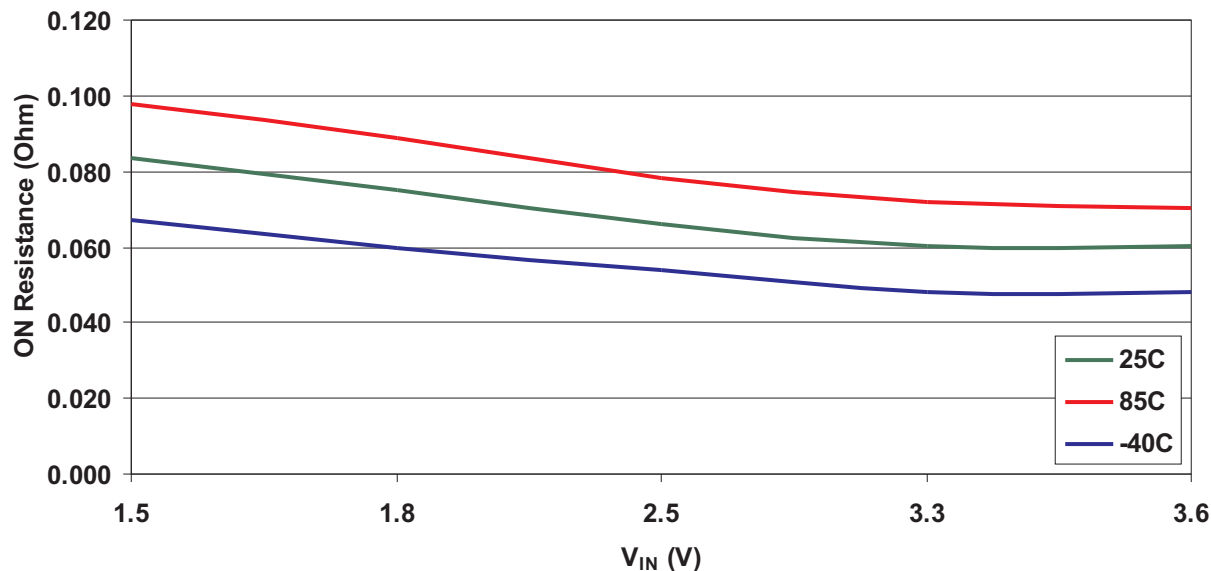


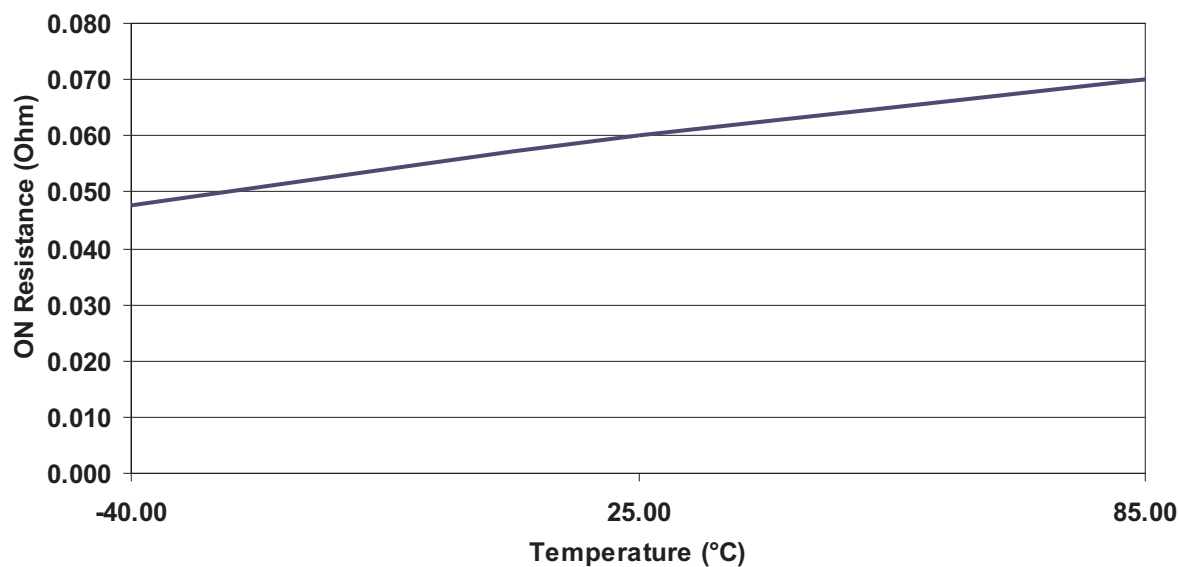
Figure 1. Test Circuit and t_{ON}/t_{OFF} Waveforms

TYPICAL CHARACTERISTICS

ON Resistance vs Input Voltage

 $I_{OUT} = -200\text{ mA}$ 

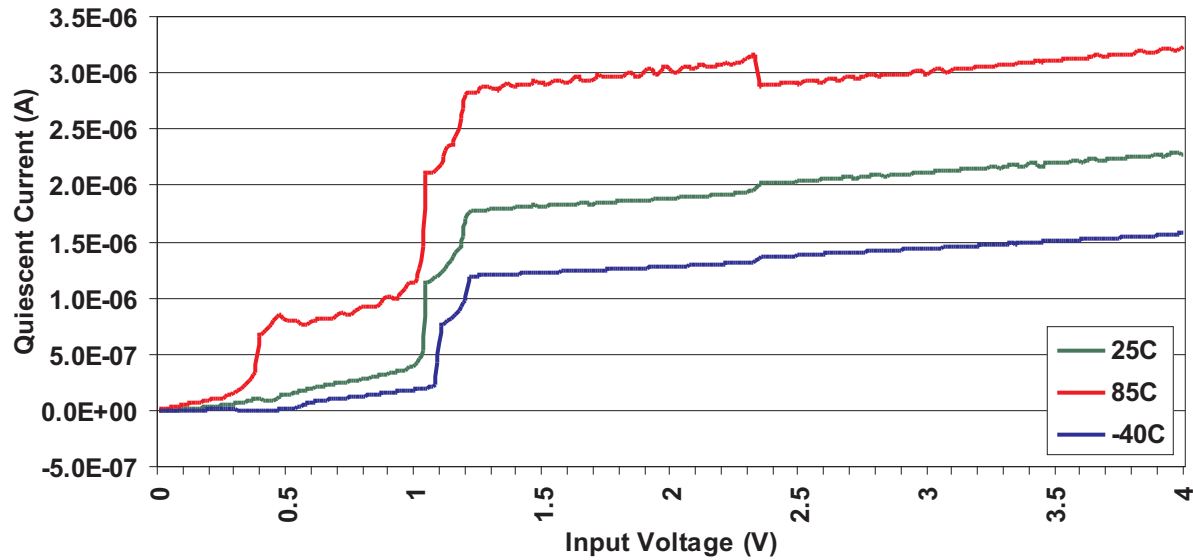
ON Resistance vs Temperature

 $V_{IN} = 3.6\text{ V}$, $I_{OUT} = -200\text{ mA}$ 

TYPICAL CHARACTERISTICS (continued)

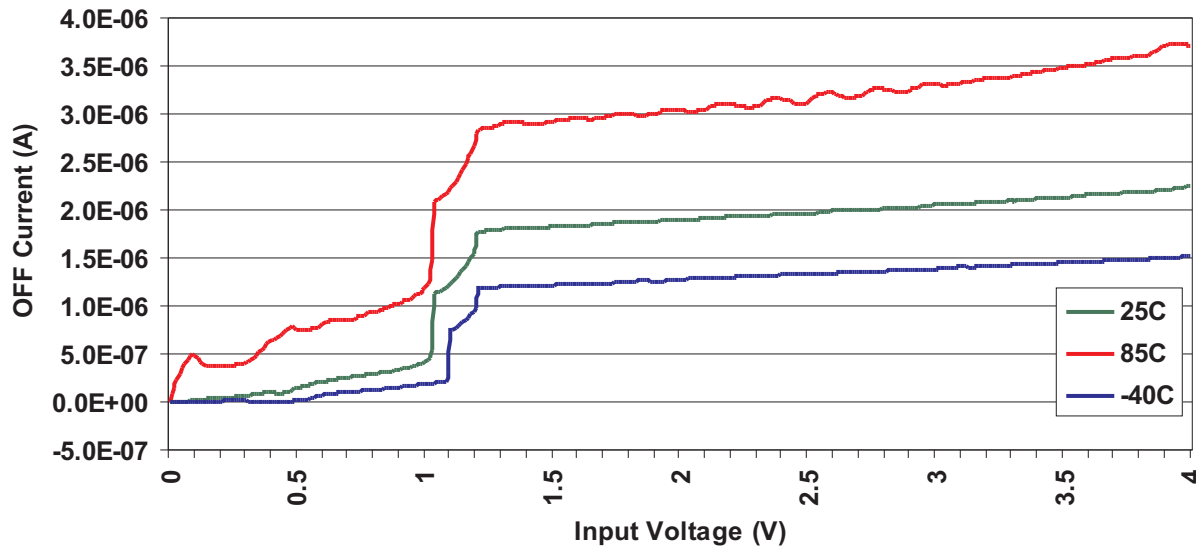
I_{IN} (Quiescent Current) vs Input Voltage

$V_{ON} = V_{IN} = 3.6\text{ V}$, $I_{OUT} = 0$



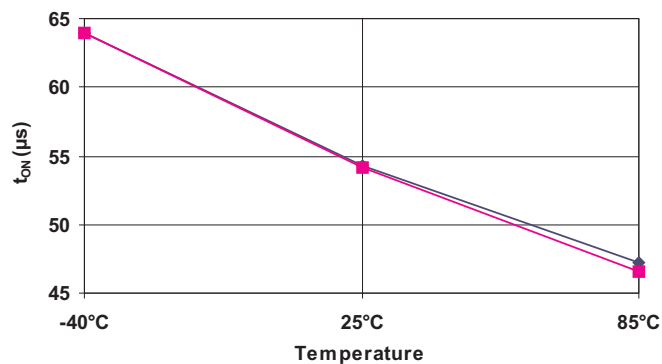
OFF Current (I_{INOFF}) vs Input Voltage

$V_{ON} = 0\text{ V}$

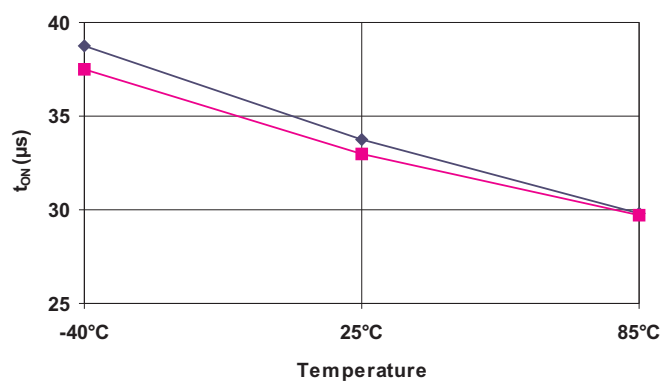


TYPICAL CHARACTERISTICS (continued)

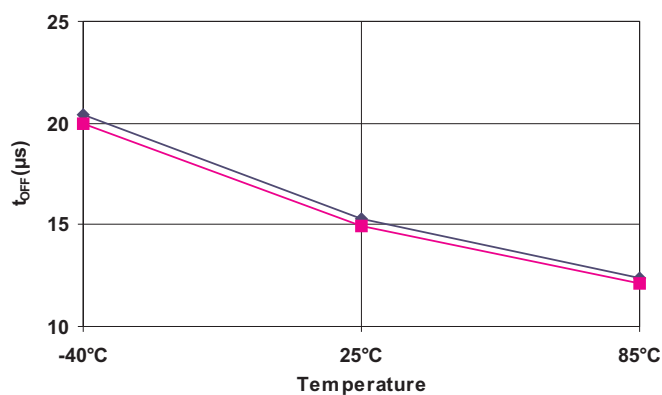
t_{ON} vs Temperature ($V_{IN} = 1.8\text{ V}$)
 $C_L = 0.1\text{ }\mu\text{F}$, $R_L = 500\text{ }\Omega$



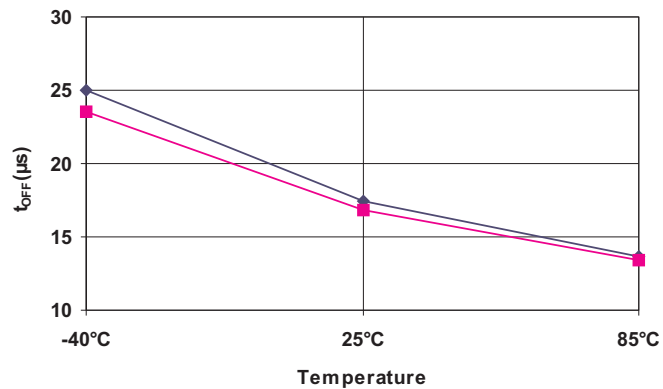
t_{ON} vs Temperature ($V_{IN} = 3.6\text{ V}$)
 $C_L = 0.1\text{ }\mu\text{F}$, $R_L = 500\text{ }\Omega$



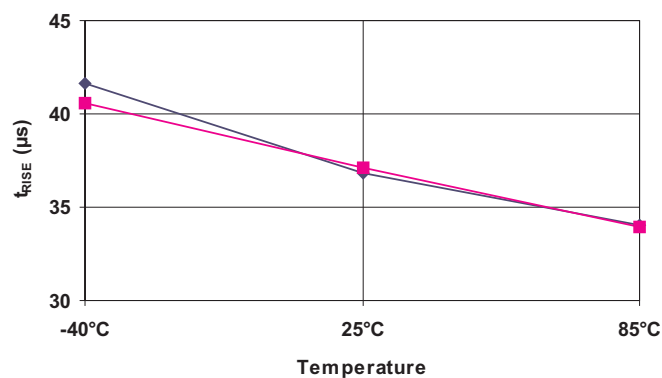
t_{OFF} vs Temperature ($V_{IN} = 1.8\text{ V}$)
 $C_L = 0.1\text{ }\mu\text{F}$, $R_L = 500\text{ }\Omega$



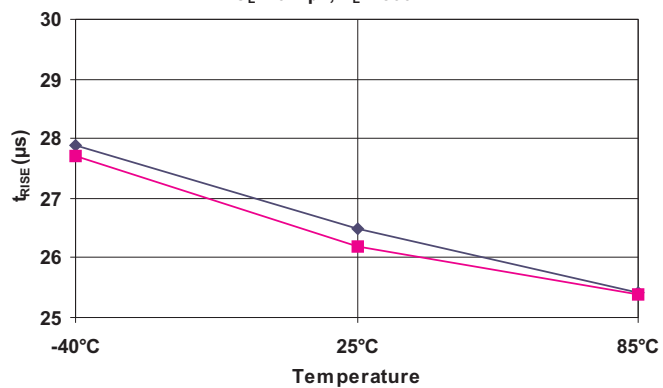
t_{OFF} vs Temperature ($V_{IN} = 3.6\text{ V}$)
 $C_L = 0.1\text{ }\mu\text{F}$, $R_L = 500\text{ }\Omega$



t_{RISE} vs Temperature ($V_{IN} = 1.8\text{ V}$)
 $C_L = 0.1\text{ }\mu\text{F}$, $R_L = 500\text{ }\Omega$

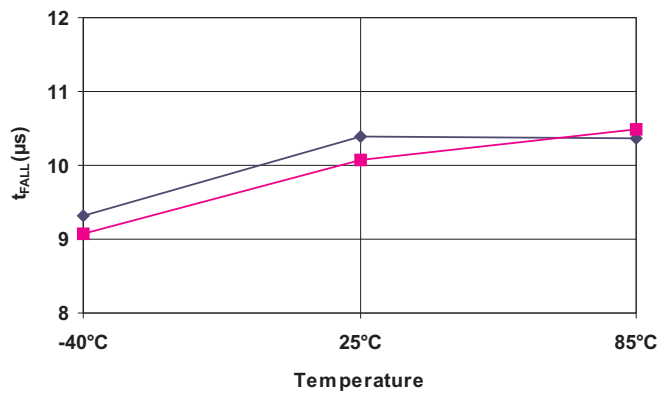


t_{RISE} vs Temperature ($V_{IN} = 3.6\text{ V}$)
 $C_L = 0.1\text{ }\mu\text{F}$, $R_L = 500\text{ }\Omega$

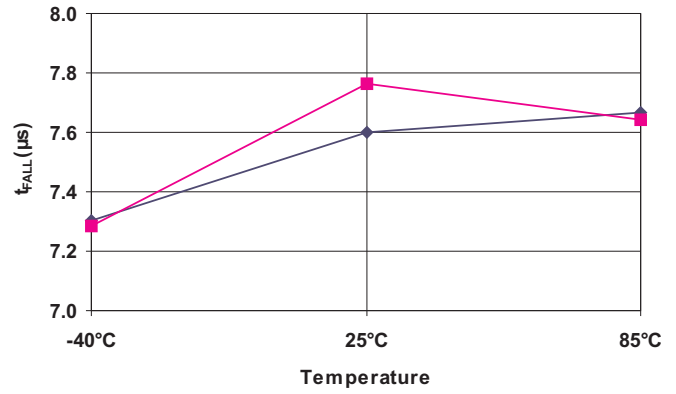


TYPICAL CHARACTERISTICS (continued)

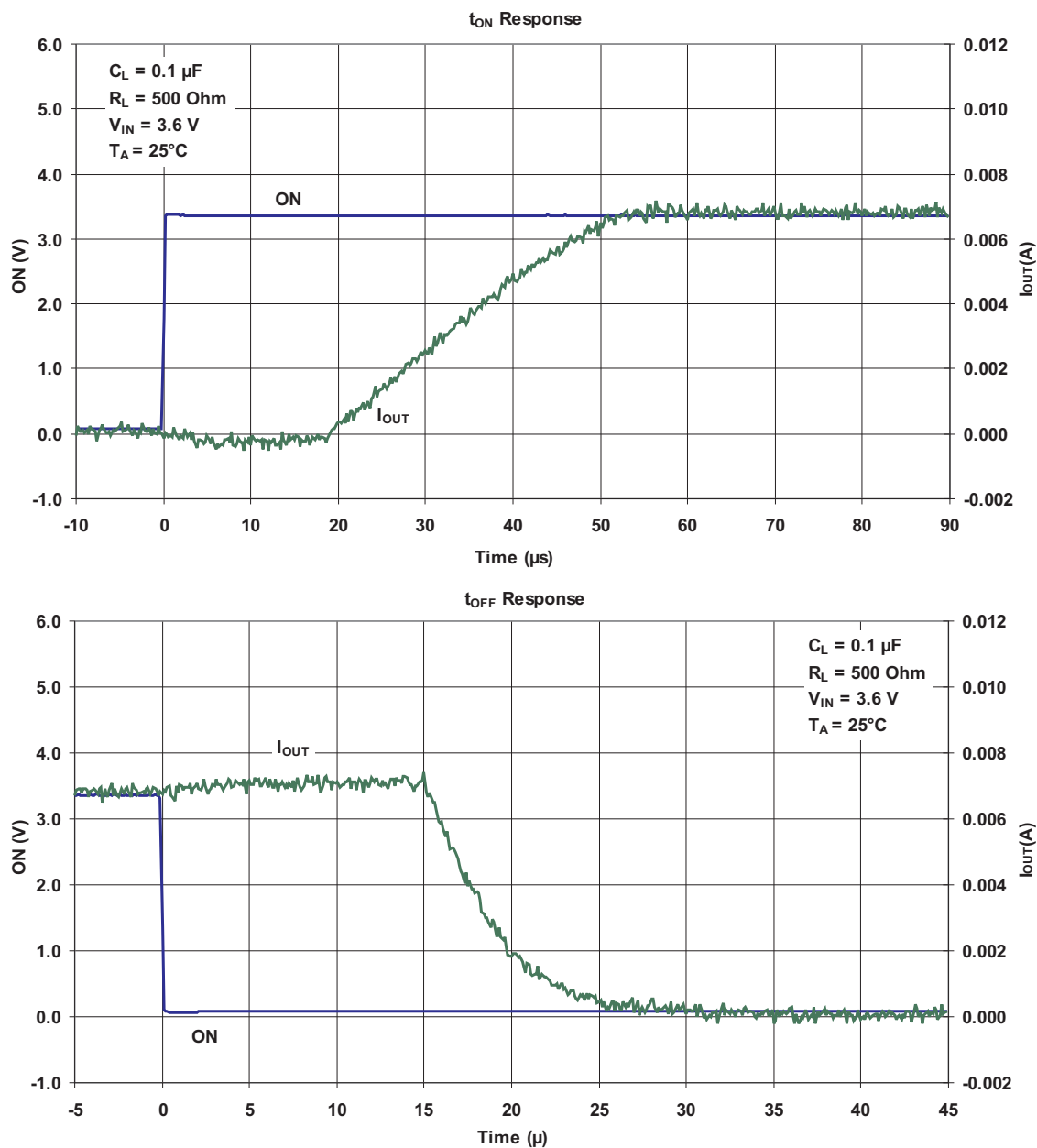
t_{FALL} vs Temperature ($V_{\text{IN}} = 1.8 \text{ V}$)
 $C_{\text{L}} = 0.1 \mu\text{F}$, $R_{\text{L}} = 500 \Omega$



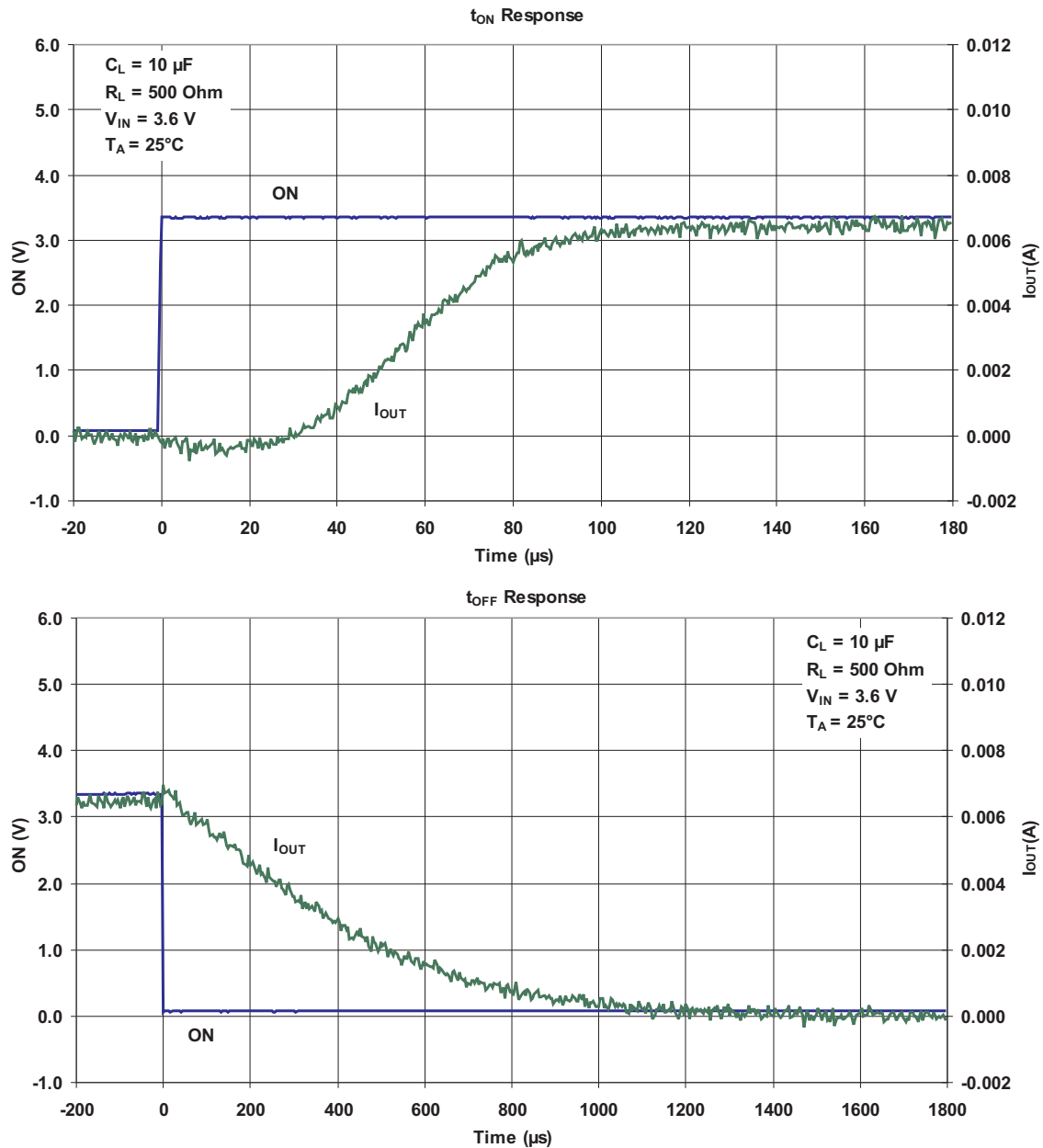
t_{FALL} vs Temperature ($V_{\text{IN}} = 3.6 \text{ V}$)
 $C_{\text{L}} = 0.1 \mu\text{F}$, $R_{\text{L}} = 500 \Omega$



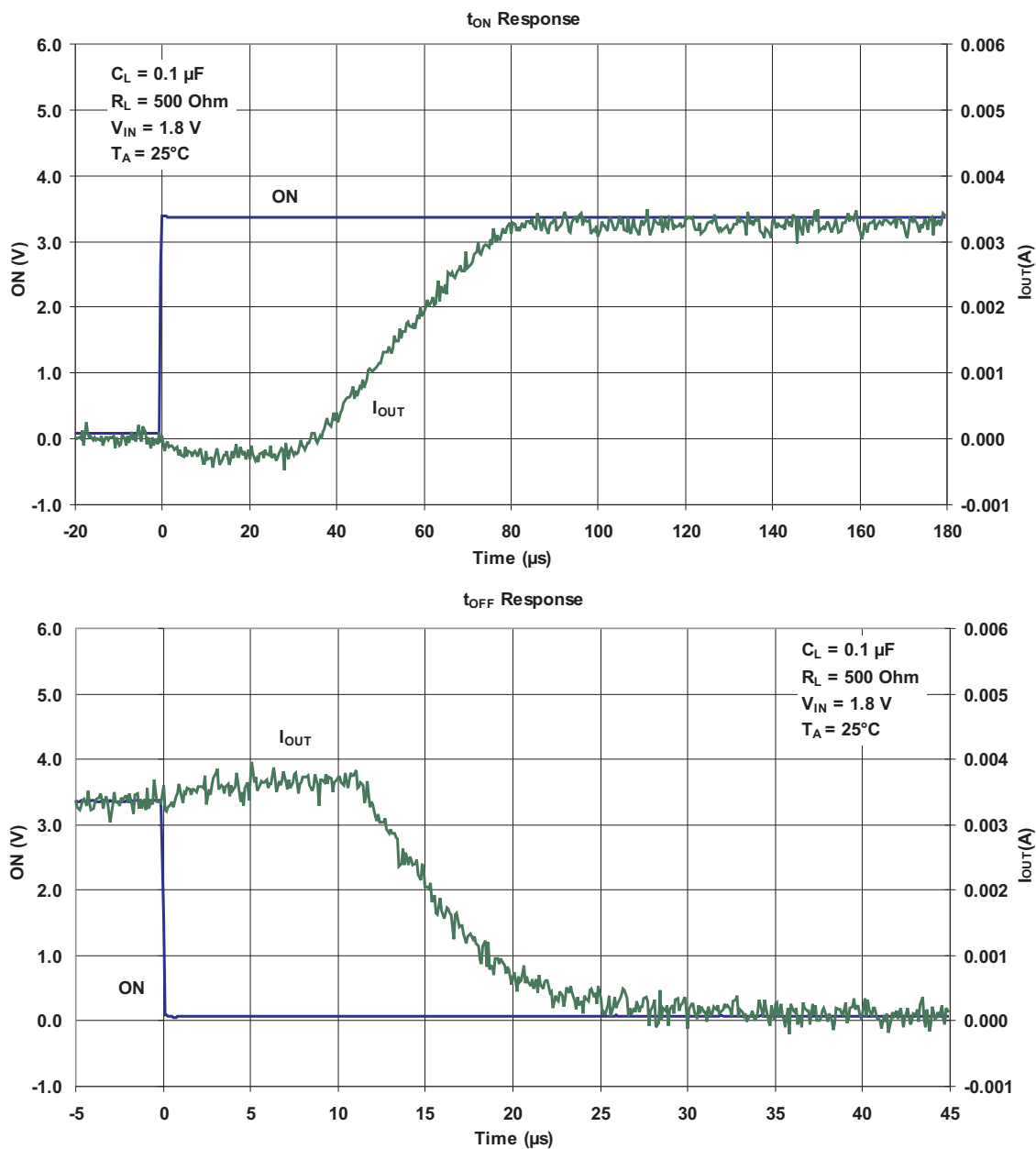
TYPICAL CHARACTERISTICS (continued)



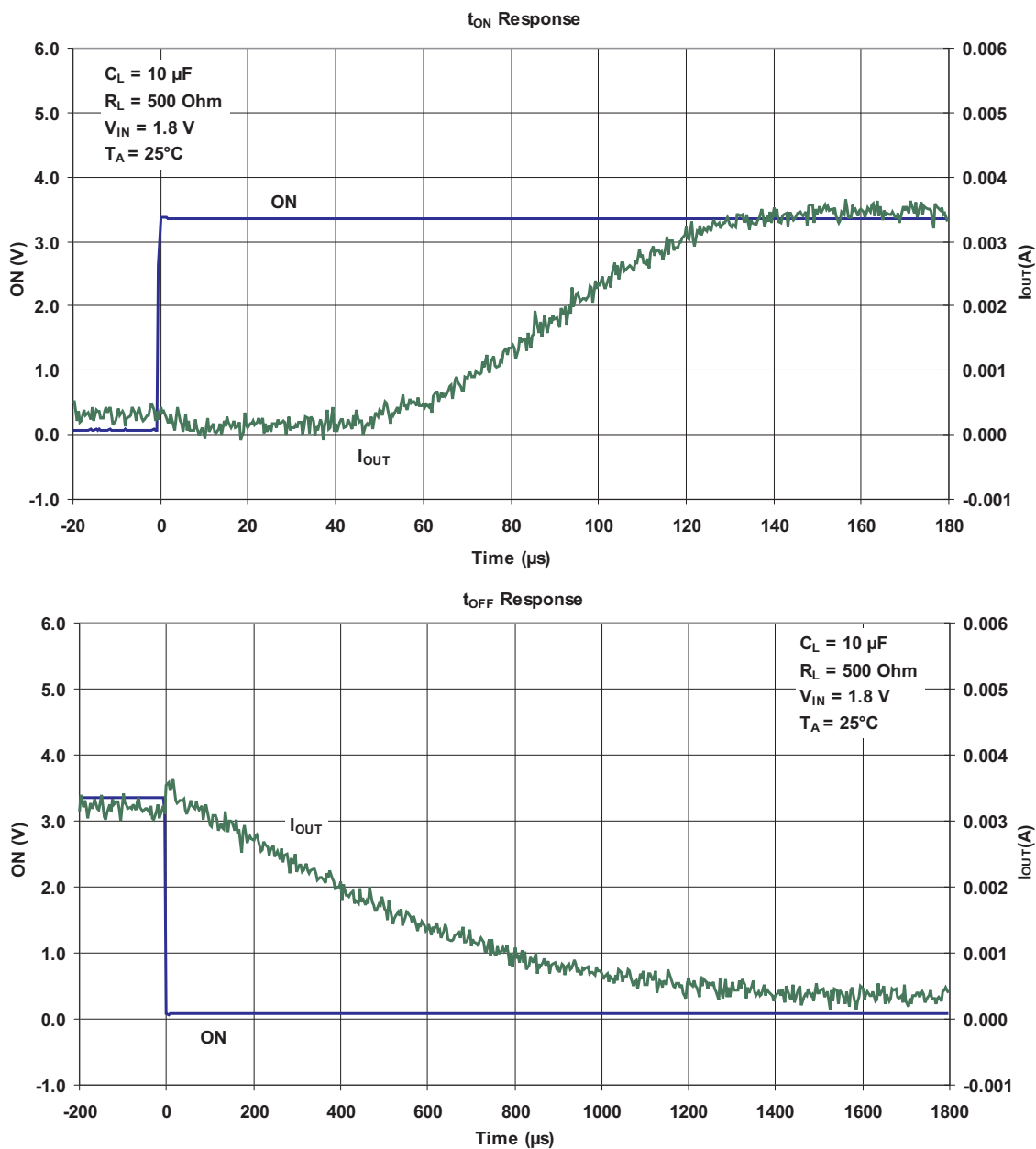
TYPICAL CHARACTERISTICS (continued)



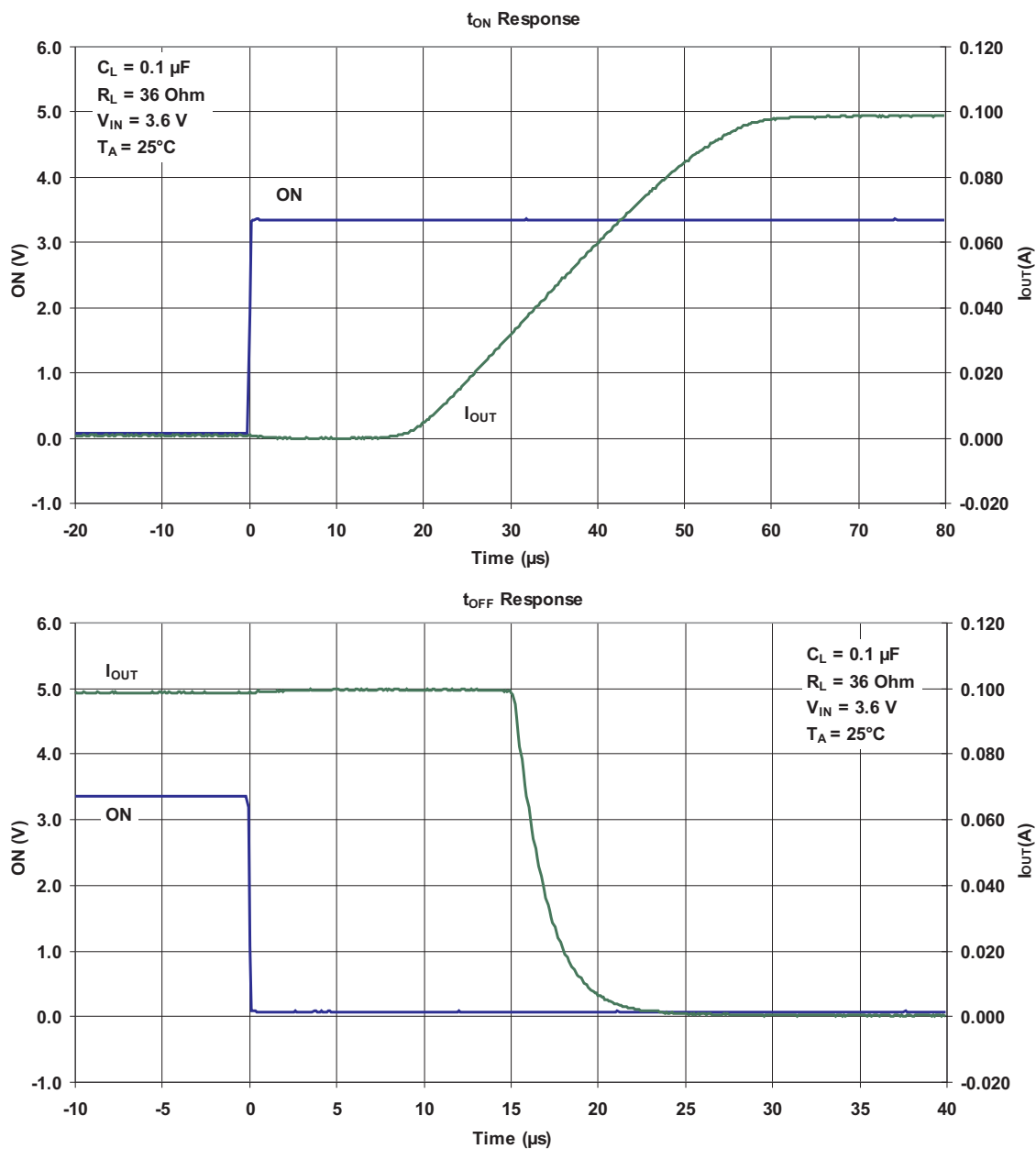
TYPICAL CHARACTERISTICS (continued)



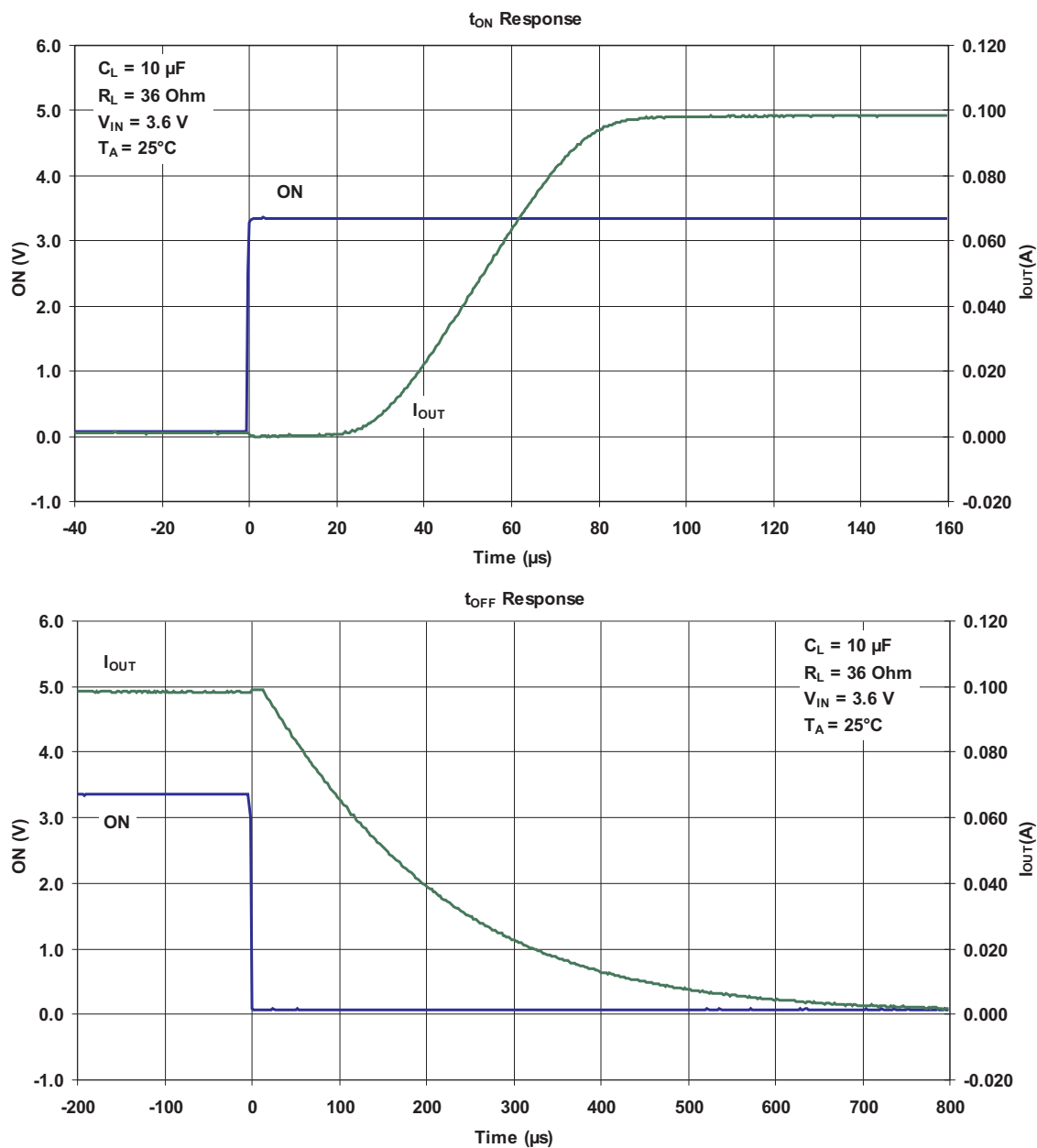
TYPICAL CHARACTERISTICS (continued)



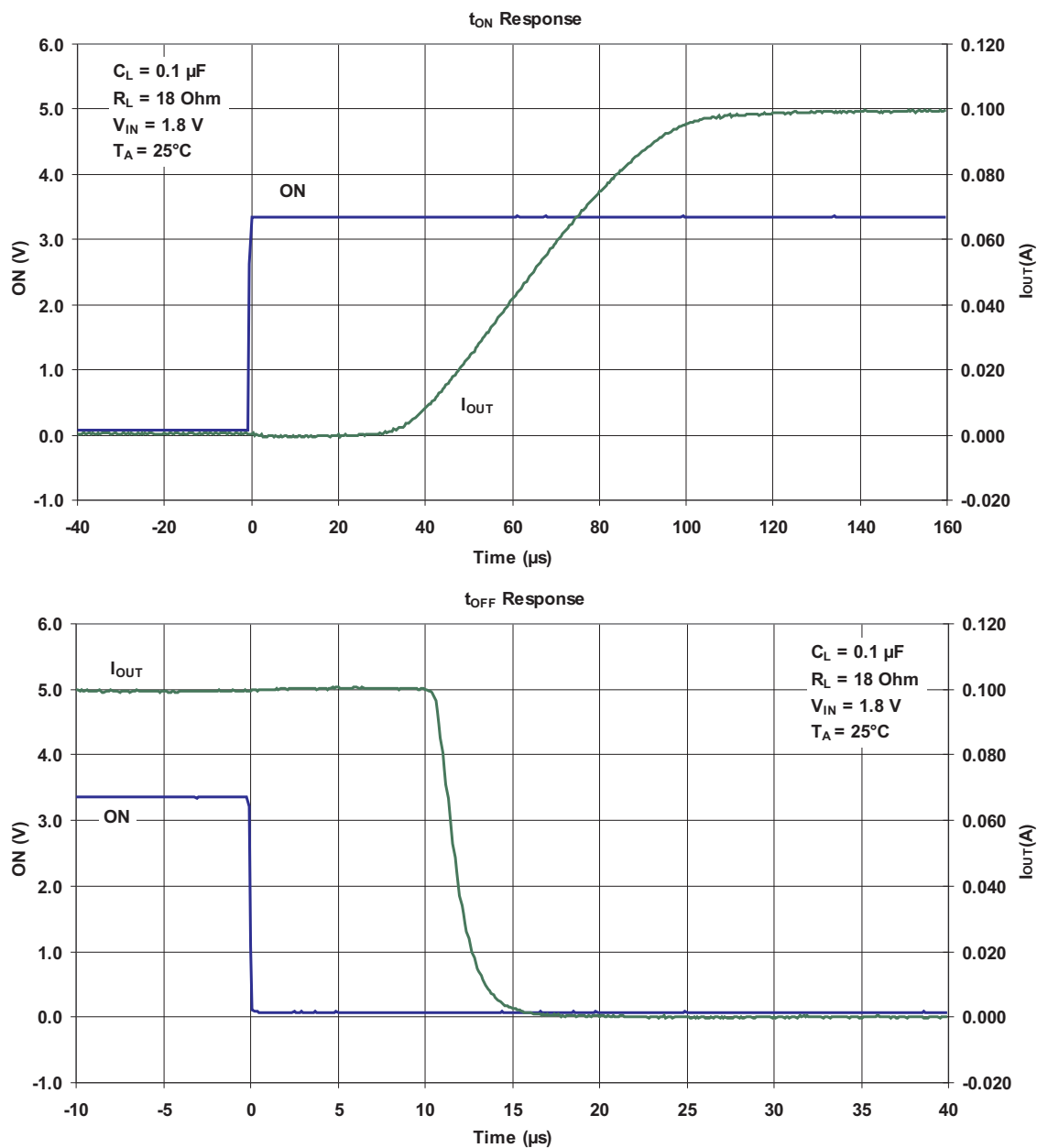
TYPICAL CHARACTERISTICS (continued)



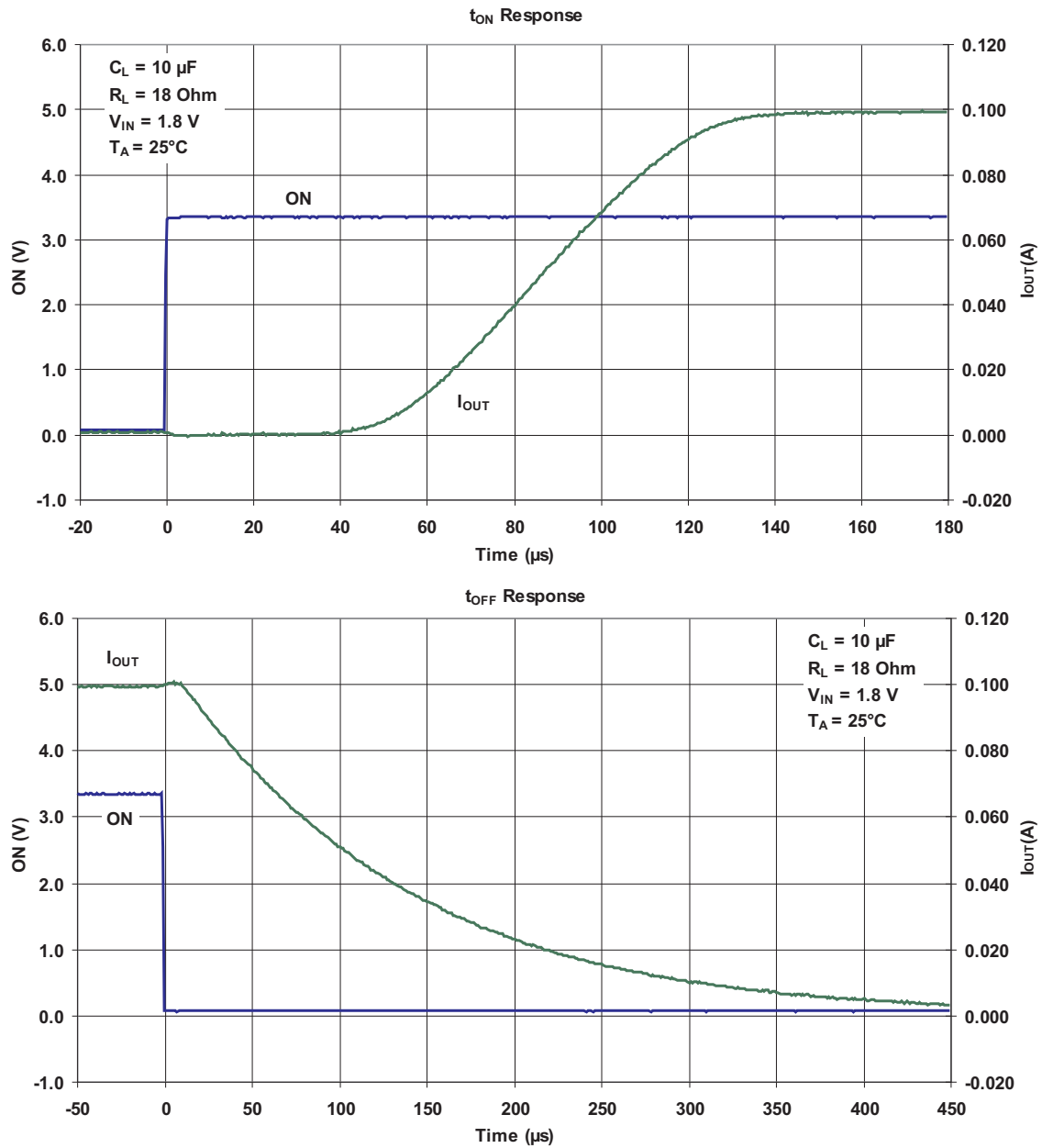
TYPICAL CHARACTERISTICS (continued)



TYPICAL CHARACTERISTICS (continued)



TYPICAL CHARACTERISTICS (continued)



APPLICATION INFORMATION

ON/OFF Control

The ON pin controls the state of the switch. The TPS22934 has built-in hysteresis on its control inputs. The load switch is active when the ON voltage is greater than the positive going voltage threshold (V_{TH+}). If the ON voltage is lower than the negative going voltage threshold (V_{TH-}), then the pass FET is deactivated and the active pulldown from VOUT to GND is activated.

This is ideal for power rail sequencing applications as shown in Figure 2 where the 2.8-V supply needs to be valid before the 1.8-V supply turn on:

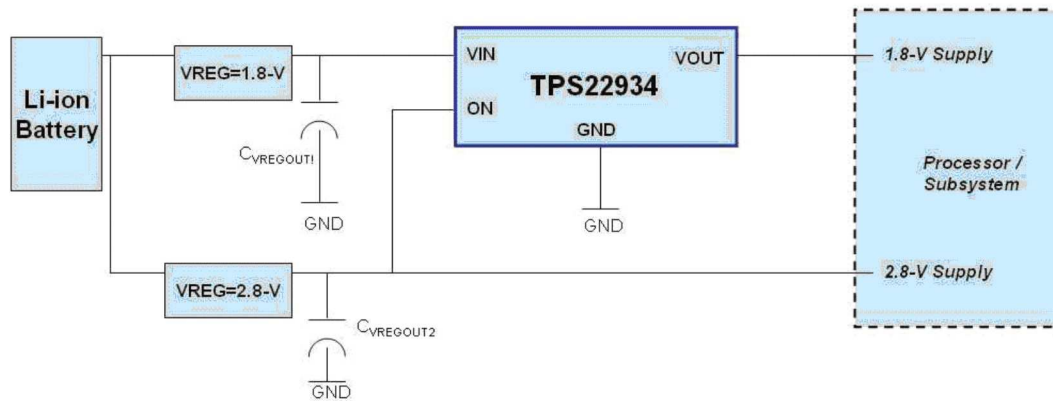


Figure 2. 1.8-V / 2.8-V Power Rail Sequencing

Input Capacitor

To limit the voltage drop on the input supply caused by transient in-rush currents when the switch turns on into a discharged load capacitor or short-circuit, a capacitor needs to be placed between VIN and GND. A 1- μ F ceramic capacitor, C_{IN} , placed close to the pins is usually sufficient. Higher values of C_{IN} can be used to further reduce the voltage drop.

Output Capacitor

Due to the integral body diode in the PMOS switch, a C_{IN} greater than C_L is highly recommended. A C_L greater than C_{IN} can cause V_{OUT} to exceed V_{IN} when the system supply is removed. This could result in current flow through the body diode from V_{OUT} to V_{IN} .

Board Layout

For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effects that parasitic trace inductances may have on normal and short-circuit operation. Using wide traces for V_{IN} , V_{OUT} , and GND helps minimize the parasitic electrical effects along with minimizing the case to ambient thermal impedance.

Undervoltage Lockout

The undervoltage lockout turns off the switch if the input voltage drops below the under-voltage lockout threshold. With the ON pin active, the input voltage rising above the undervoltage lockout threshold causes a controlled turn-on of the switch, which limits current overshoots.

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
TPS22934YZVR	ACTIVE	DSBGA	YZV	4	3000	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	Purchase Samples
TPS22934YZVT	ACTIVE	DSBGA	YZV	4	250	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	Purchase Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBsolete: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

TAPE AND REEL INFORMATION


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS22934YZVR	DSBGA	YZV	4	3000	178.0	9.2	1.0	1.0	0.63	4.0	8.0	Q1
TPS22934YZVT	DSBGA	YZV	4	250	178.0	9.2	1.0	1.0	0.63	4.0	8.0	Q1

TAPE AND REEL BOX DIMENSIONS

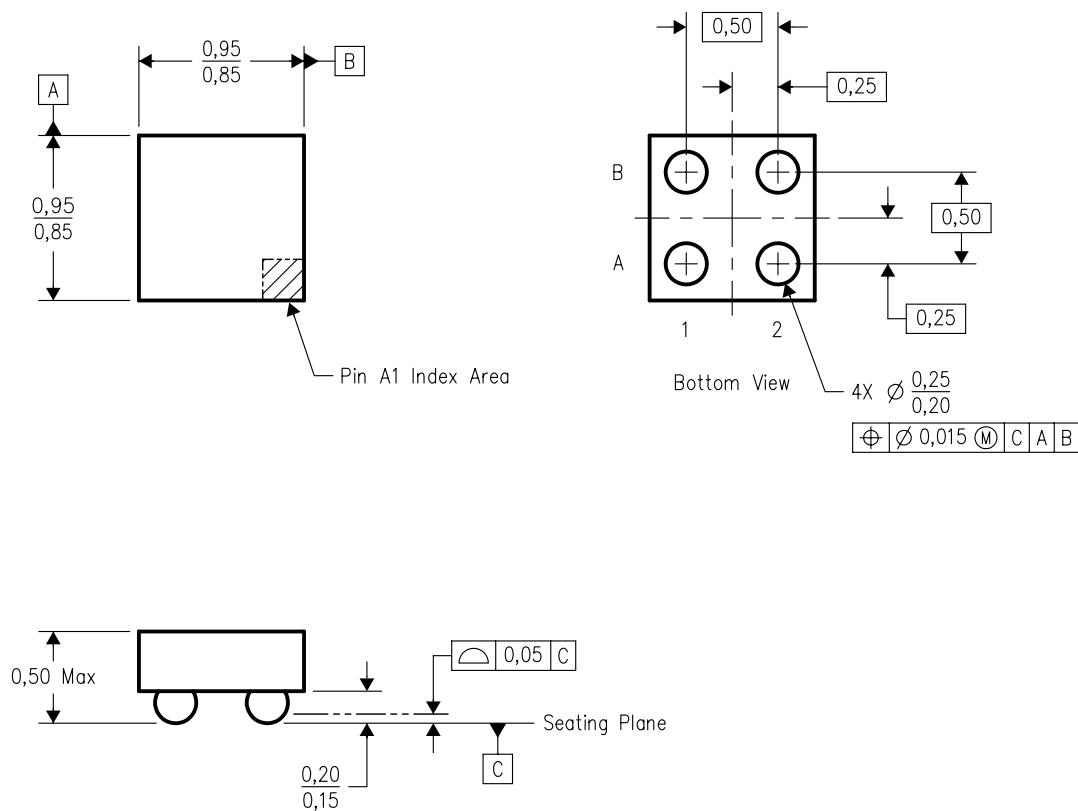


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS22934YZVR	DSBGA	YZV	4	3000	220.0	220.0	35.0
TPS22934YZVT	DSBGA	YZV	4	250	220.0	220.0	35.0

YZV (S-XBGA-N4)

DIE-SIZE BALL GRID ARRAY



4206083/A 05/2004

- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. NanoFree™ package configuration.
 - D. This package contains lead-free balls. Refer to the 4 YEV package (drawing 4206082) for tin-lead (SnPb) balls.

NanoFree is a trademark of Texas Instruments.

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have **not** been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components which meet ISO/TS16949 requirements, mainly for automotive use. Components which have not been so designated are neither designed nor intended for automotive use; and TI will not be responsible for any failure of such components to meet such requirements.

Products

Audio	www.ti.com/audio
Amplifiers	amplifier.ti.com
Data Converters	dataconverter.ti.com
DLP® Products	www.dlp.com
DSP	dsp.ti.com
Clocks and Timers	www.ti.com/clocks
Interface	interface.ti.com
Logic	logic.ti.com
Power Mgmt	power.ti.com
Microcontrollers	microcontroller.ti.com
RFID	www.ti-rfid.com
OMAP Applications Processors	www.ti.com/omap
Wireless Connectivity	www.ti.com/wirelessconnectivity

Applications

Automotive and Transportation	www.ti.com/automotive
Communications and Telecom	www.ti.com/communications
Computers and Peripherals	www.ti.com/computers
Consumer Electronics	www.ti.com/consumer-apps
Energy and Lighting	www.ti.com/energy
Industrial	www.ti.com/industrial
Medical	www.ti.com/medical
Security	www.ti.com/security
Space, Avionics and Defense	www.ti.com/space-avionics-defense
Video and Imaging	www.ti.com/video

TI E2E Community

e2e.ti.com