TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

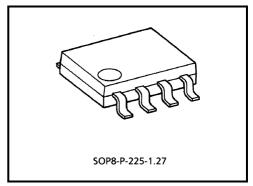
TD7101F

ELC Prescaler For Digital Synthesized Tuner

TD7101F is a 2 modulus prescaler developed for low operating voltage digital synthesized tuner, and can operate up to 150MHz.

Features

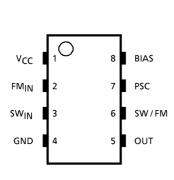
- Operating frequency range is 1.5~35MHz / 50~150MHz.
- 2 modulus prescaler: $N = 4 \times 15 / 16$ and N = 15 / 16
- Input voltage sensitivity is V_{IN} (FM) = 35mV_{rms}, V_{IN} (SW) = 40mV_{rms}
- 3V low operating supply voltage.
- The package is SOP-8 pins.

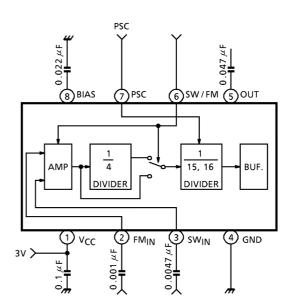


Weight: 0.08g (typ.)

Pin Connection

Block Diagram





(Note) This device is vulnerable to surge voltage.Take it into account when using this device in your system.

Pin Function

Pin No.	Symbol	Function	Remarks
1	V _{CC}	Power supply terminal.	_
2	FMIN	Signal input terminal from FM local oscillator.	_
3	SWIN	Signal input terminal from SW local oscillator.	_
4	GND	Ground terminal.	_
5	Out	Divider signal output terminal.	_
6	SW / FM	Dividing mode control terminal. "H" level input: SW _{IN} is selected, direct mode. "L" level input: FM _{IN} is selected, 1 / 4 mode.	_
7	PSC	2 modulus mode control terminal. "H" level input: 1 / 16 dividing "L" level input: 1 / 15 dividing	_
8	Bias	Bias capacitor terminal. Bias capacitor is connected.	_

Maximum Ratings (Ta = 25°C)

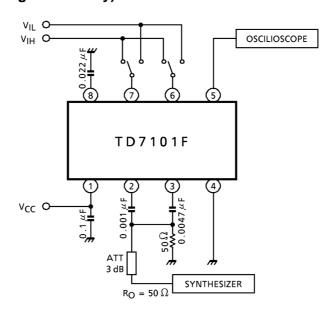
Characteristic	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	6.5	V	
Power dissipation	PD	200	mW	
Input voltage	V _{IN}	-0.3~V _{CC} + 0.3	V	
Operating temperature	T _{opr}	-10~60	°C	
Storage temperature	T _{stg}	-55~150	°C	

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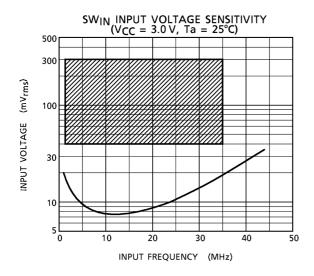
Electrical Characteristics (unless otherwise specified, V_{CC} = 1.8~5.5V, Ta = -10~60°C, f_{in} (FM) = 50~150MHz, f_{in} (SW) = 1.5~35MHz)

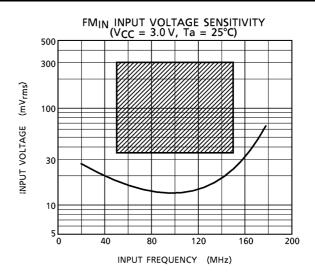
Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.	Max.	Unit
Supply voltage		V _{CC}	_	_	1.8	3.0	5.5	V
Supply cuurent		Icc	_	V _{CC} = 3.0V	_	5.5	9.5	mA
Operating frequency range		f _{IN 1}	_	FMIN	50	_	150	- MHz
		f _{IN 2}	_	SW _{IN}	1.5	_	35	
Input voltage range		V _{IN 1}	_	FM _{IN}	35	_	300	mV _{rms}
		V _{IN 2}	_	SW _{IN}	40	_	300	
Output amplitude		V _{OUT}	_	_	0.5	_	_	V_{p-p}
Input	"H" level	V _{IH}	_	PSC, SW / FM	1.6	_	V _{CC}	V
voltage	"L" level	V _{IL}	_	PSC, SW / FM	0	_	1.0	
Input current	"H level	IIH	_	PSC, SW / FM, V _{CC} = 5.0V, V _{IH} = 4.0V	_	_	60	- μΑ
	"L" level	I _{IL}	_	PSC, SW / FM, V _{CC} = 5.0V, V _{IL} = 1.0V	_	_	10	

Test Circuit (input voltage sensitivity)



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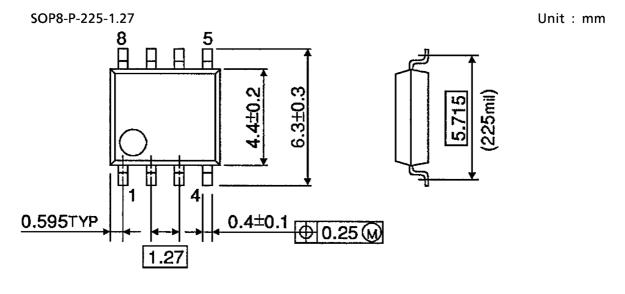




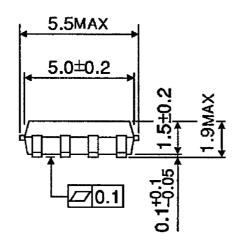
(Note)

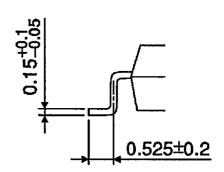
Operating range ($V_{CC} = 1.8 \sim 5.5 \text{ V}$, Ta = $-10 \sim 60 ^{\circ}\text{C}$)

Package Dimensions



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Weight: 0.08g (typ.)

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