TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# **TD62915F**

#### Intelligent Stepping Motor Driver

The TD62915F is a stepping motor driver with a current capacity of  $1.5~\rm A.$  Inputs INA and INB are combined to control the four outputs.

Since the inputs are TTL-compatible, this IC can be controlled directly from a CPU or other control system.

The IC also incorporates various protective functions as well as a self-diagnostic function for diagnostic function for diagnostic output.

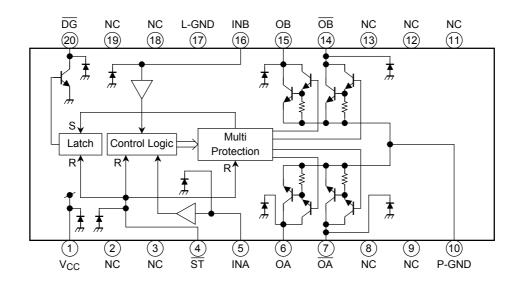
# HSOP20-P-450-1.00

Weight: 0.79 g (typ.)

#### **Features**

- Output current capacity: 1.5 A (max)
- Low standby current: 0.1 mA (max)
- Built-in protective functions: Short-circuit protection (latch) / Thermal shutdown
- Self-diagnostic output: On short-circuit detection
- Separate GND for output and logic control sections
- Package: 20-pin HSOP

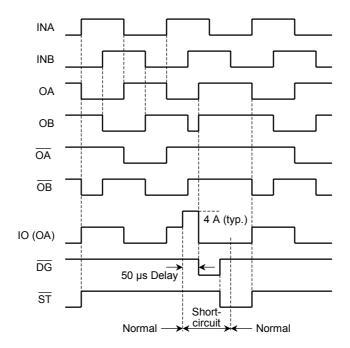
#### **Block Diagram and Pin Assignment**



## Pin Description

Pin No.	Symbol	Description
1	V <sub>CC</sub>	Power supply pin.
4	ST	When this pin is opened or grounded, the output turns off, thus reducing the current consumption to 100 $\mu A$ or less. If standby mode is not needed, the pin is connected to $V_{CC}$ .
5	INA	This is input terminal which controls output condition of pin 6 and pin 7. PNP-type voltage comparator is built in.
6	OA	PNP-type complementary output pin with a current capacity of 1.5 A. This pin is controlled by the input from pin 5. When the output is supplied with a current exceeding the detection current (4 A typ.) because of load shortcircuit, the output is latched to the OFF state after a 50 µs (typ.) delay in order to protect the IC.
7	ŌĀ	Output pin of the inversion of pin 6. This terminal has the same function as pin 6 and is controlled by pin 5.
10	P-GND	Ground terminal of output section which is usually connected with pin 17.
2, 3, 8, 9, 11, 12, 13, 18, 19	NC	Not connected. (electrically, this pin is completely open.)
14	ОВ	Output pin of the inversion of pin 15. This terminal has the same function as pin 6 and is controlled by pin 16.
15	ОВ	This terminal has the same function as pin 6 and is controlled by pin 16.
16	INB	This is input terminal which controls output condition of pin 14 and pin 15. PNP-type voltage comparator is built in.
17	L-GND	Ground terminal of logic control section which is usually connected with pin 10.
20	DG	Self-diagnostic output pin. This signal goes low when the output is short-circuited while the input is on (high). The output will be latched after a 50 µs (typ.) delay when the load is short-circuited. This pin supplies an NPN open-collector output.

## **Timing Chart**



### **Truth Table Input/Output**

Input			Output					
INA	INB	ST	OA	ŌĀ	ОВ	OB	DG	
L	L	Н	OFF	ON	OFF	ON	OFF	
L	Н	Н	OFF	ON	ON	OFF	OFF	
Н	L	Н	ON	OFF	OFF	ON	OFF	
Н	Н	Н	ON	OFF	ON	OFF	OFF	
_	_	L	OFF	OFF	OFF	OFF	OFF	
_	_	OPEN	OFF	OFF	OFF	OFF	OFF	

#### **Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	7~30	V
Input voltage	V <sub>IN</sub>	-0.3~7	V
Output voltage	V <sub>CC</sub>	-0.3~V <sub>CC</sub>	٧
Output current	IO	1.5 (Note 1)	Α
Power dissipation	P <sub>D</sub>	2.0 (Note 2)	W
Operation temperature	T <sub>opr</sub>	-40~85	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C
Lead temperature mounted on PCB	T <sub>sol</sub>	260 (10 s)	°C

Note 1: Maximum current value when using infinite heatsink

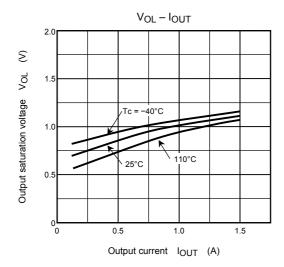
Note 2: When mounted on PCB (50 mm × 50 mm × 1.6 mm, copper foil area: 50%)

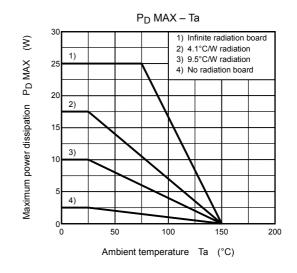
### **HSOP20-P-450 Thermal Resistance Data (Ta = 25°C)**

Characteristics	Test Condition	Rating	Unit
R <sub>θj-a</sub>	-	125	°C/W
R <sub>θj-c</sub>	-	13	°C/W
P <sub>D1</sub>	When using infinite heatsink	9.6	W
P <sub>D2</sub>	When mounted on iron sheet (50 mm × 50 mm × 1.6 mm)	3.2	W
P <sub>D3</sub>	When mounted on PCB (50 mm × 50 mm × 1.6 mm, copper foil area: 50%)	2.0	W
P <sub>D4</sub>	Without a board	1.0	W

# Electrical Characteristics (V<sub>CC</sub> = 24 V, Ta = 25°C)

Characteristics	Symbol	Pin	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Current consumption	Icc	$V_{CC}$	_	_	12	25	40	mA
Input voltage	V <sub>IL</sub>	INA/INB	_	_	_	_	8.0	V
input voitage	V <sub>IH</sub>	IINA/IIND		_	2.4	_	_	
Input current	I <sub>IL</sub>	INA/INB	_	V <sub>IN</sub> = 0.4 V	-50	_	_	μΑ
input current	l <sub>IH</sub>	INA/INB		V <sub>IN</sub> = 5 V	_	_	10	
Input voltage	V <sub>IL</sub>	ST		_	_	_	0.8	V
input voitage	V <sub>IH</sub>	31	_	_	3.0	_	_	
Output saturation voltage	V <sub>SAT</sub>	OA, OA OB, OB	_	I <sub>O</sub> = 1.5 A, Tc = 25°C	_	1.25	1.5	V
Output leakage current	I <sub>LEAK</sub>	OA, <u>OA</u> OB, OB	_	V <sub>O</sub> = V <sub>CC</sub>	_	_	±10	μΑ
Output voltage	V <sub>OL</sub>	DG	_	I <sub>OL</sub> = 3 mA	_	_	0.3	V
Output leakage current	I <sub>LEAK</sub>	DG	_	V <sub>O</sub> = V <sub>CC</sub>	_	_	10	μΑ
Overcurrent detection	I <sub>SD</sub>	_	_	_	1.8	4	6	Α
Chutdown tomporature	T <sub>SD-H</sub>	_	_	OUT = ON → OFF	_	160	_	°C
Shutdown temperature	T <sub>SD-L</sub>	_		OUT = OFF → ON	_	130	_	
Standby current	I <sub>ST</sub>	V <sub>CC</sub> + V <sub>S</sub>	_	ST = GND	_	_	100	μΑ
Propagation delay time	t <sub>pLH</sub>	_		_	_	1	10	
Fropagation delay time	t <sub>pHL</sub>	_		_	_	1	10	μs

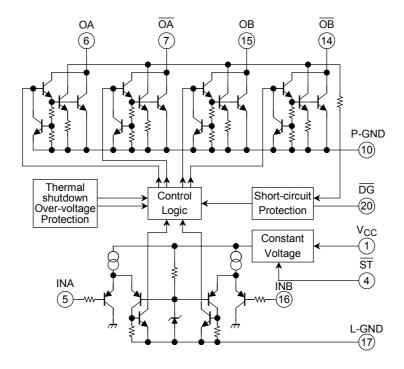




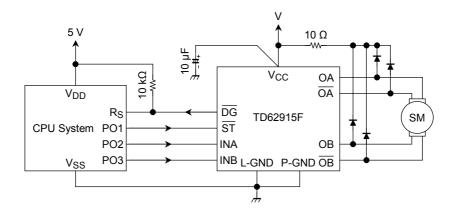
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#### **Equivalent Circuit**



#### **Application Circuit**



#### **Precautions when Using the Device in Application**

The IC may degrade or break down due to overvoltage or overcurrent.

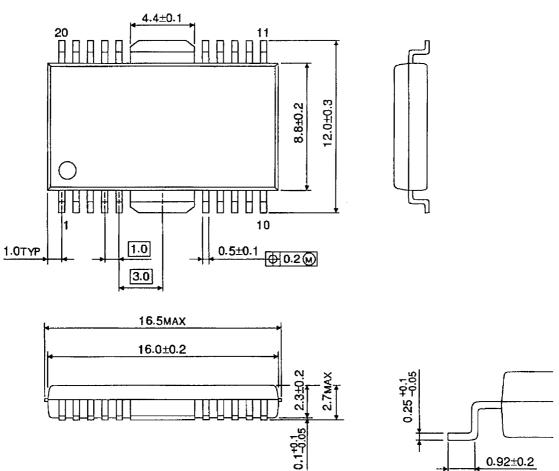
Also the IC may break down due to short circuit between output pins, an output pin and the  $V_{CC}$  pin, or an output pin and the GND pin.

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Design an output line, VCC line and GND line with great care.

### **Package Dimensions**

HSOP20-P-450-1.00 Unit: mm



Weight: 0.79 g (typ.)

#### **RESTRICTIONS ON PRODUCT USE**

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