TOSHIBA TD62914P

## TENTATIVE

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62914P

### 0.7 A MOTOR DRIVER WITH DIAGNOSIS

The TD62914P is a 0.7 A motor driver which directly drivers a bidirectional DC motor. Inputs DI1 and DI2 are combined to select one of forward, reverse, stop, and brake modes. Since the inputs are TTL-compatible, this IC can be controlled directly from a CPU or other control system. The IC also has various protective, self-diagnostic, and standby functions.



• Bidirectional DC motor driver.

Current capacity : 0.7A (Max.)

Low standby current : 100  $\mu$ A (Max.)

• Four operation modes: Forward, reverse, stop, and brake.

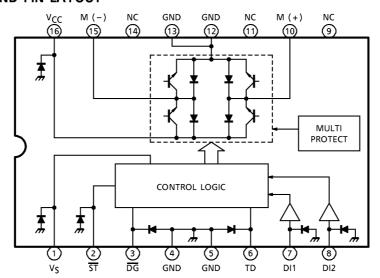
• Protective functions : Thermal shutdown and short-circuit protection

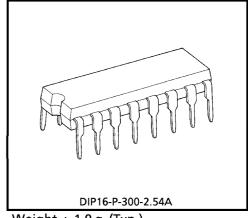
• Built-in counter electromotive force absorption diodes.

• Self-diagnostic output : On short-circuit detection.

• Plastic DIP-16pin.

#### **BLOCK DIAGRAM AND PIN LAYOUT**





Weight: 1.0 g (Typ.)

## **PIN DESCRIPTION**

PIN No.	SYMBOL	DESCRIPTION			
1	Vs	Control power supply pin. This pin is completely isolated from V <sub>CC</sub> .			
2	ST	When this pin is opened or grounded, the output turns off, thus reducing the current consumption to 100 $\mu$ A or less. If standby mode is not needed, the pin is connected to V <sub>CC</sub> .			
3	DG	Self-diagnostic output pin. When the output current becomes 1.5 A or more, this pin outputs a protection switching waveform similar to the waveform which would be supplied from M (+)/M (-) pin. If a capacitor is connected to the TD pin, the signal from this pin will become low after a specific delay. The output from the pin is an open-collector output.  The delay time is calculated approximately by the following:  TD = $50 \times C_T$ (ms) $C_T$ ( $\mu$ F)The permissible range of $C_T$ is from 0.01 $\mu$ F to 2 $\mu$ F.			
6	TD	Provides a delay for the $\overline{DG}$ output. A capacitor is connected between this pin and GND. When the pin is opened, the $\overline{DG}$ pin supplies a switching output.			
7	DI1	Output status control pin.			
8	DI2	Connects to a PNP-type voltage comparator.			
10	M (+)	Connects to the DC motor. Both the sink and the source have a current capacity of 0.7 A. Diodes for absorbing counter electromotive force are contained on the V <sub>CC</sub> and GND sides.			
15	M (-)	Connects to the DC motor together with pin 10 and has the same function a pin 10. This pin is controlled by the inputs from pins 7 and 8.			
16	Vcc	Output section power supply pin.			
4, 5, 12, 13	GND	Grounded.			
9, 11, 14	NC	Not connected. (Electrically, this pin is completely open.)			

## TRUTH TABLE

	INPUT		OUT	PUT	OUTPUT MODE	
DI1	DI2	ST	M (+)	M (-)	OUTPUT WIODE	
Н	Н	Н	L	L	Brake	
L	Н	Н	L	Н	Reverse (CCW)	
Н	L	Н	Н	L	Forward (CW)	
L	L	Н	OFF (high impedance)		Stop	
H/L	H/L	L	OFF (high impedance)		Standby (stop)	

## MAXIMUM RATINGS (Ta = 25°C)

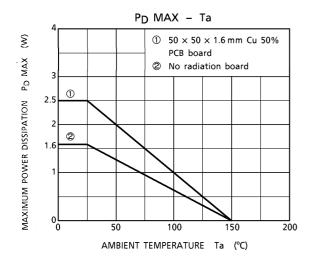
CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Supply Voltage	Vcc	7 to 30	V
Input Voltage	V <sub>IN</sub>	-0.3 to V <sub>C</sub> C	V
Output Current	lo	0.7	Α
Power Dissipation	PD	1.6	W
Operating Temperature	T <sub>opr</sub>	-40 to 85	°C
Storage Temperature	T <sub>stg</sub>	- 55 to 150	°C
Lead Temperature-time	T <sub>sol</sub>	260 (10 s)	°C

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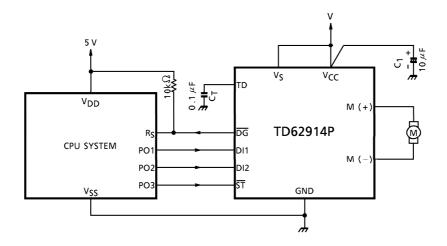
## **ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 24 \text{ V}$ , Ta = 25°C)

CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
	ls1		_	Stop	_	5	10		
Current Consumption 1	ls2	V <sub>S</sub>	_	Forward / Reverse	_	10	20	mA	
	ls3		_	Brake	_	10	20		
	I <sub>CC1</sub>	V <sub>CC</sub>	_	Stop	_	1.0	2.0	mA	
Current Consumption 2	I <sub>CC2</sub>		_	Forward / Reverse	_	15	30		
	I <sub>CC3</sub>		_	Brake	_	1.0	2.0		
	V <sub>IL</sub>	DI1 / DI2	_	_	_	_	0.8	V	
lanut Valtaga	V <sub>IH</sub>		_	_	2.0	_	_		
Input Voltage	V <sub>IL</sub>	- ST	_	_	_	_	0.5		
	VIH		_	_	2.0	_	_		
	Iμ	DI1/DI2	_	V <sub>IN</sub> = 0.4 V	<b>–</b> 10	_	10	μΑ	
Input Current	lін		_	$V_{IN} = V_{CC}$	<b>–</b> 10	_	10		
Impat Carrent	IIL	- ST	—	$V_{IN} = 0.4 V$			20		
	lн		_	V <sub>IN</sub> = V <sub>CC</sub>	_	_	2.0	mA	
Output Saturation Voltage	V <sub>sat</sub> (total)	M (+)/ M (-)	_	I <sub>O</sub> = 0.5 A, Tc = 25°C	1	1.7	2.4	V	
Output Lasks as Compant	<sup>I</sup> LEAK∙U	M (+)/	_	V <sub>OUT</sub> = 0 V		_	- 10	_	
Output Leakage Current	ILEAK·L	M (-)		V <sub>OUT</sub> = V <sub>CC</sub>		_	10	$\mu A$	
Output Voltage	V <sub>OL</sub>	DG	_	I <sub>OL</sub> = 3 mA	_	_	0.5	V	
Output Leakage Current	ILEAK		_	V <sub>OUT</sub> = V <sub>CC</sub>	_	_	10	μΑ	
Diode Forward Voltage	V <sub>F</sub> .U	M (+)/ M (-)		I <sub>F</sub> = 0.5 A	_	1.5	_	V	
Diode Forward Voltage	V <sub>F</sub> .L				_	1.5	_		
Over-current Detection	ISD	_	_	_	1.0	1.5	2.0	Α	
Shutdown Temperature	$T_{SD}$	_		_		150	_	°C	
Standby Current	IST	Vcc + Vs		<u>ST</u> = 0 V		_	100	μΑ	
Transfer Delay Time	t <sub>pLH</sub>	_	_	_	_	1	10	μs	
Transier Delay Time	t <sub>pHL</sub>	_	_	_	-	1	10		

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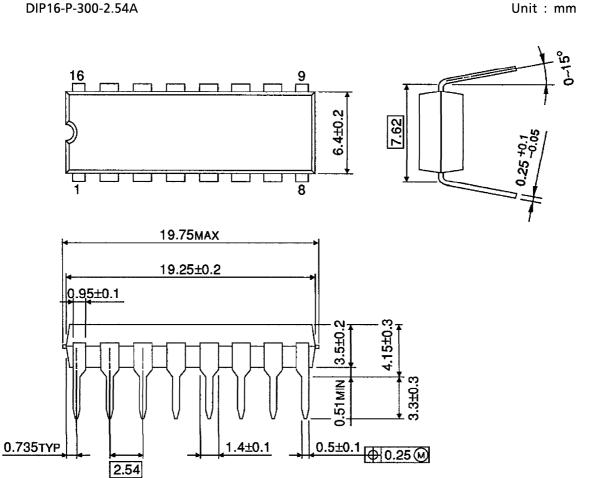
#### **EXAMPLE OF APPLICATION CIRCUIT**



Cautions for Wiring: C<sub>1</sub> is for absorbing disturbance, noise, etc. Connect is as close to the IC as possible.

## **PACKAGE DIMENSIONS**

DIP16-P-300-2.54A



Weight: 1.0 g (Typ.)

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000707EBA

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