

**Preliminary** TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

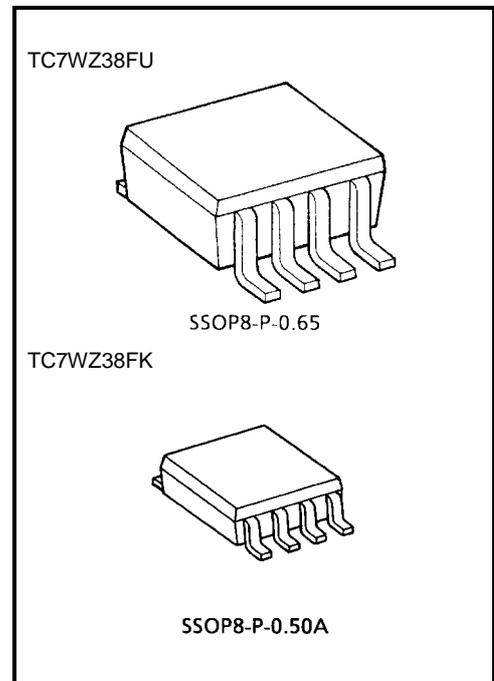
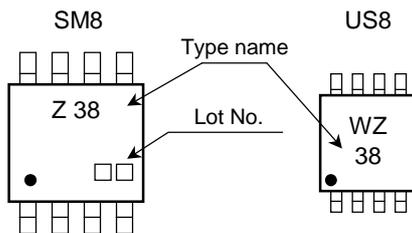
# TC7WZ38FU, TC7WZ38FK

Dual 2 Input Nand Gate (open drain)

## Features

- High output drive: 24 mA (min) @VCC = 3 V
- Super high speed operation:  $t_{pZL}$  2.2 ns (typ.) @VCC = 5 V, 50 pF
- Operation voltage range: VCC (opr) = 1.65~5.5 V
- Latch-up performance:  $\pm 500$  mA or more
- ESD performance:  $\pm 200$  V or more (JEITA)  
 $\pm 2000$  V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

## Marking

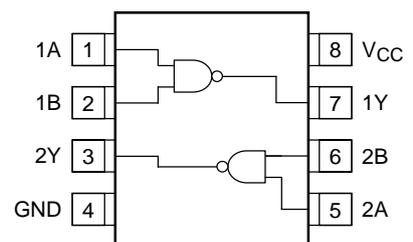


Weight  
 SSOP8-P-0.65 : 0.02 g (typ.)  
 SSOP8-P-0.50A : 0.01 g (typ.)

## Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	-0.5~6	V
DC input voltage	V <sub>IN</sub>	-0.5~6	V
DC output voltage	V <sub>OUT</sub>	-0.5~6	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	I <sub>OK</sub>	-20	mA
DC output current	I <sub>OUT</sub>	50	mA
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	$\pm 50$	mA
Power dissipation	P <sub>D</sub>	300 (SM8) 200 (US8)	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C
Lead temperature (10s)	T <sub>L</sub>	260	°C

## Pin Assignment (top view)



## Truth Table

A	B	Y
L	L	*H
L	H	*H
H	L	*H
H	H	L

\*: High impedance

## Logic Diagram



## Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	1.65~5.5	V
		1.5~5.5 (Note 1)	
Input voltage	$V_{IN}$	0~5.5	V
Output voltage	$V_{OUT}$	0~5.5 (Note 2)	V
		0~ $V_{CC}$ (Note 3)	
Operating temperature	$T_{opr}$	-40~85	°C
Input rise and fall time	$d_t/d_v$	0~20 ( $V_{CC} = 1.8\text{ V} \pm 0.15\text{ V}$ , $2.5\text{ V} \pm 0.2\text{ V}$ )	ns/V
		0~10 ( $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$ )	
		0~5 ( $V_{CC} = 5.5\text{ V} \pm 0.5\text{ V}$ )	

Note 1: Data retention only

Note 2:  $V_{CC} = 0\text{ V}$

Note 3: Low state

## Electrical Characteristics

### DC Characteristics

Characteristics		Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit	
					V <sub>CC</sub> (V)	Min	Typ.	Max	Min		Max
Input voltage	High level	V <sub>IH</sub>	—	1.65~1.95	0.75 × V <sub>CC</sub>	—	—	0.75 × V <sub>CC</sub>	—	V	
				2.3~5.5	0.7 × V <sub>CC</sub>	—	—	0.7 × V <sub>CC</sub>	—		
	Low level	V <sub>IL</sub>		1.65~1.95	—	—	0.25 × V <sub>CC</sub>	—	0.25 × V <sub>CC</sub>		
				2.3~5.5	—	—	0.3 × V <sub>CC</sub>	—	0.3 × V <sub>CC</sub>		
Output voltage	Low level	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 100 μA	1.65	—	0	0.1	—	0.1	V
					2.3	—	0	0.1	—	0.1	
					3.0	—	0	0.1	—	0.1	
					4.5	—	0	0.1	—	0.1	
				I <sub>OL</sub> = 4 mA	1.65	—	0.08	0.24	—	0.24	
				I <sub>OL</sub> = 8 mA	2.3	—	0.1	0.3	—	0.3	
				I <sub>OL</sub> = 16 mA	3.0	—	0.15	0.4	—	0.4	
				I <sub>OL</sub> = 24 mA	3.0	—	0.22	0.55	—	0.55	
				I <sub>OL</sub> = 32 mA	4.5	—	0.22	0.55	—	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND	0~5.5	—	—	±1	—	±10	μA	
Off-state current		I <sub>OZ</sub>	V <sub>IN</sub> = V <sub>IL</sub> , V <sub>OUT</sub> = V <sub>CC</sub> or GND	5.5	—	—	±5	—	±10	μA	
Power off leakage current		I <sub>OFF</sub>	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V	0.0	—	—	1	—	10	μA	
Quiescent supply current		I <sub>CC</sub>	V <sub>IN</sub> = 5.5 V or GND	1.65~5.5	—	—	1	—	10	μA	

## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

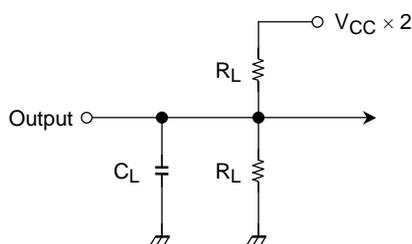
Characteristics	Symbol	Test Condition	Ta = 25°C			Ta = -40~85°C		Unit	
			V <sub>CC</sub> (V)	Min	Typ.	Max	Min		Max
Propagation delay time	t <sub>pZL</sub>	C <sub>L</sub> = 50 pF, R <sub>L</sub> = 500 Ω	1.8 ± 0.15	2.0	5.2	9.2	2.0	9.6	ns
			2.5 ± 0.2	1.5	3.5	5.7	1.5	6.1	
			3.3 ± 0.3	1.0	2.8	4.1	1.0	4.5	
			5.0 ± 0.5	0.5	2.2	3.4	0.5	3.6	
	t <sub>pLZ</sub>	C <sub>L</sub> = 50 pF, R <sub>L</sub> = 500 Ω	1.8 ± 0.15	2.0	4.6	9.2	2.0	9.6	
			2.5 ± 0.2	1.5	3.2	5.7	1.5	6.1	
			3.3 ± 0.3	1.0	2.4	4.1	1.0	4.5	
			5.0 ± 0.5	0.5	1.6	3.4	0.5	3.6	
Input capacitance	C <sub>IN</sub>	—	0~5.5	—	3.0	—	—	—	pF
Output capacitance	C <sub>OUT</sub>	—	0~5.5	—	2.5	—	—	—	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note)	3.3	—	6.9	—	—	—	pF
			5.5	—	13	—	—	—	

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

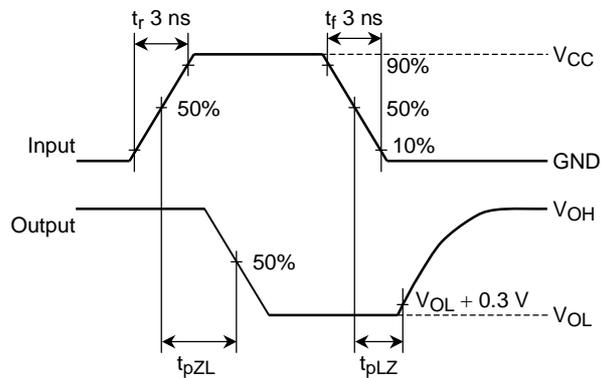
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$$

### Test Circuit



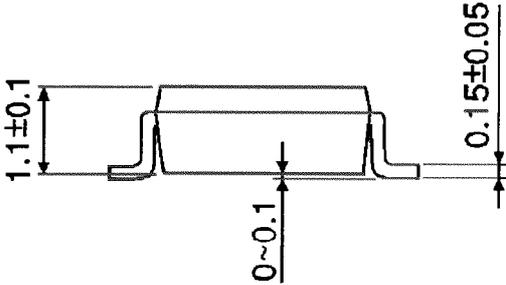
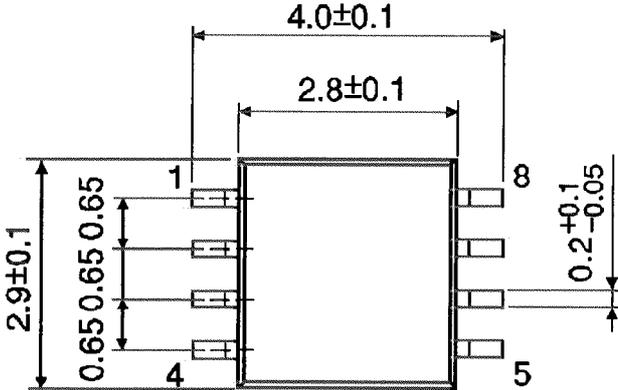
### AC Waveform



Package Dimensions

SSOP8-P-0.65

Unit : mm

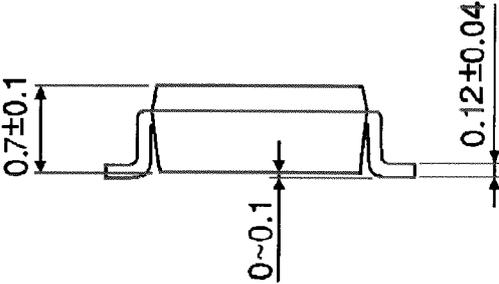
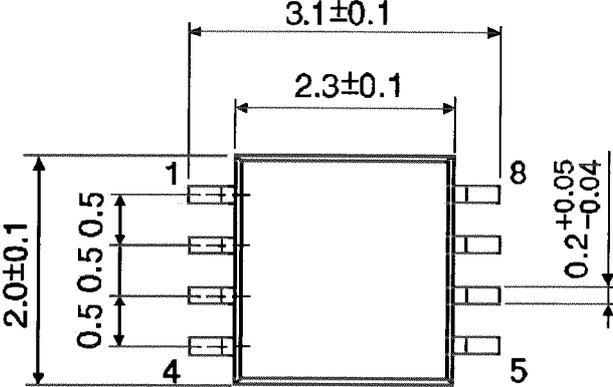


Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A

Unit : mm



Weight: 0.01 g (typ.)

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