**TOSHIBA** TC7SZU04F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7SZU04F, TC7SZU04FU

## **INVERTER (UNBUFFER)**

#### **FEATURES**

• High Output Drive : ± 16 mA (Typ.)

 $@V_{CC} = 4.5 V$ 

• Super High Speed Operation : tpD 2.4 ns (Typ.)

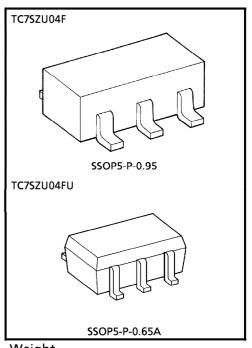
 $@V_{CC} = 5 \text{ V}, 50 \text{ pF}$ 

• Operation Voltage Range :  $V_{CC(opr)} = 1.8 \sim 5.5 \text{ V}$ 

• Supply Voltage Data Retention : V<sub>CC</sub> = 1.5~5.5 V

#### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	VCC	-0.5~6	V
DC Input Voltage	VIN	-0.5~6	V
DC Output Voltage	Vout	-0.5~V <sub>CC</sub> + 0.5	V
Input Diode Current	Ικ	± 20	mA
Output Diode Current	<sup>I</sup> ок	± 20	mA
DC Output Current	lout	± 50	mA
DC V <sub>CC</sub> / Ground Current	lcc	± 50	mA
Power Dissipation	PD	200	mW
Storage Temperature	T <sub>stg</sub>	- 65~150	°C
Lead Temperature (10 s)	TL	260	°C



Weight SSOP5-P-0.95 : 0.016 g (Typ.) SSOP5-P-0.65A : 0.006 g (Typ.)

2001-05-31

## DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	CVMPOL	TEST CONDITION			Ta = 25°C		$Ta = -40 \sim 85^{\circ}C$		UNIT	
CHARACTERISTIC SYMBOL		TEST CONDITION		Vcc (V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
				1.8 –	0.85			0.85		
High-Level Input Voltage	\ \ <sub>\\\</sub>			2.7	× Vcc		_	× Vcc	_	V
	VIH			3.0 -	0.8 ×			0.8 ×		] <b>'</b>
				5.5	Vcc			VCC		
				1.8 –			0.15		0.15	
Low-Level Input	\ \/			2.7	_		× Vcc		× Vcc	<sub>v</sub>
Voltage	VIL			3.0 –			0.2 ×		0.2 ×	"
				5.5	_		Vcc		Vcc	
				1.8	1.6	1.8		1.6		
		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	I <sub>OH</sub> = -100 μA	2.3	2.1	2.3		2.1	1	- V
		AIM = AIF		3.0	2.7	3.0	_	2.7		
High-Level ,,	\ \/a			4.5	4.0	4.4	_	4.0	_	
Output Voltage	Vон	V <sub>IN</sub> = GND	$I_{OH} = -4  \text{mA}$	2.3	1.9	2.14		1.9	_	
			$I_{OH} = -8  \text{mA}$	3.0	2.4	2.75	_	2.4	_	
			$I_{OH} = -12  \text{mA}$	3.0	2.3	2.61	_	2.3		
			$I_{OH} = -16  \text{mA}$	4.5	3.8	4.13	_	3.8	_	
			I <sub>OL</sub> = 100 μA	1.8	_	0	0.2	_	0.2	- V
				2.3	_	0	0.2	_	0.2	
Low-Level Output Voltage				3.0	_	0	0.3	_	0.3	
	\ \/			4.5	_	0	0.5	_	0.5	
	VOL	V <sub>IN</sub> = V <sub>CC</sub>	I <sub>OL</sub> = 4 mA	2.3	_	0.1	0.3	_	0.3	
			$I_{OL} = 8 \text{ mA}$ $I_{OL} = 12 \text{ mA}$	3.0	_	0.17	0.4	_	0.4	
				3.0	_	0.25	0.55	_	0.55	
			I <sub>OL</sub> = 16 mA	4.5	_	0.26	0.55	_	0.55	
Input Leakage	Leakage		0 -			± 1		± 10		
Current $I_{IN}$ $V_{IN} = 5.5 \text{ V or GND}$			5.5						$\mu$ A	
Quiescent Supply Current	ICC	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2	_	20	μΑ

AC ELECTRICA	<b>CHARACTERISTICS</b>	(Input tr	$= t_f$	= 3  ns
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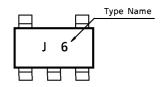
	1								
CHARACTERISTIC	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = -4	UNIT	
CHARACTERISTIC	3 I WIBOL		V <sub>CC</sub> (V)	MIN.	TYP.	MAX.	MIN.	MAX.	OINIT
			1.8	1.0	_	8.5	1.0	9.0	
Propagation t <sub>PLH</sub> Delay Time t <sub>PHL</sub>		$C_L = 15 pF$ ,	2.5 ± 0.2	0.8	_	6.2	0.8	6.5	
	$R_L = 1 M\Omega$	3.3 ± 0.3	0.5	_	4.5	0.5	4.8	ns	
		5.0 ± 0.5	0.5	_	3.9	0.5	4.1		
		C <sub>L</sub> = 50 pF,	3.3 ± 0.3	1.0	_	6.0	1.5	6.5	
		$R_L = 500 \Omega$	5.0 ± 0.5	0.8	_	5.0	0.8	5.5	
Input Capacitance	C <sub>IN</sub>		0 - 5.5	_	4.5	_	_	_	рF
Power Dissipation	1	(Note 1)	3.3	_	6.3	_	_	_	"E
Capacitance	C <sub>PD</sub>		5.5	_	9.5	_	_	_	pF

(Note 1) C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is Calculated from the operating current consumption without load.

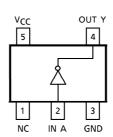
Average operating current can be obtained by the equation.

$$ICC(opr) = CPD \cdot VCC \cdot fIN + ICC$$

### **MARKING**



## PIN ASSIGNMENT (TOP VIEW)



### TRUTH TABLE

Α	Υ
L	Н
Н	L

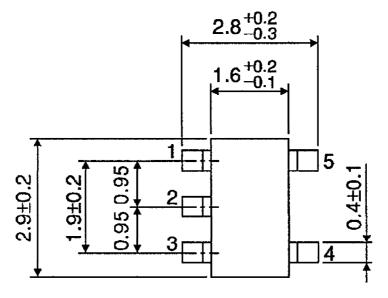
## **LOGIC DIAGRAM**

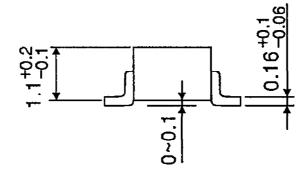


## PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm



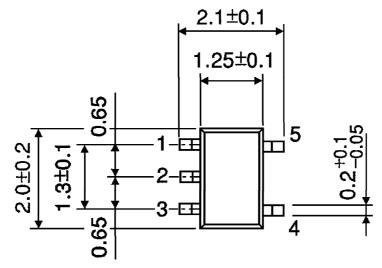


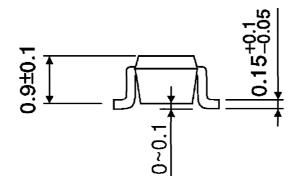
Weight: 0.016 g (Typ.)

## PACKAGE DIMENSIONS

SSOP5-P-0.65A

Unit: mm





Weight: 0.006 g (Typ.)

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