**TOSHIBA** TC7SZ32F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7SZ32F, TC7SZ32FU

## 2 INPUT OR GATE

#### **FEATURES**

• High Output Drive : ±24 mA (Typ.)

 $(V_{CC} = 3 V)$ 

• Super High Speed Operation : tpD = 2.4 ns (Typ.)

 $(V_{CC} = 5 V, 50 pF)$ 

• Operation Voltage Range :  $V_{CC (opr)} = 1.8 \sim 5.5 \text{ V}$ 

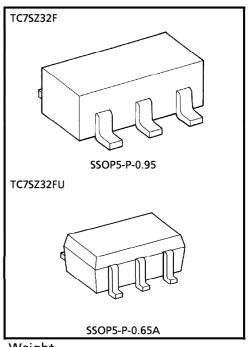
Supply Voltage Data Retention : V<sub>CC</sub> = 1.5~5.5 V

• 5 V Toleratnt Function

• Matches the Performance of TC74LCX Series when Operated at 3.3 V V<sub>CC</sub>

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	V <sub>CC</sub>	-0.5~6	V
DC Input Voltage	VIN	-0.5~6	V
DC Output Voltage	Vout	-0.5~6	V
Input Diode Current	lικ	± 20	mA
Output Diode Current	<sup>I</sup> ОК	± 20	mA
DC Output Current	lout	± 50	mA
DC V <sub>CC</sub> / Ground Current	lcc	± 50	mA
Power Dissipation	PD	200	mW
Storage Temperature	T <sub>stg</sub>	<b>-65∼150</b>	°C
Lead Temperature (10 s)	TL	260	°C



Weight SSOP5-P-0.95 : 0.016 g (Typ.) SSOP5-P-0.65A : 0.006 g (Typ.)

2001-05-31

## DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC SYMBOL		TECT COMPLETION		Vac	Ta = 25°C			Ta = -40~85°C		
CHARACTERISTIC	SAMBOL	TEST CONDITION		Vcc (V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
High-Level				1.8	0.88 × V <sub>CC</sub>	_	_	0.88 × V <sub>CC</sub>	_	V
Input Voltage	V <sub>IH</sub>			2.3~ 5.5	0.75 × V <sub>CC</sub>	_	_	0.75 × V <sub>CC</sub>	ı	
Low-Level				1.8	_	_	0.12 × V <sub>CC</sub>	_	0.12 × V <sub>CC</sub>	V
Input Voltage	nput Voltage				_	_	0.25 × V <sub>C</sub> C		0.25 × V <sub>CC</sub>	
				1.8	1.7	1.8	_	1.7	_	V
			$I_{OH} = -100 \mu A$	2.3	2.2	2.3		2.2	_	
			$\mu$	3.0	2.9	3.0	_	2.9		
High-Level Output Voltage	Vall	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		4.5	4.4	4.5	_	4.4	1	
	V <sub>ОН</sub>		$I_{OH} = -8  \text{mA}$	2.3	1.9	2.15	_	1.9	1	
			$I_{OH} = -16  \text{mA}$	3.0	2.4	2.8	_	2.4	_	
			$I_{OH} = -24  \text{mA}$	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32  \text{mA}$	4.5	3.8	4.2	_	3.8		
			100 A	1.8	_	0	0.1	_	0.1	
				2.3	_	0	0.1	_	0.1	
			$I_{OL} = 100 \mu A$	3.0	_	0	0.1	_	0.1	
Low-Level	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	\ \.\ \ \.\.\.		4.5	_	0	0.1	_	0.1	v
Output Voltage	VOL	$V_{OL}$ $V_{IN} = V_{IL}$	I <sub>OL</sub> = 8 mA	2.3	_	0.1	0.3	_	0.3	]
			I <sub>OL</sub> = 16 mA	3.0	_	0.15	0.4	_	0.4	
			$I_{OL} = 24  \text{mA}$	3.0	_	0.22	0.55	_	0.55	
			I <sub>OL</sub> = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input Leakage Current	IN	V <sub>IN</sub> = 5.5 V or GND		0~ 5.5			± 1		± 10	μΑ
Power Off Leakage Current	lOFF	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5 V		0.0	_	_	1	_	10	μΑ
Quiescent Supply Current	lcc	V <sub>IN</sub> = V <sub>CC</sub>	or GND	5.5	_	_	2	_	20	μΑ

AC ELECTRICAL CHARACTERISTICS (I	Input $t_r = \frac{1}{2}$	$t_f = 3 \text{ ns}$
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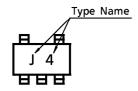
CHARACTERISTIC SYMBOL		TEST CONDITION		Ta = 25°C		Ta = -4			
			V <sub>CC</sub> (V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
			1.8	2.0	4.6	10.0	2.0	10.5	
		$C_L = 15  pF,$	2.5 ± 0.2	0.8	3.0	7.0	0.8	7.5	
Propagation	t <sub>PLH</sub>	$R_L = 1 M\Omega$	3.3 ± 0.3	0.5	2.4	4.7	0.5	5.0	]
I	t <sub>PHL</sub>	I .	5.0 ± 0.5	0.5	1.9	4.1	0.5	4.4	ns
		C <sub>L</sub> = 50 pF,	3.3 ± 0.3	1.5	3.0	5.2	1.5	5.5	
		$R_L = 500 \Omega$	5.0 ± 0.5	0.8	2.4	4.5	0.8	4.8	
Input Capacitance	C <sub>IN</sub>		0~5.5	_	4	1	_	_	pF
Power Dissipation Capacitance	Coo	(Note 1)	3.3	_	20		_	_	26
	C <sub>PD</sub> (N	(Note 1)	5.5	_	26	_	_	_	pF

(Note 1) CpD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

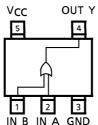
Average operating current can be obtained by the equation.

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

#### **MARKING**



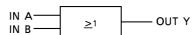
## PIN ASSIGNMENT (TOP VIEW)



### TRUTH TABLE

А	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

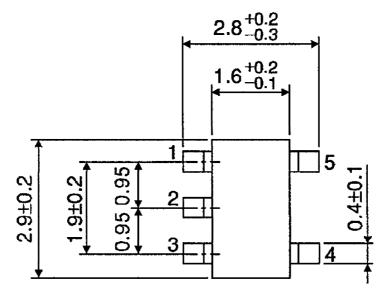
**LOGIC DIAGRAM** 

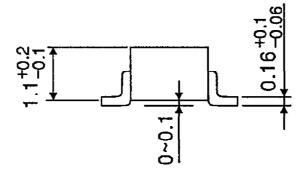


## PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm



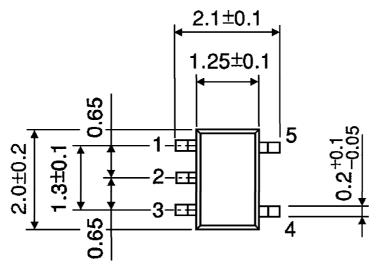


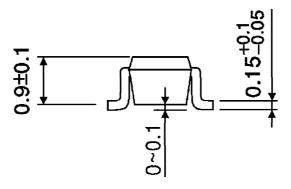
Weight: 0.016 g (Typ.)

Unit: mm

## PACKAGE DIMENSIONS

SSOP5-P-0.65A





Weight: 0.006 g (Typ.)

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