TOSHIBA TC7SZ04F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC7SZ04F, TC7SZ04FU

INVERTER

FEATURES

• High Output Drive : ± 24 mA (Typ.)

 $@V_{CC} = 3 V$

• Super High Speed Operation : tpD 2.4 ns (Typ.)

 $@V_{CC} = 5 \text{ V}, 50 \text{ pF}$

• Operation Voltage Range : $V_{CC}(opr) = 1.8 \sim 5.5 V$

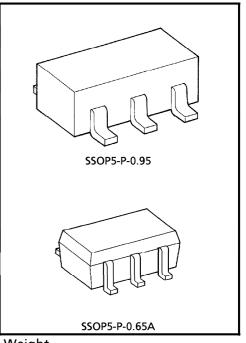
Supply Voltage Data Retention : V_{CC} = 1.5~5.5 V

• 5 V Tolerant Function

• Matches the Performance of TC74LCX Series when Operated at 3.3 V V_{CC}

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	SYMBOL RATING		
Supply Voltage Range	Vcc	-0.5~6	V	
DC Input Voltage	VIN	-0.5~6	V	
DC Output Voltage	Vout	-0.5~6	V	
Input Diode Current	lικ	± 20	mA	
Output Diode Current	^I ок	± 20	mA	
DC Output Current	lout	± 50	mA	
DC V _{CC} / Ground Current	lcc	± 50	mA	
Power Dissipation	PD	200	mW	
Storage Temperature	T _{stg}	- 65∼150	°C	
Lead Temperature (10 s)	TL	260	°C	



Weight SSOP5-P-0.95 : 0.016 g (Typ.) SSOP5-P-0.65A : 0.006 g (Typ.)

2001-05-31

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC SYMBOL TEST CONDITION			Ta = 25°C		$Ta = -40 \sim 85^{\circ}C$					
		TEST CONDITION		Vсс (V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
High-Level Input Voltage				1.8	0.88 × V _C C	_	_	0.88 × V _{CC}	_	- V
				2.3 – 5.5	0.75 × V _{CC}	_	_	0.75 × V _{CC}	_	
Low-Level Input				1.8	_		0.12 × V _{CC}	_	0.12 × V _{CC}	V
Voltage	VIL			2.3 – 5.5	_	_	0.25 × V _{CC}	_	0.25 × V _{CC}	
				1.8	1.7	1.8	_	1.7	_	
High-Level Output Voltage			$I_{OH} = -100 \mu A$	2.3	2.2	2.3	_	2.2	_	
				3.0	2.9	3.0	_	2.9	_	V
	Voн	VIN = VIL		4.5	4.4	4.5	_	4.4	_	
	νОн		$I_{OH} = -8 \text{mA}$	2.3	1.9	2.15	_	1.9	_	
			$I_{OH} = -16 \text{mA}$	3.0	2.4	2.8	_	2.4	_	
			$I_{OH} = -24 \text{mA}$	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
		V _{OL} V _{IN} = V _{IH}	I _{OL} = 100 μA	1.8	_	0	0.1	_	0.1	V
				2.3	_	0	0.1	_	0.1	
				3.0	_	0	0.1	_	0.1	
Low-Level	Voi			4.5	_	0	0.1	_	0.1	
Output Voltage	VOL		IOL = 8 mA	2.3	_	0.1	0.3	_	0.3	
			$I_{OL} = 16 \text{mA}$	3.0	_	0.15	0.4	_	0.4	
			$I_{OL} = 24 \text{mA}$	3.0		0.22	0.55	_	0.55	
			$I_{OL} = 32 \text{ mA}$	4.5		0.22	0.55	_	0.55	
Input Leakage Current	lIN	$V_{IN} = 5.5 V$ or GND		0 – 5.5	_	_	± 1	_	± 10	μΑ
Power Off Leakage Current	^I OFF	V _{IN} or V _{OUT} = 5.5 V		0.0	_	_	1	_	10	μΑ
Quiescent Supply Current	lcc	V _{IN} = V _{CC} or GND		5.5	_	_	2	_	20	μΑ

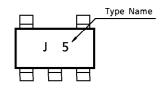
CHADACTERISTIC	CHARACTERISTIC SYMBOL	TEST CONDITION		Ta = 25°C Ta = -40		40∼85°C	~85°C			
CHARACTERISTIC		TEST CONDITION	V _{CC} (V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT	
			1.8	2.0	4.4	9.5	2.0	10.0		
		CL = 15 pF,	2.5 ± 0.2	0.8	2.9	6.5	0.8	7.0		
Propagation	tPLH	$RL = 1 M\Omega$	3.3 ± 0.3	0.5	2.1	4.5	0.5	4.7]	
Delay Time tPHL		5.0 ± 0.5	0.5	1.8	3.9	0.5	4.1	ns		
		CL = 50 pF,	3.3 ± 0.3	1.5	2.9	5.0	1.5	5.2		
		$RL = 500 \Omega$	5.0 ± 0.5	0.8	2.4	4.3	0.8	4.5		
Input Capacitance	CIN		0 - 5.5	_	4	_	_	_	pF	
Power Dissipation	C _{PD} (Note 1)	ower Dissipation	(Note 1)	3.3	_	20	_	_	_	nE.
Capacitance		5.5	_	26	_	_	_	pF		

(Note 1) CpD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

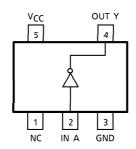
Average operating current can be obtained by the equation.

$$ICC (opr) = CPD \cdot VCC \cdot fIN + ICC$$

MARKING



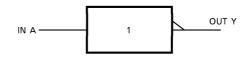
PIN ASSIGNMENT (TOP VIEW)



TRUTH TABLE

А	Υ
L	Н
Н	L

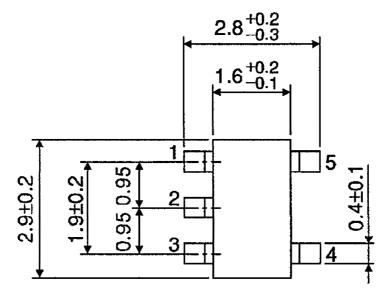
LOGIC DIAGRAM

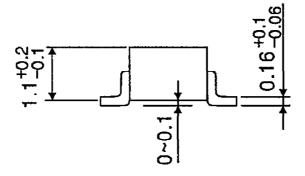


PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm



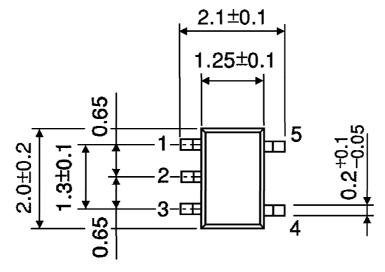


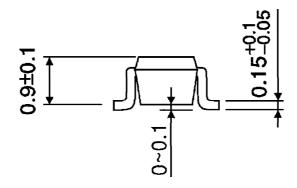
Weight: 0.016 g (Typ.)

PACKAGE DIMENSIONS

SSOP5-P-0.65A

Unit: mm





Weight: 0.006 g (Typ.)

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