TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SBD385AFU

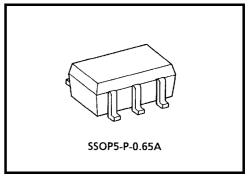
Single Bus Switch with Level Shifting

The TC7SBD385AFU provides single bit of high-speed TTL-compatible switching. The low on resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as just 1-bit low-impedance switch with output-enable (OE) input. When OE is high, the switch is on and data can flow from port A to port B, or vice versa. When OE is low, the switch is open and a high-impedance state exists between the two ports.

The device is enable to realize the shift of signal level from $5~\mathrm{V}$ to $3.3~\mathrm{V}$.

All inputs are equipped with protection circuits against static discharge.



Weight: 0.006 g (typ.)

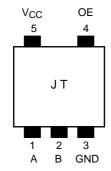
Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed operation: $t_{pd} = 0.32 \text{ ns (max)}$
- Low on resistance: $RON = 5 \Omega$ (typ.)
- ESD performance: Machine model > $\pm 200 \text{ V}$

Human body model $> \pm 2000 \text{ V}$

- TTL level input (control input)
- Low Power Dissipation: Icc = 10 μA (max.)
- Package: USV

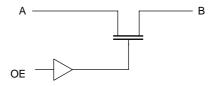
Pin Assignment (top view)



Truth Table

Input	Function		
OE	Function		
L	Disconnect		
Н	A port = B port		

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	٧
DC switch voltage	Vs	-0.5~7.0	٧
Input diode current	I _{IK}	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	PD	200	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	Vs	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Charac	teristics	Symbol	Test Condition		V _{CC} (V)	Min	Typ. (Note 1)	Max	Unit
Innut valtage	"H" level	V _{IH}	_	-	4.5~5.5	2.0	_	_	V
Input voltage	"L" level	VIL	_	-	4.5~5.5	_	_	0.8	V
High lavel avenue			1011 44		4.75	2.3	2.8	3.2	
High-level outp	(Note 2)	VoH	IOH=-1 μ A V _{IS} = V _{CC}		5.0	2.5	3.0	3.4	٧
	(Note 2)				5.25	2.7	3.2	3.6	
Input leakage of	current	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μΑ
Power off leaka	age current	l _{OFF}	A, B, OE= 0~5.5 V		0	_	_	±1.0	μА
Off-STATE lea (switch off)	kage current	I _{SZ}	A, B = 0~5.5 V, OE = V _{CC}		4.5~5.5	_	_	±1.0	μΑ
		V 0.V	I _{IS} = 64 mA	4.5	_	5	9		
				4.75	_	5	8		
ON resistance		D	$V_{IS} = 0 V$	I _{IS} = 30 mA	4.5	_	5	9	Ω
(Note 3)	KON		11S = 30 111A	4.75	_	5	8	32	
		V/10 - 2.2 V/ Jro - 15 m	/ 22// 45 ***		4.5 —	35	65		
		$V_{IS} = 2.3 \text{ V}, I_{IS} = 15 \text{ mA}$		4.75	_	35	50		
Quiescent supp	oly current	ICC	VIN = VCC or GND,I _{OUT} = 0		5.5	_	_	10	μА
Increase in I _{CC}	; per input	Δlcc	V _{IN} = 3.4 V (one input)		5.5	_	_	2.5	mA

Note 1: Typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$.

Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a VOH voltage level when using Pull-down resistance for an output terminal.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note 4)	4.5	ı	0.32	ns
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5		4.5	ns
Output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 3	4.5	_	5.0	ns

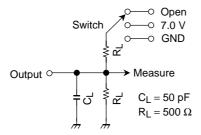
Note 4: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	(Note 5)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	$OE = V_{CC}$ (Note 5)	5.0	10	pF

Note 5: This parameter is guaranteed by design.

AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
t _{pLZ} , t _{pZL}	7.0 V		
t _{pHZ} , t _{pZH}	GND		

Figure 1

AC Waveform

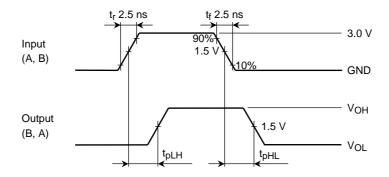


Figure 2 t_{pLH}, t_{pHL}

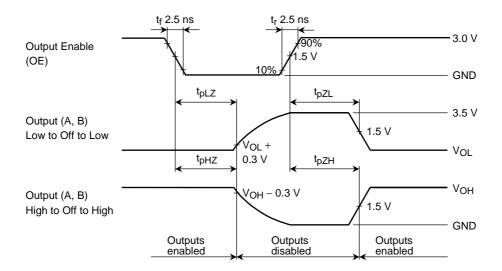
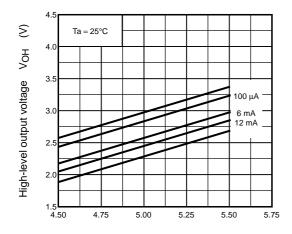
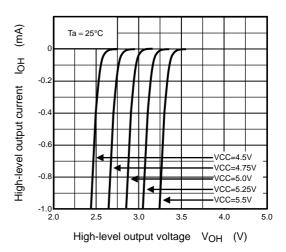
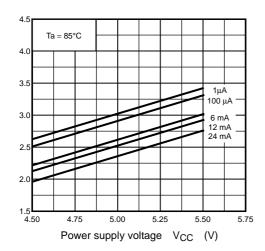


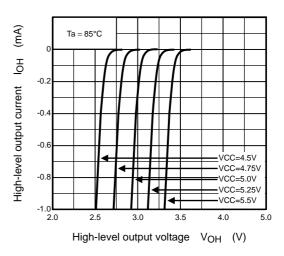
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

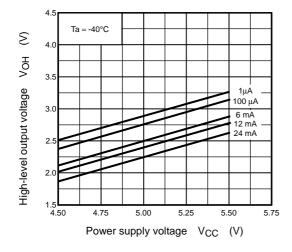
V_{OH} - V_{CC} Characteristics (typ.)











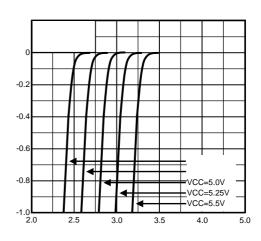
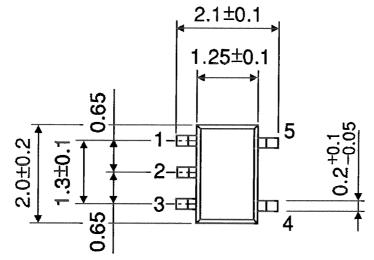


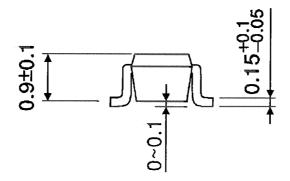
Figure 4

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Package Dimensions

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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