TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SA05F,TC7SA05FU

Inverter (open drain) with 3.6 V Tolerant Input and Output

#### **Features**

• Low voltage operation:  $V_{CC} = 1.8 \sim 3.6 \text{ V}$ 

• High speed operation :  $t_{pd} = 2.8 \text{ ns (max)} (V_{CC} = 3.0 \sim 3.6 \text{ V})$ 

:  $t_{pd} = 3.7 \text{ ns (max) (VCC} = 2.3 \sim 2.7 \text{ V)}$ 

 $: t_{pd} = 7.4 \text{ ns (max) (V}_{CC} = 1.8 \text{ V})$ 

• High Output current  $: IOH/IOL = \pm 24 \text{ mA (min)} (VCC = 3.0 \text{ V})$ 

 $: I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$ 

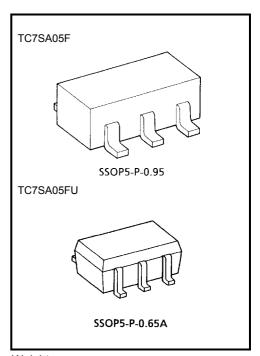
 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.8 \text{ V)}$ 

• Latch-up performance: ±300 mA or more

• ESD performance : Human body model  $> \pm 200 \text{ V}$ 

: Machine model  $> \pm 2000 \text{ V}$ 

• Power down protection is provided on all inputs and outputs.



Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

#### **Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V	
DC input voltage	V <sub>IN</sub>	-0.5~4.6	V	
DC output voltage	V	-0.5~4.6 (Note 1)	V	
DC output voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5 (Note 2)	V	
Input diode current	I <sub>IK</sub>	-50	mA	
Output diode current	lok	±50 (Note 3)	mA	
DC output current	lout	±50	mA	
Power dissipation	P <sub>D</sub>	200	mW	
DC V <sub>CC</sub> /ground current	Icc	±100	mA	
Storage temperature range	T <sub>stg</sub>	-65~150	°C	

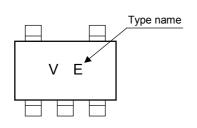
Note 1:  $V_{CC} = 0 V$ 

Note 2: High or low state. IOUT absolute maximum rating be observed.

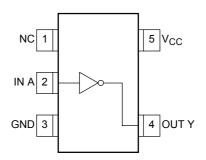
Note 3: Vout < GND, Vout > Vcc

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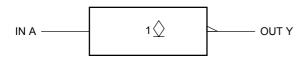
#### Marking



#### Pin Assignment (top view)



## **Logic Diagram**



#### **Truth Table**

Α	Y
L	*H
Н	L

\*: High impedance

## **Recommended Operating Range**

Characteristics	Symbol	Rating	Unit
Dower aupply voltage	V	1.8~3.6	V
Power supply voltage	V <sub>CC</sub>	1.2~3.6 (Note 4)	V
Input voltage	V <sub>IN</sub>	-0.3~3.6	V
Output voltage	V <sub>OUT</sub>	0~3.6 (Note 5)	V
Output voltage		0~V <sub>CC</sub> (Note 6)	V
		24 (Note 7)	
Output current	I <sub>OH</sub> /I <sub>OL</sub>	18 (Note 8)	mA
		6 (Note 9)	
Operating temperature range	T <sub>opr</sub>	-40~85	°C
Input rise and fall time	dt/dv	0~10 (Note 10)	ns/V

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or low state

Note 7:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 9:  $V_{CC} = 1.8 \text{ V}$ 

Note 10:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 

#### **Electrical Characteristics**

# DC Characteristics (Ta = $-40\sim85^{\circ}$ C, 2.7 V < V<sub>CC</sub> $\leq$ 3.6 V)

Characteristics Symbol Test Condition		Condition	V <sub>CC</sub> (V)	Min	Max	Unit		
	High level	V <sub>IH</sub>		_	2.7~3.6	2.0	_	.,
Input voltage	Low level	V <sub>IL</sub>		_	2.7~3.6	_	0.8	V
			I <sub>OL</sub> :		2.7~3.6	_	0.2	
Outrout walts as			DL VIN = VIH	I <sub>OL</sub> = 12 mA	2.7	_	0.4	V
Output voltage Low level	Low level	V <sub>OL</sub>		I <sub>OL</sub> = 18 mA	3.0	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage curre	ent	I <sub>IN</sub>	V <sub>IN</sub> = 0~3.6 V	•	2.7~3.6		±5.0	μΑ
Power off leakage	current	I <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0~	3.6 V	0	_	10.0	μΑ
Quiescent supply current		I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.7~3.6	_	20.0	
			$V_{CC} \le (V_{IN}, V_{OUT}) \le 3.6 \text{ V}$		2.7~3.6	_	±20.0	μΑ
Increase in I <sub>CC</sub> pe	r input	Δl <sub>CC</sub>	$V_{IH} = V_{CC} - 0.6$	V	2.7~3.6	_	750	

## DC Characteristics (Ta = $-40\sim85^{\circ}$ C, 2.3 V $\leq$ V<sub>CC</sub> $\leq$ 2.7 V)

Characteristics		Symbol	Test Condition			Min	Max	Unit
Onarac	Characteristics		1031	rest condition		IVIIII	IVIAX	Offic
Input voltage	High level	V <sub>IH</sub>		_	2.3~2.7	1.6	_	V
input voltage	Low level	V <sub>IL</sub>		_	2.3~2.7	_	0.7	V
				I <sub>OL</sub> = 100 μA	2.3~2.7	_	0.2	
Output voltage Low level	V <sub>OL</sub>	$V_{IN} = V_{IH}$	$I_{OL} = 12 \text{ mA}$	2.3	_	0.4	V	
				I <sub>OL</sub> = 18 mA	2.3	_	0.6	
Input leakage curre	ent	I <sub>IN</sub>	V <sub>IN</sub> = 0~3.6 V	•	2.3~2.7	_	±5.0	μА
Power off leakage	current	l <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0~3	.6 V	0	_	10.0	μА
Quioscont supply current		lcc	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.3~2.7	_	20.0	μА
Quiescent supply	Quiescent supply current		$V_{CC} \leqq (V_{IN},V_{OUT}) \leqq 3.6 \; V$		2.3~2.7	_	±20.0	

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# DC Characteristics (Ta = $-40\sim85^{\circ}$ C, 1.8 V $\leq$ V<sub>CC</sub> < 2.3 V)

Characteristics		Symbol	Test C	Condition		Min	Max	Unit
					V <sub>CC</sub> (V)			
Input voltage	High level	V <sub>IH</sub>	<del>_</del>		1.8~2.3	$\begin{array}{c} 0.7 \times \\ V_{CC} \end{array}$	_	V
input voitage	Low level	V <sub>IL</sub>		_	1.8~2.3	_	0.2 × V <sub>CC</sub>	V
Output valtage	Lowlovel	V/-	Var. Var.	$I_{OL} = 100 \mu A$	1.8	_	0.2	V
Output voltage	Low level	V <sub>OL</sub>	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 6 mA	1.8	_	0.3	V
Input leakage curre	ent	I <sub>IN</sub>	V <sub>IN</sub> = 0~3.6 V	•	1.8	_	±5.0	μА
Power off leakage	current	I <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0~3.	6 V	0	_	10.0	μΑ
Quiescent supply current		loo	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.8	_	20.0	Δ
Quiescent supply to	Julient	Icc	V <sub>CC</sub> ≤ (V <sub>IN</sub> , V <sub>OU</sub> T	r) ≦ 3.6 V	1.8	_	±20.0	μΑ

## AC Characteristics (Ta = $-40 \sim 85$ °C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
			1.8	1.0	8.2	
Propagation delay time	t <sub>pZL</sub>	Figure 1, Figure 2	$2.5 \pm 0.2$	8.0	4.1	ns
			$3.3 \pm 0.3$	0.6	3.5	
	t <sub>pLZ</sub>	Figure 1, Figure 2	1.8	1.0	6.8	
			$2.5 \pm 0.2$	0.8	3.8	ns
			$3.3 \pm 0.3$	0.6	3.5	

For  $C_L = 50\ pF$ , add approximately 300 ps to the AC maximum specification.

# Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
		V <sub>IN</sub> = 1.8 V, V <sub>IL</sub> = 0 V	(Note 11)	1.8	0.25	
Quiet output maximum dynamic V <sub>OL</sub>	$V_{OLP}$	V <sub>IN</sub> = 2.5 V, V <sub>IL</sub> = 0 V	(Note 11)	2.5	0.6	ns
		V <sub>IN</sub> = 3.3 V, V <sub>IL</sub> = 0 V	(Note 11)	3.3	8.0	
		V <sub>IN</sub> = 1.8 V, V <sub>IL</sub> = 0 V	(Note 11)	1.8	-0.25	
Quiet output minimum dynamic V <sub>OL</sub>	$V_{OLV}$	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 11)	2.5	-0.6	ns
		V <sub>IN</sub> = 3.3 V, V <sub>IL</sub> = 0 V	(Note 11)	3.3	-0.8	
		V <sub>IN</sub> = 1.8 V, V <sub>IL</sub> = 0 V	(Note 11)	1.8	1.5	
Quiet output minimum dynamic V <sub>OH</sub>	$V_{OLP}$	V <sub>IN</sub> = 2.5 V, V <sub>IL</sub> = 0 V	(Note 11)	2.5	1.9	ns
		V <sub>IN</sub> = 3.3 V, V <sub>IL</sub> = 0 V	(Note 11)	3.3	2.2	

Note 11: Parameter guaranteed by design.

#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Tost Condition	Test Condition		Tyro	Unit
Criaracteristics	Symbol	l est Condition		V <sub>CC</sub> (V)	Тур.	Offic
Input capacitance	C <sub>IN</sub>	_		1.8, 2.5, 3.3		pF
Power dissipation capacitance	$C_{PD}$	f <sub>IN</sub> = 10 MHz	(Note 12)	1.8, 2.5, 3.3		pF

Note 12: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

#### **AC Test Circuit**

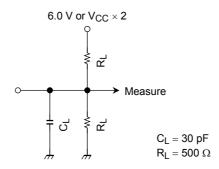
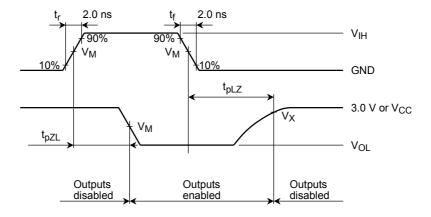


Figure 1

#### **AC Wareform**



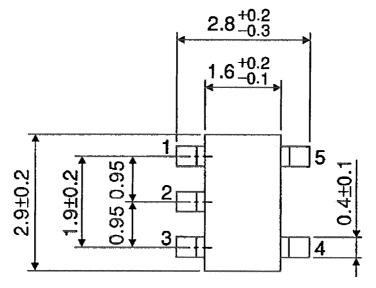
Symbol	Vcc						
Syllibol	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 V				
V <sub>IH</sub>	2.7 V	V <sub>CC</sub>	V <sub>CC</sub>				
$V_{M}$	1.5 V	V <sub>CC</sub> /2	V <sub>CC</sub> /2				
VX	V <sub>OH</sub> – 0.3 V	V <sub>OH</sub> – 0.15 V	V <sub>OH</sub> – 0.15 V				

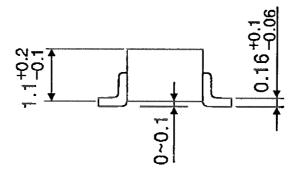
Figure 2 t<sub>pZL</sub>, t<sub>pLZ</sub>

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## **Package Dimensions**

SSOP5-P-0.95 Unit: mm





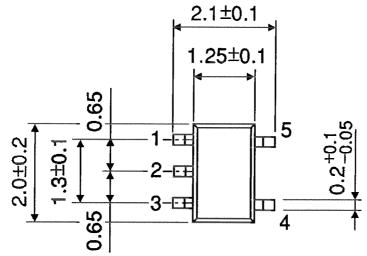
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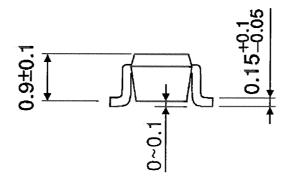
Weight: 0.016 g (typ.)

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## **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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