TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC74VHC21F, TC74VHC21FN, TC74VHC21FT

DUAL 4-INPUT AND GATE

The TC74VHC21 is an advanced high speed CMOS 4-INPUT AND GATE fabricated with silicon gate C²MOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

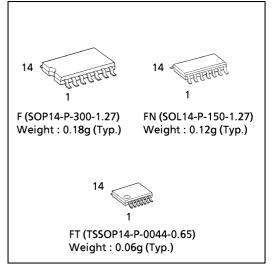
The internal circuit is composed of 4 stages including a buffer output, which provide high noise immunity and stable output.

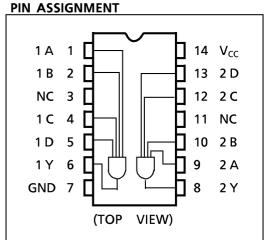
An input protection circuit ensures that 0 to 5.5V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

FEATURES:

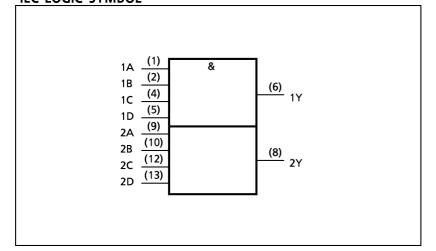
- High Speed······ t_{pd} = 3.3ns(typ.) at V_{CC} = 5V
- High Noise Immunity..... $V_{NIH} = V_{NII} = 28\% V_{CC}$ (Min.)
- Power Down Protection is provided on all inputs.
- Balanced Propagation Delays ····· t_{pLH} ≃ t_{pHL}
- Wide Operating Voltage Range \cdots V_{CC} (opr) = $2V \sim 5.5V$
- Pin and Function Compatible with 74ALS21

(Note) The JEDEC SOP (FN) is not available in Japan.





IEC LOGIC SYMBOL



TRUTH TABLE

	INP	OUTPUT									
Α	В	С	D	Υ							
ш	X	Х	Х	L							
Х	L	Х	Х	L							
Х	Х	L	Х	L							
Х	Х	Х	L	L							
Ι	Ι	Н	Η	Н							
V D // C											

X : Don't Care

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	V_{CC}	-0.5~7.0	>
DC Input Voltage	V _{IN}	-0.5~7.0	٧
DC Output Voltage	V _{OUT}	$-0.5 \sim V_{CC} + 0.5$	V
Input Diode Current	I _{IK}	-20	mA
Output Diode Current	I _{OK}	± 20	mA
DC Output Current	I _{OUT}	± 25	mA
DC V _{CC} /Ground Current	I _{cc}	± 50	mA
Power Dissipation	P _D	180	mW
Storage Temperature	T _{stg}	−65~150	°C

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V _{cc}	2.0~5.5	>
Input Voltage	VIN	0~5.5	٧
Output Voltage	V _{OUT}	0~V _{cc}	٧
Operating Temperature	T _{opr}	−40~85	°C
Input Rise and Fall Time	dt/dv	$0\sim100 \ (V_{CC} = 3.3 \pm 0.3 V)$ $0\sim20 \ (V_{CC} = 5 \pm 0.5 V)$	ns / V

DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION		V _{cc}	Ta = 25°C		С	Ta = -4	UNIT	
PARAIVIETER	STIVIBUL			(V)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
High - Level	.,			2.0	1.50	_	_	1.50	_	
Input Voltage	V _{IH}			3.0~ 5.5	$V_{cc} \times 0.7$	_	_	$V_{cc} \times 0.7$	_	V
Low - Level	.,			2.0 3.0~	_	_	0.50	_	0.50	,,
Input Voltage	V _{IL}				_	_	$V_{cc} \times 0.3$	_	$V_{cc} \times 0.3$	V
	V _{OH}	V _{1 N} = V _{1 H}	$I_{OH} = -50\mu A$	2.0	1.9	2.0	_	1.9		
High - Level Output Voltage				3.0 4.5	2.9 4.4	3.0 4.5	_	2.9 4.4	_	V
			$I_{OH} = -4mA$ $I_{OH} = -8mA$	3.0 4.5	2.58 3.94	_	_	2.48 3.80		
				2.0	_	0.0	0.1	_	0.1	
Low - Level Output Voltage	V _{OL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 50 \mu A$	3.0 4.5	_	0.0 0.0	0.1 0.1	_ _	0.1 0.1	V
			I _{OL} = 4mA I _{OL} = 8mA	3.0 4.5		_	0.36 0.36	_	0.44 0.44	
Input Leakage Current	I _{IN}	$V_{1N} = 5.5V \text{ or GND}$		0~5.5	_	_	± 0.1	_	± 1.0	_
Quiescent Supply Current	I _{cc}	$V_{IN} = V_{CC}$ or GN	5.5	_	_	2.0	_	20.0	μΑ	

AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3ns$)

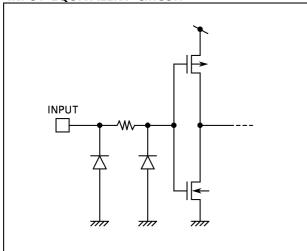
PARAMETER	SYMBOL	TEST CONDITION		Ta = 25°C			Ta = -40~85°C		UNIT	
PARAIVIETER	STIVIBOL		V _{CC} (V)	CL (pF)	MIN.	TYP.	MAX.	MIN.	MAX.	UIVII
	t _{pLH} t _{pHL}		3.3 ± 0.3	15		4.8	7.0	1.0	8.5	ns
Propagation Delay Time				50	1	7.3	10.5	1.0	12.0	
Fropagation Delay Time			5.0 ± 0.5	15	1	3.3	5.0	1.0	6.0	
				50	-	4.8	7.0	1.0	8.0	
Input Capacitance	C _{I N}				_	4	10	_	10	»E
Power Dissipation Capacitance C _{PD}		(Note 1)		_	20	_	_	_	pF

Note (1) C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

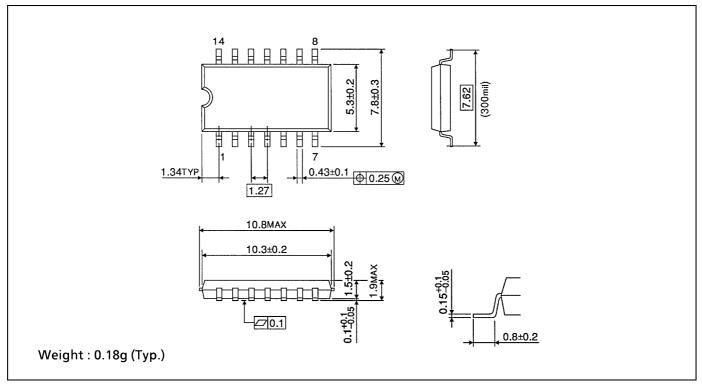
$$I_{CC(opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2 \text{ (per Gate)}$$

INPUT EQUIVALENT CIRCUIT



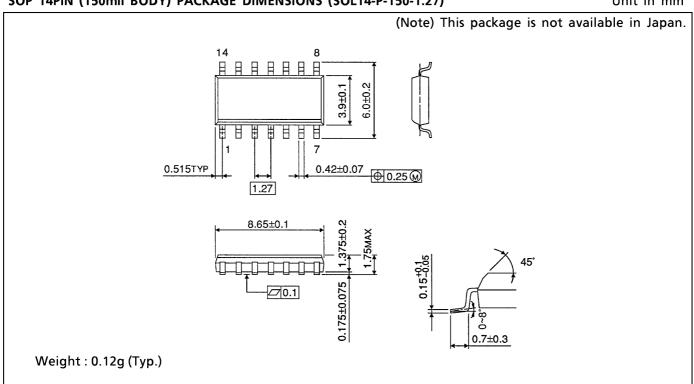
SOP 14PIN (200mil BODY) PACKAGE DIMENSIONS (SOP14-P-300-1.27)

Unit in mm



SOP 14PIN (150mil BODY) PACKAGE DIMENSIONS (SOL14-P-150-1.27)

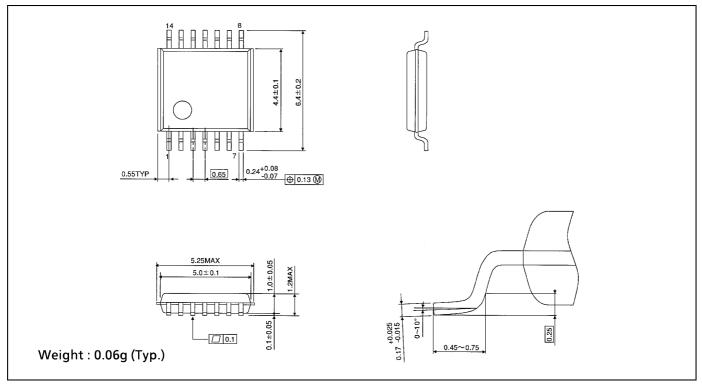
Unit in mm



4

TSSOP 14PIN PACKAGE DIMENSIONS (TSSOP14-P-0044-0.65)

Unit in mm



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