TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8440H

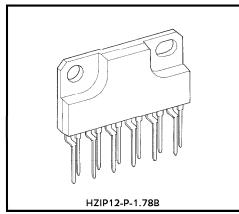
DC MOTOR FULL BRIDGE DRIVER

The TA8440H is a full-bridge driver for selecting the forward and reverse running of a motor with brushes and is able to control 4 modes of forward, reverse, stop and braking.

The motor driving unit and the control unit have a separate power supply line, independently and the TA8440H is also usable as a stepping motor driver.

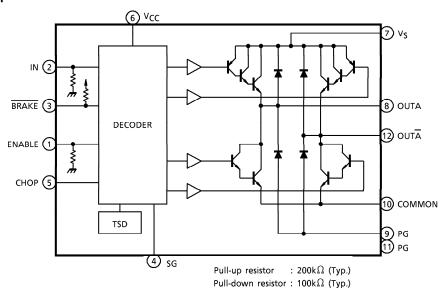
FEATURES

- Output current is as large as 1.5A (AVE) and 3.0A (PEAK).
- 4 modes of forward, reverse, stop, and braking are available and a counter-electromotive force absorbing diode has been built-in.
- Thermal shutdown circuit incorporated.
- Input is compatible with CMOS.
- Built-in input pull-up resistor. BRAKE = 200k Ω (Typ.)
- Built-in input pull-down resistor. IN, ENABLE = $100k\Omega$ (Typ.)



Weight: 4.04g (Typ.)

BLOCK DIAGRAM



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PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION				
1	ENABLE	ENABLE terminal				
2	IN	Forward rotation / reverse rotation switch terminal				
3	BRAKE	BRAKE terminal				
4	SG	Signal GND				
5	CHOP	PWM signal input terminal				
6	V_{CC}	Power voltage supply terminal for control				
7	٧s	Power voltage supply terminal for motor driver				
8	OUTA	Output terminal				
9	PG	Power GND				
10	COMMON	COMMON terminal				
11	PG	Power GND				
12	OUTĀ	Output terminal				

FUNCTION

INPUT				OUT	MODE	
IN	BRAKE	ENABLE	СНОР	OUTA	OUTĀ	MOTOR
Н	Н	Н	L	Н	L	CW/CCW
L	Н	Н	L	L	Н	CCW/CW
(*)	(*)	L	(*)	∞	∞	Stop
(*)	L	Н	(*)	L	L	Brake
Н	Н	Н	Н	∞	L	Chop
L	Н	Н	Н	Ĺ	∞	Chop

(*) Don't care (∞) High impedance

MAXIMUM RATINGS (Ta = 25°C)

111 5 111 12 11 12 1 1 1 1 1 1 1 1 1 1 1							
CHARACTERIST	TC TC	SYMBOL	RATING	UNIT			
Supply Voltage		V _{CC} V _S	. 7 50	V			
Input Voltage		V _{IN}	−0.3~V _{CC}	V			
Output Current	AVE.	lO (AVE.)	1.5	Α			
Output Current	PEAK	IO (PEAK)	3.0 (Note 1)	Α			
Power Dissipation			2.52 (Note 2)	w			
Power Dissipation		PD	25.0 (Note 3)	VV			
Operating Temperatu	ire	T _{opr}	- 30∼75	°C			
Storage Temperature		T _{stg}	- 55∼150	Ç			

(Note 1) t = 100 ms

(Note 2) No heat sink

(Note 3) $Tc = 75^{\circ}C$

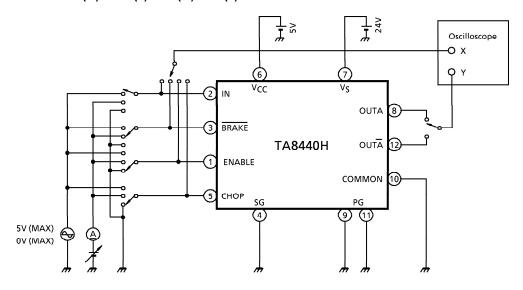
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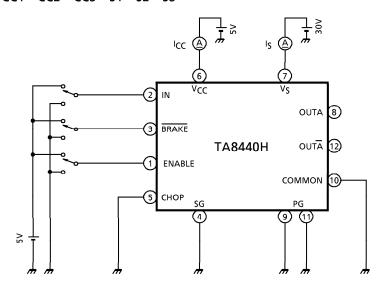
ELECTRICAL CHARACTERISTICS ($V_{CC} = 5V$, $V_S = 24V$, Ta = 25°C)

		· cc ·							
CHARACTE	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN.	TYP.	MAX.	UNIT	
1	High	V _{IN} (H)	_	IN, CHOP, ENABLE, BRAKE		3.5	_	Vcc	V
Input Voltage	Low	VIN (L)	1			GND	_	1.5	
	High	IN-1 (H)	1	СНОР	V _{IN} = 5V	_	5	52	μΑ
Input Current		I _{IN-2} (H)		IN, ENABLE		_	40	60	
		^I IN-3 (H)		BRAKE		_	0	5.5	
		I _{IN-1} (L)		СНОР	V _{IN} = 0V Source type	_	0	5.5	
Input Current	Low	I _{IN-2} (L)		IN, ENABLE		_	0	5.5	
		^I IN-3 (L)		BRAKE		_	25	52	
		lcc1		Stop		_	6	10.5	
Current Consumption (I)		l _{CC2}	2	Forward / reverse		_	10	14.5	mA
		lCC3		Brake			14	18.5	
		I _{S1}		Stop		_	2	4.2	mA
Current Consump	otion (II)	I _{S2}	2	Forward / reverse		_	3.5	5.0	
		I _{S3}		Brake		_	2.5	3.7	
Output	Upper Side	V _{sat-U1}		I _{OUT} = 1.5A		1.5	2.0	2.7	
Saturation	Under Side	V _{sat-L1}	3	1001 - 1.9A		0.7	1.25	1.9	- V
Voltage	Upper Side	V _{sat-U2}]	I _{OUT} = 3.0A		2.7	3.0	3.9	
	Under Side	V _{sat-L2}				1.7	2.0	2.9	
Diode Forward Orientation	Upper Side	V _{F-U1}	_	I _{OUT} = 1.5A		_	3.5	_	V
Voltage	Under Side	V _{F-L1}				_	1.3		
Output Leakage		ГОН	4	V _S = 30V		_	_	200	μΑ
Current	Under Side	lol		v2 = 30 v			_	100	
Shut Down Temperature		T_{SD}	_			_	170		°C
		t_pLH		IN-OUT		_	2.7	_	
Transfer Time		t_{pHL}		114-001	IN-OUT		1.2	—	μ s
		t _{pLH}	1	CHOP-OUT		_	0.7	_	
		t _{pHL}				_	2.5	_	
		t _{pLH}	1 —	ENIA DI E CLIT		_	2.9	<u> </u>	
		t _{pHL}	1	ENABLE-OU	I	_	1.1	_	1
		t _{pLH}	1	PRAKE OUT		_	45	_	
	t _{pHL}	1	BRAKE-OUT	AKE-OUT		45	<u> </u>		
	P''-					<u> </u>	1		

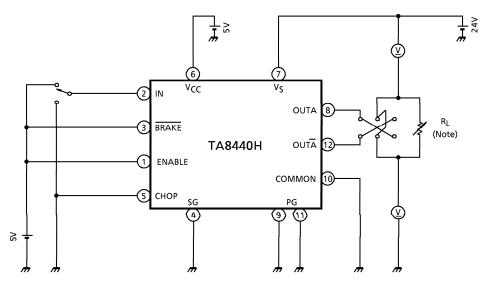
TEST CIRCUIT 1. $V_{IN(H)}$, $V_{IN(L)}$, $I_{IN(H)}$, $I_{IN(L)}$



TEST CIRCUIT 2. I_{CC1}, I_{CC2}, I_{CC3}, I_{S1}, I_{S2}, I_{S3}

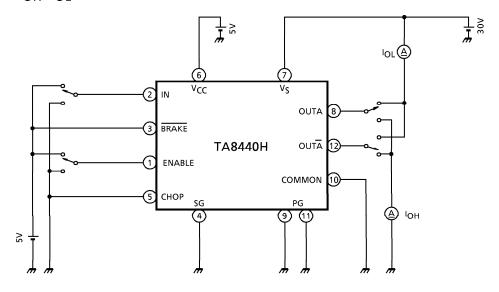


TEST CIRCUIT 3. V_{sat-L} , V_{sat-U}

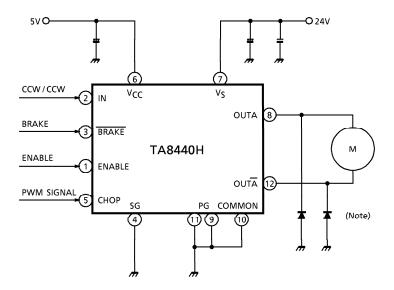


(Note) Calibrate $I_{\mbox{OUT}}$ to 1.5/3.0A by RL.

TEST CIRCUIT 4. IOH, IOL



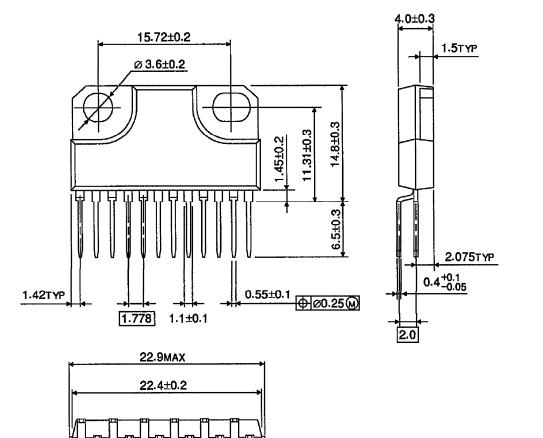
APPLICATION CIRCUIT



- (Note 1) Schottky diode (2GWJ42) to be connected additionally between each output (pin 16/19/20/23) and GND for preventing Punch-Through Current.
- Utmost care is necessary in the design of the output line, V_S and GND line since (Note 2) IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

OUTLINE DRAWING

HZIP12-P-1.78B Unit: mm



12

Weight: 4.04g (Typ.)