

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

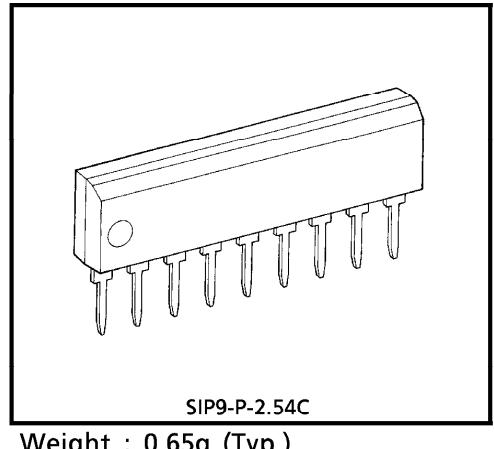
TA8193S

DUAL LINE OUTPUT AMPLIFIER

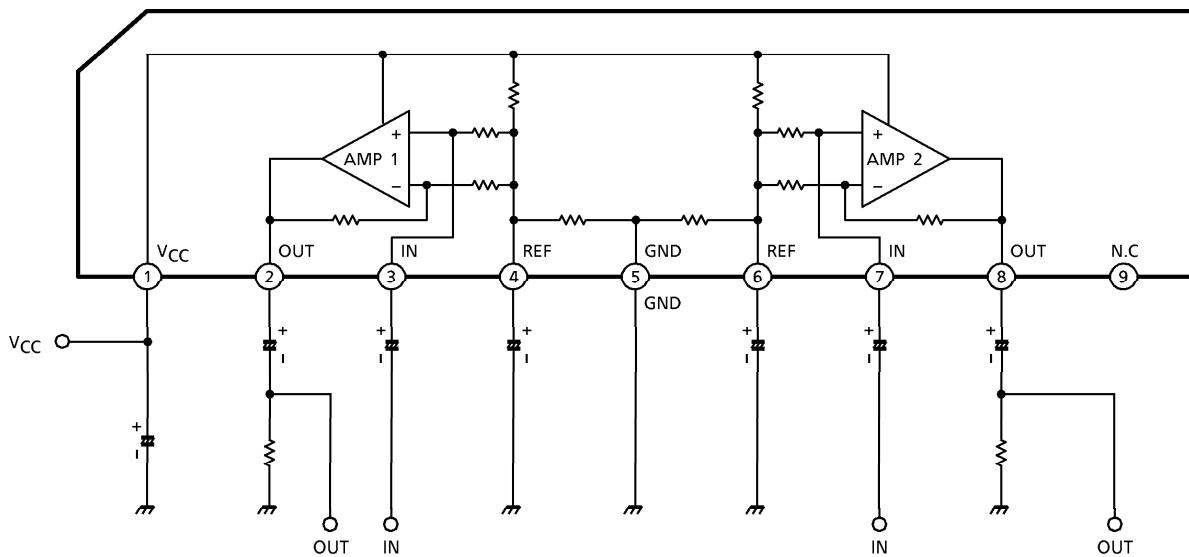
The TA8193S is dual Line Output Amplifier designed for audio use.

FEATURES

- Suitable for Line Output Amplifier
- Built-in Feedback Resistor
- Few External Parts
- Fixed Voltage Gain : $G_V = 20\text{dB}$ (Typ.)
- Low Noise : $V_{NO} = 30\mu\text{V}_{rms}$ (Typ.)
 $(R_g = 10k\Omega, \text{ DIN AUDIO})$
- Small Package : Slim SIP 9pin
- Operating Supply Voltage Range : $V_{CC(\text{opr.})} = 5\sim 16\text{V}$ ($T_a = 25^\circ\text{C}$)



BLOCK DIAGRAM



MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

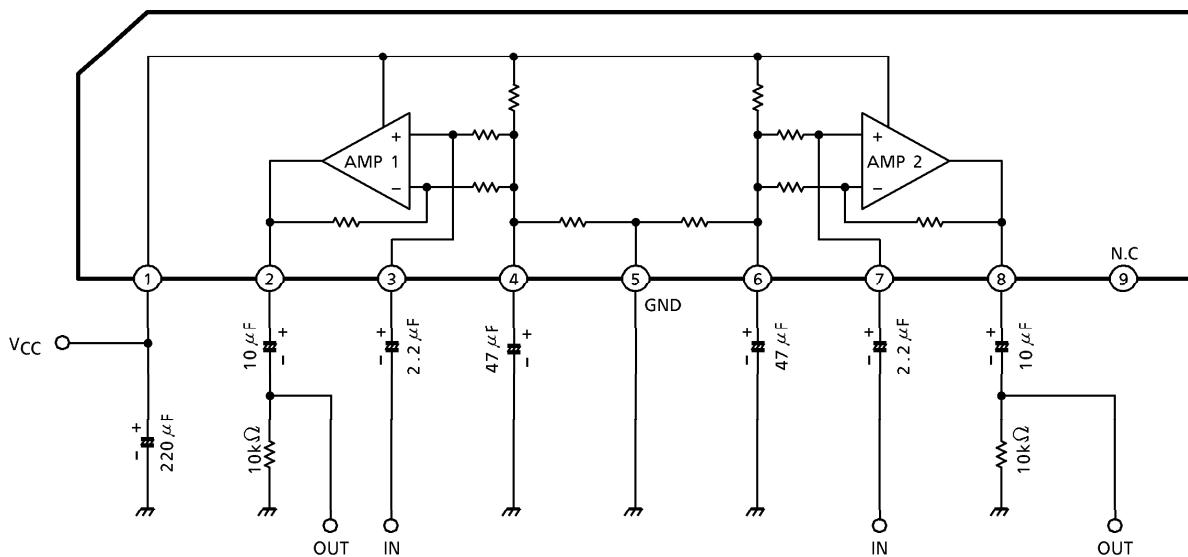
CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{CC}	18	V
Power Dissipation	P_D (Note)	500	mW
Operating Temperature	T_{opr}	-30~85	°C
Storage Temperature	T_{stg}	-55~150	°C

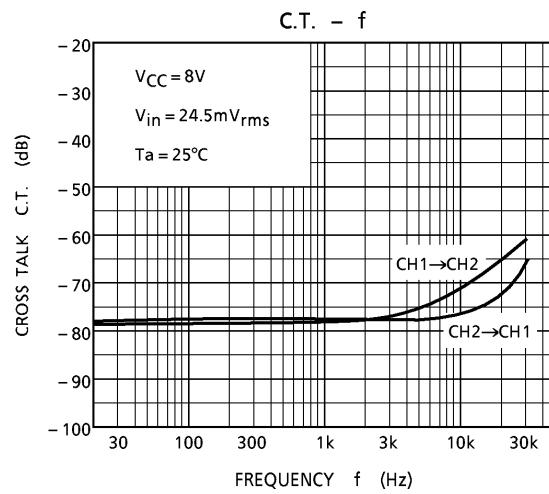
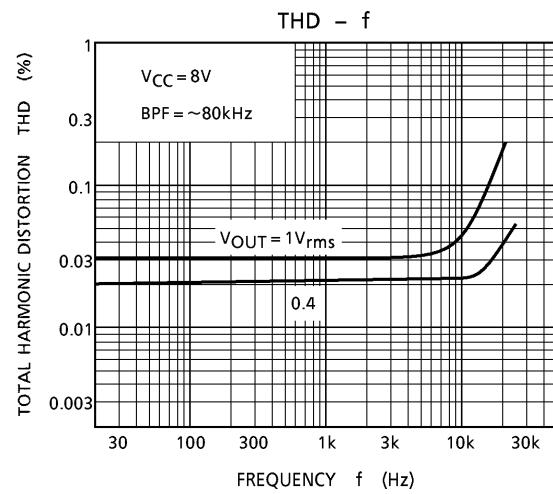
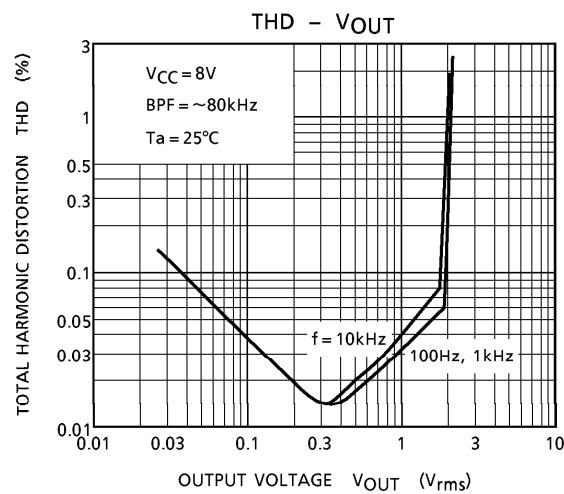
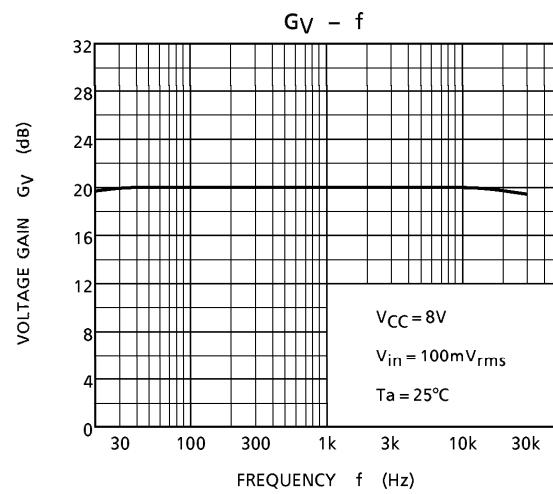
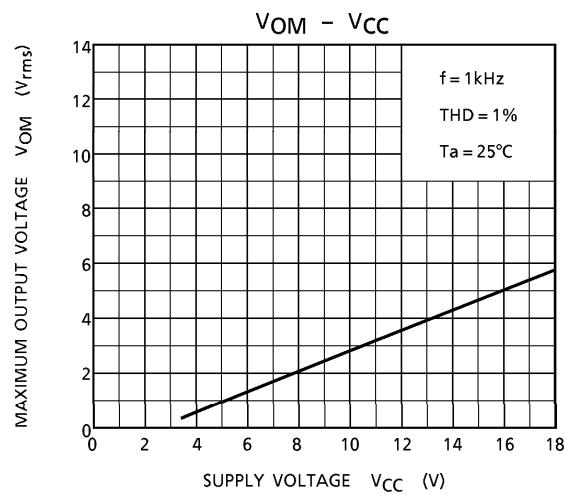
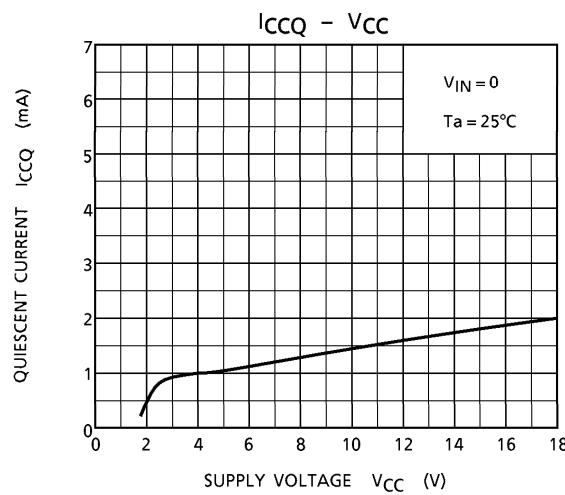
(Note) Derated above $T_a = 25^\circ\text{C}$ in the proportion of $4\text{mW} / ^\circ\text{C}$.

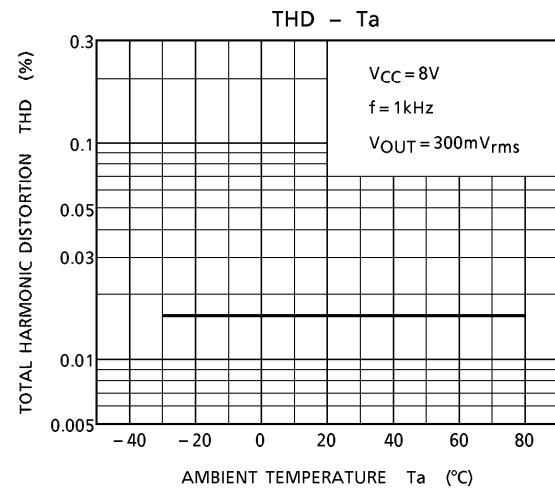
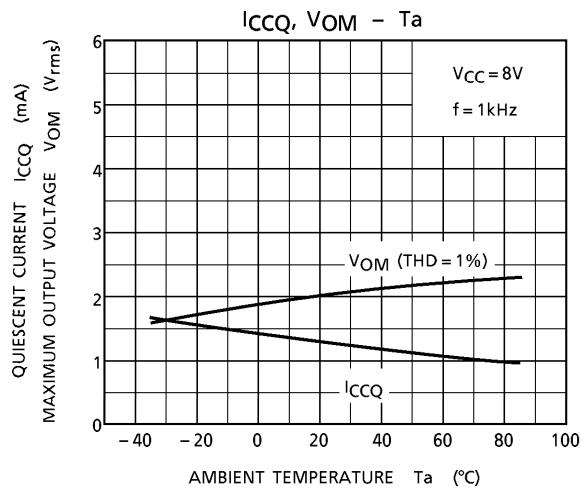
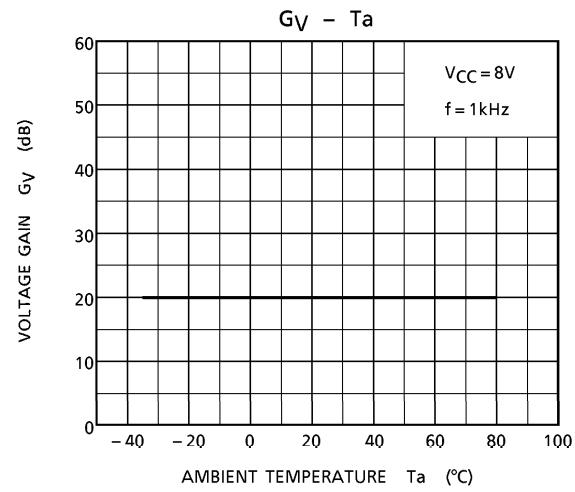
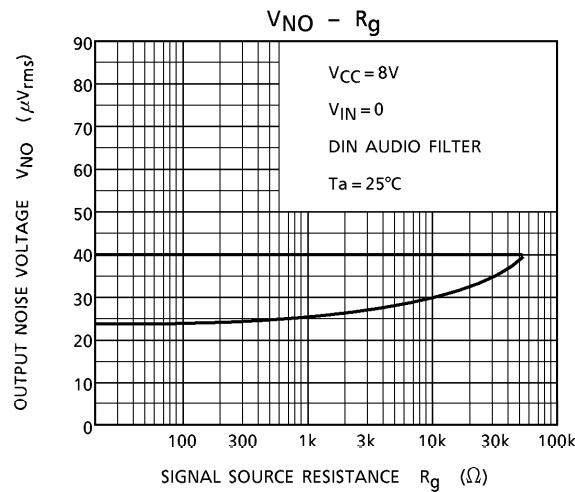
ELECTRICAL CHARACTERISTICS (Unless otherwise specified, $V_{CC} = 8\text{V}$, $f = 1\text{kHz}$, $R_L = 10\text{k}\Omega$, $T_a = 25^\circ\text{C}$)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Quiescent Current	I_{CCQ}	—	$V_{IN} = 0$	—	1.5	2.5	mA
Maximum Output Voltage	V_{OM}	—	$\text{THD} = 1\%$	1.4	2.0	—	V_{rms}
Closed Loop Voltage Gain	G_V	—	—	18.5	20	21.5	dB
Total Harmonic Distortion	THD	—	$V_{OUT} = 300\text{mV}_{\text{rms}}$	—	0.02	0.3	%
Output Noise Voltage	V_{NO}	—	$R_g = 10\text{k}\Omega$, $\text{BPF} = 20\sim 20\text{kHz}$	—	30	60	μV_{rms}
Cross Talk	C.T	—	$R_g = 10\text{k}\Omega$	—	-75	—	dB

TEST CIRCUIT

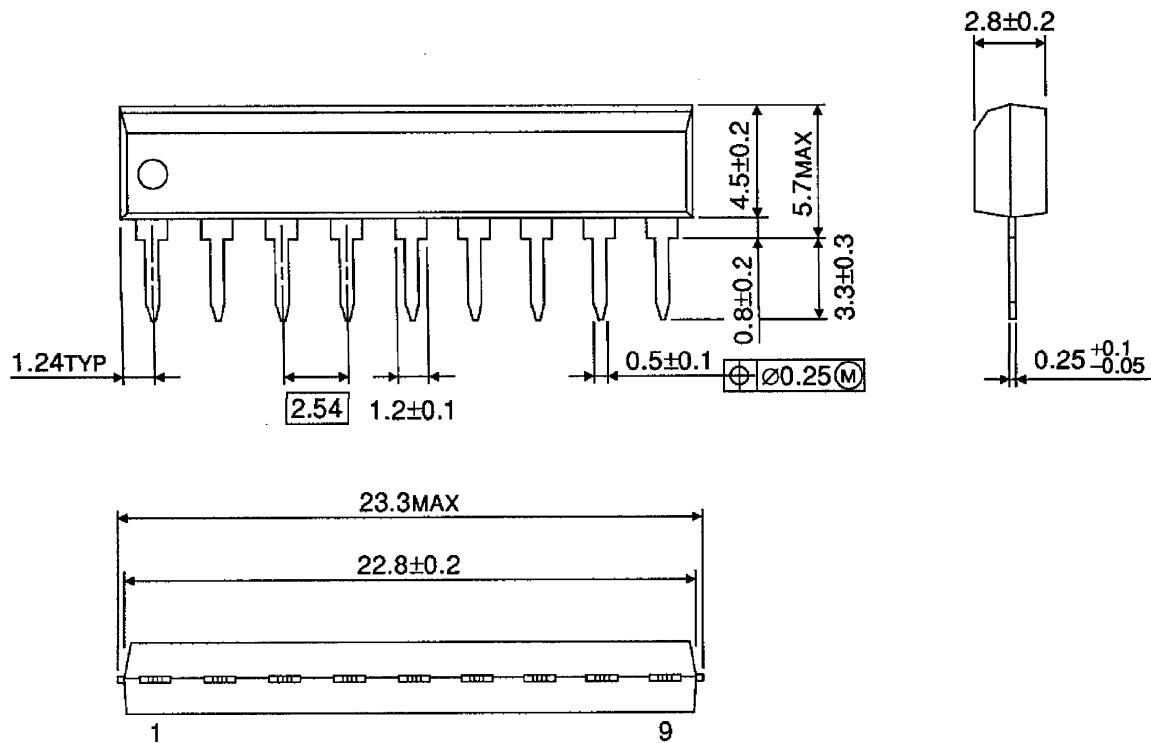






PACKAGE DIMENSIONS
SIP9-P-2.54C

Unit : mm



Weight : 0.65g (Typ.)

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