

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

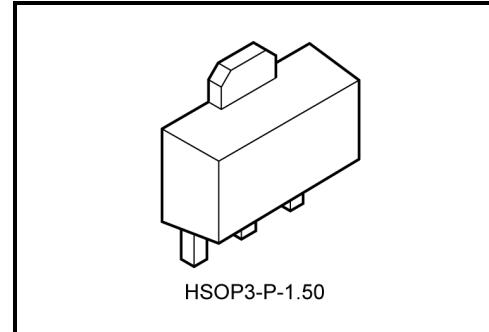
# TA76431F, TA76431FR

Adjustable Precision Shunt Regulator

## Features

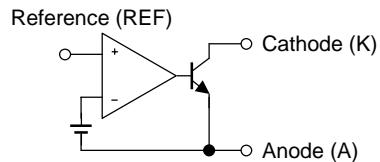
- Precision Reference Voltage:  $V_{REF} = 2.495 \text{ V} \pm 2\%$
- Small Temperature Coefficient:  $|\alpha V_{REF}| = 46 \text{ ppm}/^\circ\text{C}$
- Adjustable Output Voltage:  $V_{REF} \leq V_{OUT} \leq 36 \text{ V}$
- Low Dynamic Output Impedance:  $|Z_{KA}| = 0.15 \Omega \text{ (Typ.)}$
- Small Flat Package
- TA76431FR is a new Toshiba shunt regulator.

This device's pin assignment is the reverse of that of the TA76431F.

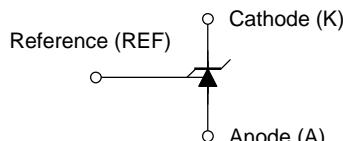


Weight: 0.05 g (typ.)

## Functional Block Diagram



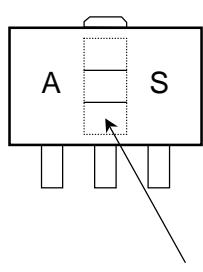
## Circuit Symbol



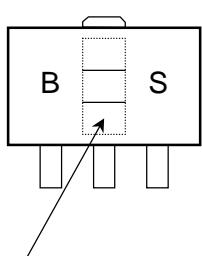
This IC contains electrostatic sensitive elements. Please take care to avoid generating static electricity when handling these devices.

## Marking

(1) TA76431F



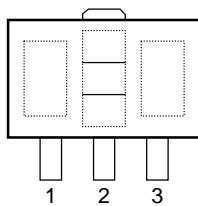
(2) TA76431FR



Lot No. (three-digit number)

The week of manufacture

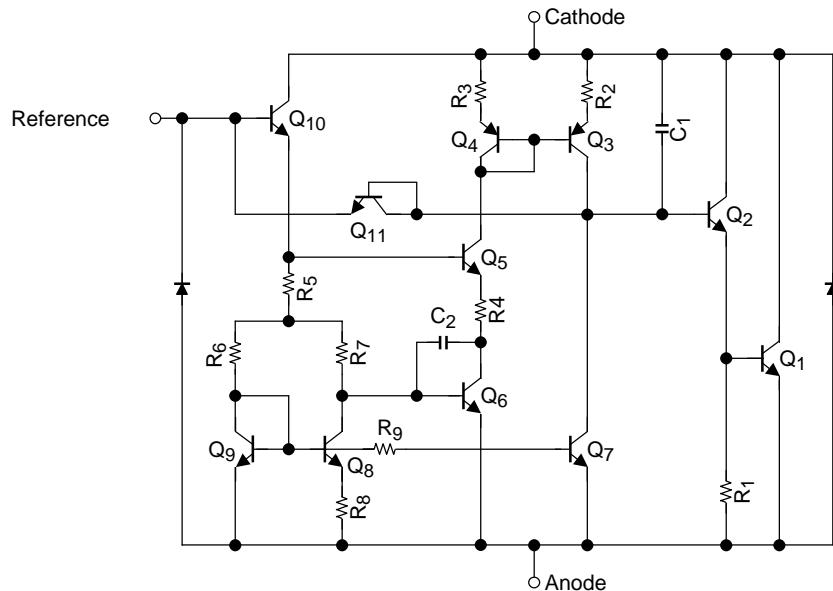
The year of manufacture: last decimal digit of the year of manufacture

**Pin Assignment**

| No. | (1) TA76431F    | (2) TA76431FR   |
|-----|-----------------|-----------------|
| 1   | Cathode (K)     | Reference (REF) |
| 2   | Anode (A)       | Anode (A)       |
| 3   | Reference (REF) | Cathode (K)     |

**How to Order**

| No. | Product No.       | Package Type                          | Packing Type                          | Minimum Order |
|-----|-------------------|---------------------------------------|---------------------------------------|---------------|
| (1) | TA76431F          | PW-MINI (SOT-89) (surface-mount type) | On cut tape (TE12L): 100/tape section | 100           |
|     | TA76431F (TE12L)  |                                       | Embossed tape: 1000/tape              | 1 tape        |
| (2) | TA76431FR         | PW-MINI (SOT-89) (surface-mount type) | On cut tape (TE12L): 100/tape section | 100           |
|     | TA76431FR (TE12L) |                                       | Embossed tape: 1000/tape              | 1 tape        |

**Equivalent Circuit**

**Maximum Ratings (Ta = 25°C)**

| Characteristics                 | Symbol            | Rating      | Unit |
|---------------------------------|-------------------|-------------|------|
| Cathode voltage                 | V <sub>KA</sub>   | 37          | V    |
| Cathode current                 | I <sub>K</sub>    | -100~150    | mA   |
| Reference voltage               | V <sub>REF</sub>  | 7           | V    |
| Reference current               | I <sub>REF</sub>  | 50          | μA   |
| Reference-anode reverse current | -I <sub>REF</sub> | 10          | mA   |
| Power dissipation (Ta = 25°C)   | P <sub>D</sub>    | 500         | mW   |
|                                 |                   | 1000 (Note) |      |
| Operating temperature           | T <sub>opr</sub>  | -40~85      | °C   |
| Storage temperature             | T <sub>stg</sub>  | -55~150     | °C   |

Note: Mounted on ceramic substrate (250 mm<sup>2</sup> × 0.8 mm t)

**Recommended Operating Conditions**

| Characteristics       | Symbol           | Min              | Typ. | Max | Unit |
|-----------------------|------------------|------------------|------|-----|------|
| Cathode voltage       | V <sub>KA</sub>  | V <sub>REF</sub> | —    | 36  | V    |
| Cathode current       | I <sub>K</sub>   | 1                | —    | 100 | mA   |
| Operating temperature | T <sub>opr</sub> | -40              | —    | 85  | °C   |

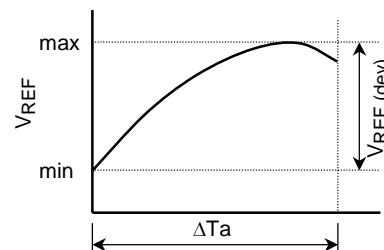
**Electrical Characteristics (Unless otherwise specified, Ta = 25°C, I<sub>K</sub> = 10 mA)**

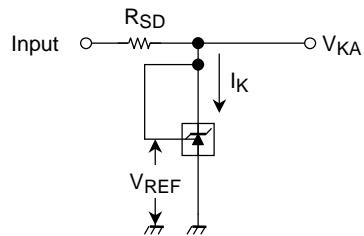
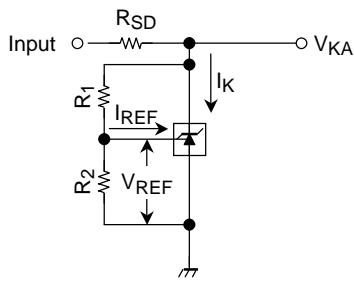
| Characteristics                                                             | Symbol                 | Test Condition                                                                                   | Min   | Typ.  | Max   | Unit |
|-----------------------------------------------------------------------------|------------------------|--------------------------------------------------------------------------------------------------|-------|-------|-------|------|
| Reference voltage                                                           | V <sub>REF</sub>       | V <sub>KA</sub> = V <sub>REF</sub>                                                               | 2.440 | 2.495 | 2.550 | V    |
| Deviation of reference input voltage over temperature                       | V <sub>REF</sub> (dev) | 0°C ≤ Ta ≤ 70°C, V <sub>KA</sub> = V <sub>REF</sub>                                              | —     | 8     | 17    | mV   |
| Ratio of change in reference input voltage to the change in cathode voltage | ΔV <sub>REF</sub> /ΔV  | V <sub>REF</sub> ≤ V <sub>KA</sub> ≤ 10 V                                                        | —     | 0.8   | 2.7   | mV/V |
|                                                                             |                        | 10 V ≤ V <sub>KA</sub> ≤ 36 V                                                                    | —     | 0.5   | 2.0   |      |
| Reference Input current                                                     | I <sub>REF</sub>       | V <sub>KA</sub> = V <sub>REF</sub>                                                               | —     | 1.4   | 4     | μA   |
| Deviation of reference input current over temperature                       | I <sub>REF</sub> (dev) | 0°C ≤ Ta ≤ 70°C, V <sub>KA</sub> = V <sub>REF</sub> , R <sub>1</sub> = 10 kΩ, R <sub>2</sub> = ∞ | —     | 0.3   | 1.2   | μA   |
| Minimum cathode current for regulation                                      | I <sub>Kmin</sub>      | V <sub>KA</sub> = V <sub>REF</sub>                                                               | —     | 0.4   | 1.0   | mA   |
| Off-State cathode current                                                   | I <sub>Koff</sub>      | V <sub>KA</sub> = 36 V, V <sub>REF</sub> = 0 V                                                   | —     | —     | 1.0   | μA   |
| Dynamic impedance                                                           | Z <sub>KA</sub>        | V <sub>KA</sub> = V <sub>REF</sub> , f ≤ 1 kHz, 1 mA ≤ I <sub>K</sub> ≤ 100 mA                   | —     | 0.15  | 0.5   | Ω    |

The deviation parameters V<sub>REF</sub> (dev) and I<sub>REF</sub> (dev) are defined as the maximum variation of the V<sub>REF</sub> and I<sub>REF</sub> over the rated temperature range.

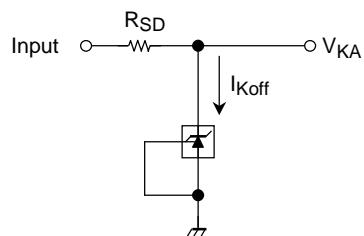
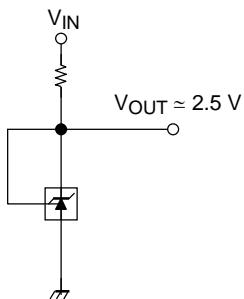
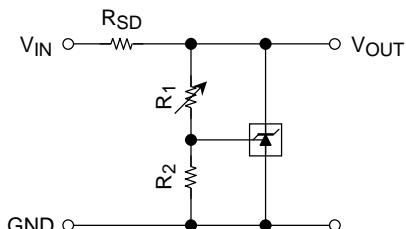
The average temperature coefficient of the V<sub>REF</sub> is defined as:

$$|\alpha V_{REF}| = \frac{\left( \frac{V_{REF}(\text{dev})}{V_{REF} @ 25^\circ\text{C}} \right) \times 10^6}{\Delta T_a} (\text{ppm}/^\circ\text{C})$$

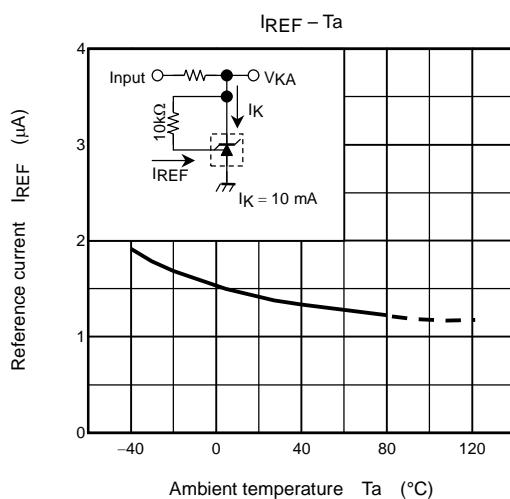
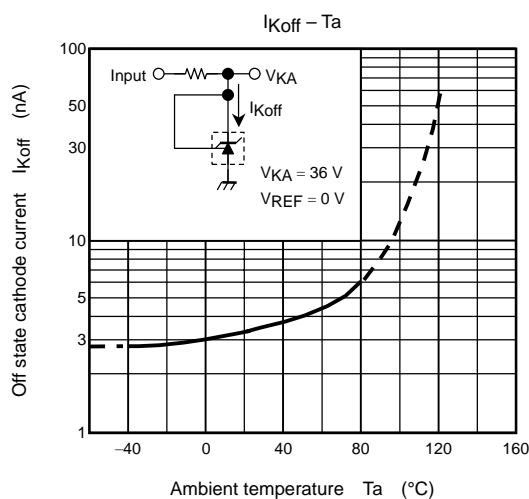
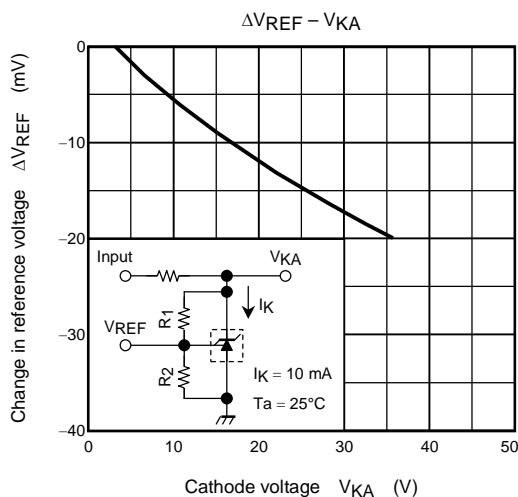
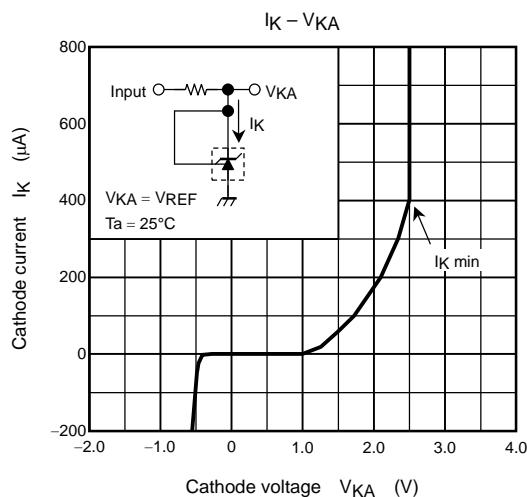
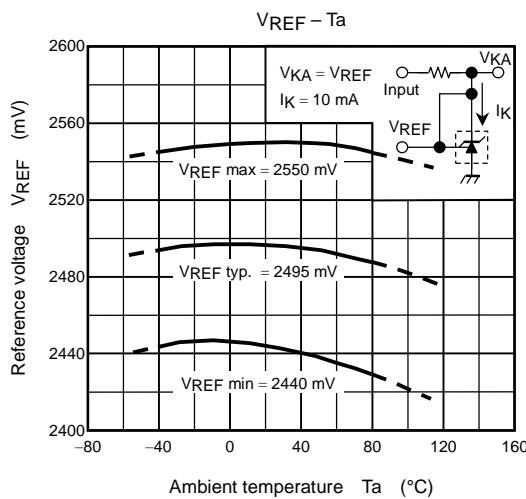
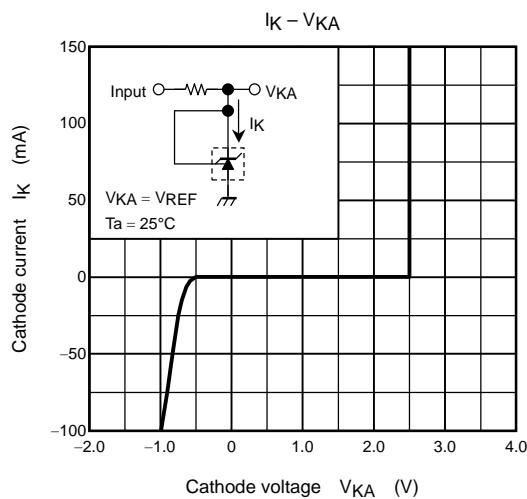


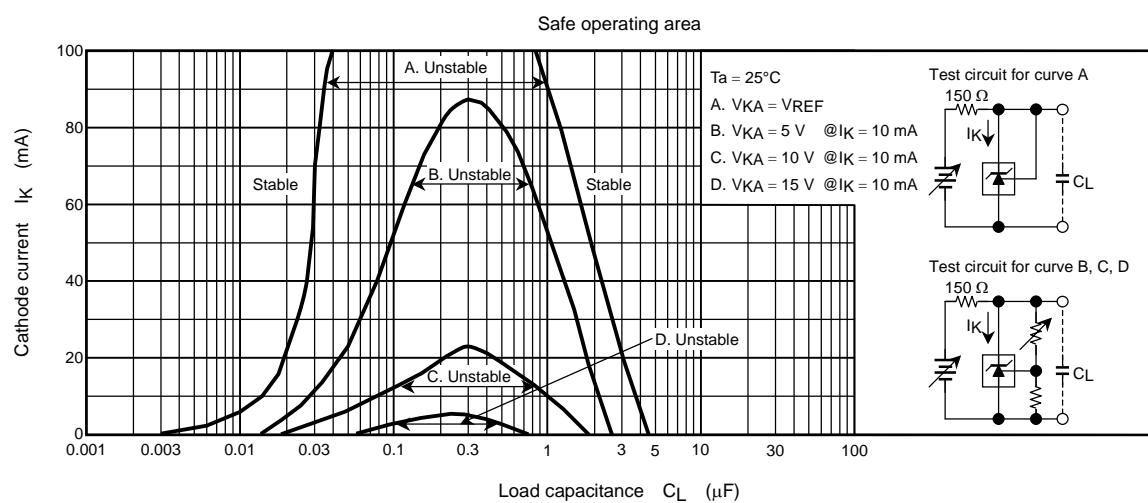
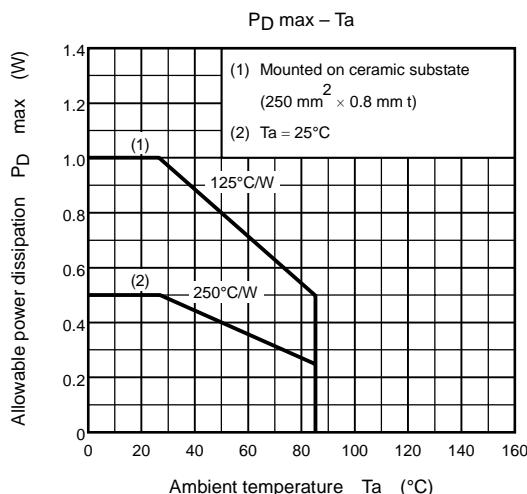
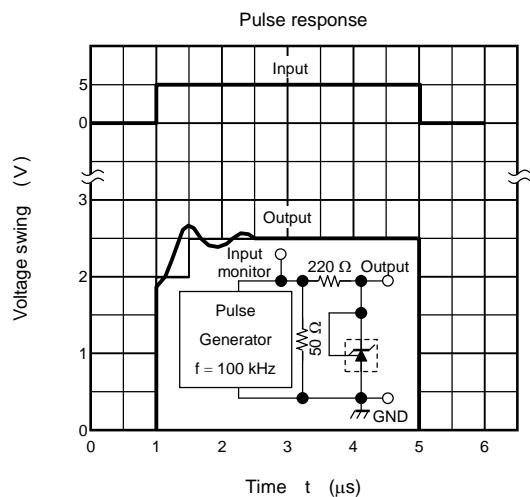
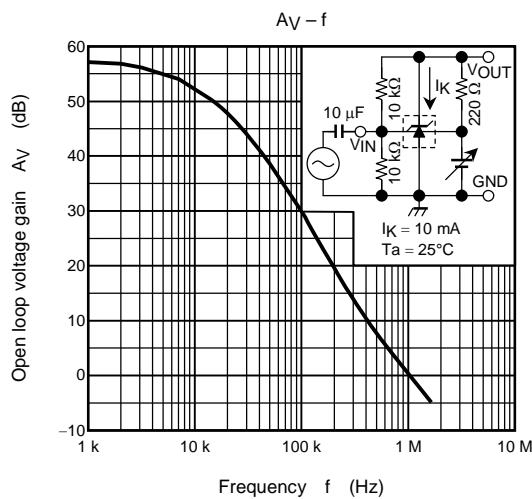
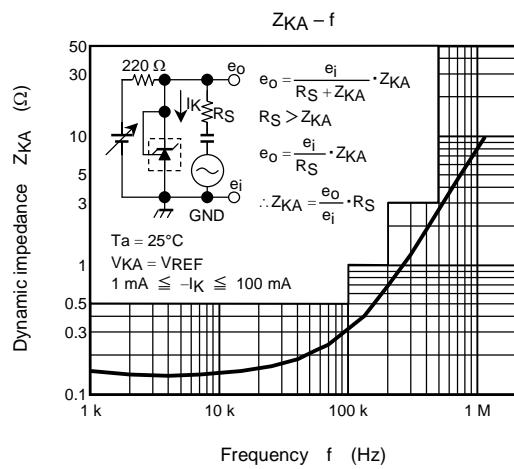
**Test Parameter****(1)  $V_{KA} = V_{REF}$  Mode****(2)  $V_{KA} > V_{REF}$  Mode**

$$V_{KA} = V_{REF} \left( 1 + \frac{R_1}{R_2} \right) + I_{REF} \cdot R_1$$

**(3) OFF-State Mode****Typical Application Circuits****(1) 2.5 V Reference****(2) Shunt Regulator**

$$V_{OUT} = V_{REF} \left( 1 + \frac{R_1}{R_2} \right) + I_{REF} \cdot R_1$$

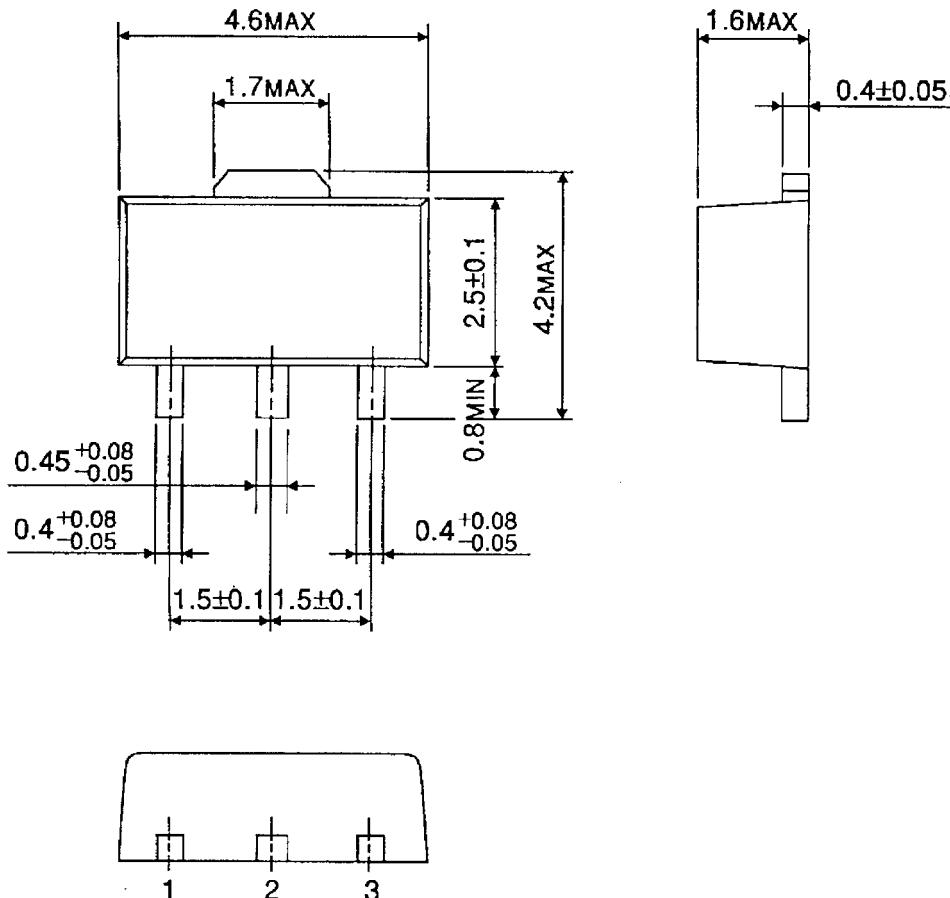




**Package Dimensions**

HSOP3-P-1.50

Unit : mm



Weight: 0.05 g (typ.)

## RESTRICTIONS ON PRODUCT USE

000707EBA

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