TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

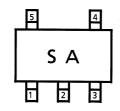
# **TA75S01F**

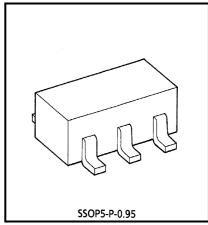
### Single Operational Amplifier

#### **Features**

- In the linear mode the input common mode voltage range includes ground.
- The internally compensated Operational Amplifier is small package.
- Low power dissipation and power drain suitable for battery operation.
- Differential input voltage range equal to the power supply voltage.
- Large output voltage swing: 0VDC to 3.4VDC (VDC = 5VDC)
- Wide power supply voltage range and single power supply is possible.
- Single supply  $3V_{DC}$  to  $12V_{DC}$  or dual supplies  $\pm 1.5V_{DC}$  to  $\pm 6V_{DC}$ .

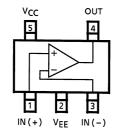
### Marking (Top View)





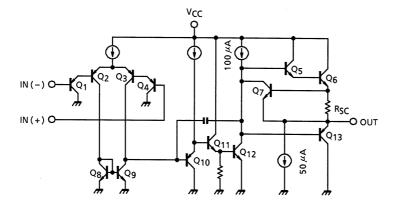
Weight: 0.014g (typ.)

### **Pin Connection (Top View)**



TA75S01F

## **Equivalent Circuit**



## **Maximum Ratings (Ta = 25°C)**

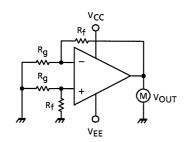
Characterisstic	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub> , V <sub>EE</sub>	±6 or 12	V	
Differential input voltage	DV <sub>IN</sub>	±12	٧	
Input voltage	V <sub>IN</sub>	-0.3~ V <sub>CC</sub>	٧	
Power dissipation	P <sub>D</sub>	200	mW	
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Storage temperature	T <sub>stg</sub>	-55~125	°C	

## Electrical Characteristics ( $V_{CC} = 5E$ , $V_{EE} = GND$ , Ta = 25°C)

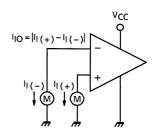
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V <sub>IO</sub>	1	Rg≤10kΩ	_	2	7	mV
Input offset current	I <sub>IO</sub>	2	_	_	5	50	nA
Input bias current	IĮ	2	_	_	45	250	nA
Common mode input voltage	CMV <sub>IN</sub>	3	_	0	_	V <sub>CC</sub> -1.5	V
Supply current	Icc	4	_	_	0.4	0.8	mA
Voltage gain	G <sub>V</sub>	_	RL≥2kΩ	86	100	_	dB
Maximum output voltage swing	V <sub>op-p</sub>	5	RL = 2kΩ	0	_	3.4	V
Common mode rejection ratio	CMRR	3	_	65	85	_	dB
Supply voltage rejection ratio	SVRR	_	Rg = 10kΩ	65	100	_	dB
Source current	I <sub>source</sub>	6	IN (-) = 0V, IN (+) = 1V	20	40	_	mA
Sink current	I <sub>sink</sub>	7	IN (-) = 1V, IN (+) = 0V	10	20	_	mA
Unity gain cross frequency	fΤ	_	_	_	0.3	_	MHz

## **Test Circuit**

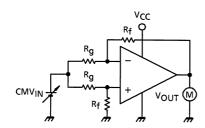
## (1) V<sub>IO</sub>



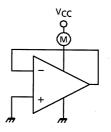
(2) I<sub>I</sub>, I<sub>IO</sub>



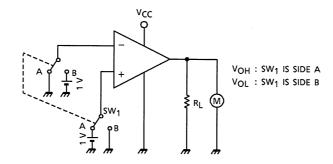
## (3) CMV<sub>IN</sub>, CMRR



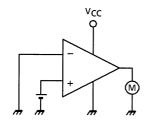
(4) I<sub>CC</sub>



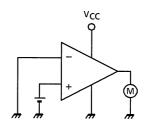
## (5) V<sub>OP-P</sub>

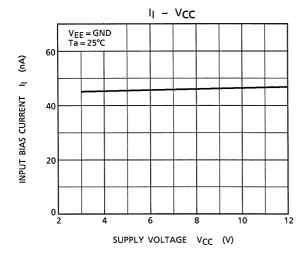


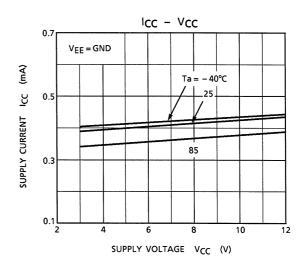
(6) I<sub>source</sub>

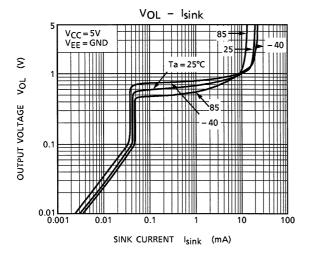


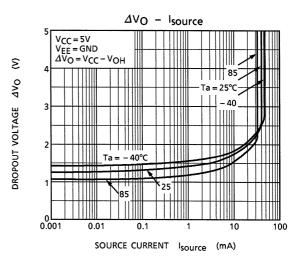
(7) I<sub>sink</sub>

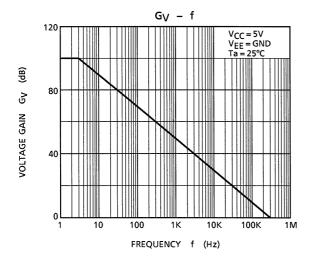


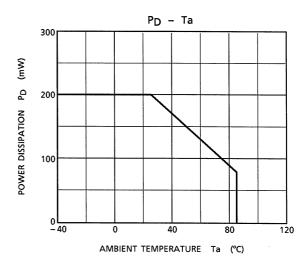






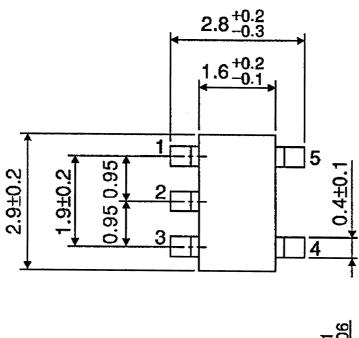


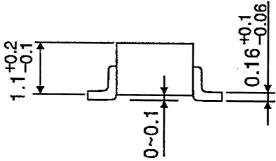




## **Package Dimensions**

SSOP5-P-0.95





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Weight: 0.014g (typ.)

2001-06-13

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