TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA7257P

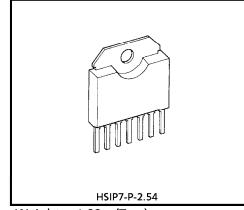
BRIDGE DRIVER

The TA7257P is a Full Bridge Driver for blashed DC Motor Rotation control.

Forward Rotation, Reverse Rotation, Stop and Braking operations are available.

It's designed for Loading and Reel Motor driver for VTR and Tape Deck, and any other consumer and industrial applications.

TA7257P have Operation Supply Voltage terminal and Motor Driving Supply Voltage terminal independently therefore Servo control operation is applicable.

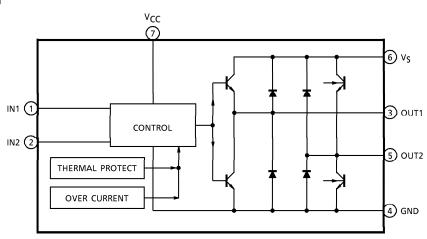


Weight: 1.88g (Typ.)

FEATURES

- Output Current Up to 1.5A (AVE.), and 4.5A (PEAK).
- 4 Function Modes (CW, CCW, STOP and Brake) are Controlled by 2 Logic Signals Fed Into 2 Input Terminals.
- Build in Over Current Protector and Thermal Shut Down Circuit.
- Operating Voltage Range : $V_{CC(opr.)} = 6 \sim 18V$, $V_{S(opr.)} = 0 \sim 18V$

BLOCK DIAGRAM



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PIN FUNCTION

PIN No.	SYMBOL	FUNCTIONAL DESCRIPTION		
1	IN1	Input terminal		
2	IN2	Input terminal		
3	OUT1	Output terminal		
4	GND	GND terminal		
5	OUT2	Output terminal		
6	٧ _S	Supply voltage terminal for Motor drive		
7	V _C C	Supply voltage terminal for Logic		

FUNCTION

IN1	IN2	OUT1	OUT2	MODE		
1	1	L	L	Brake		
0	1	L	Н	CW/CCW		
1	0	Н	L	ccw/cw		
0	0			Stop		

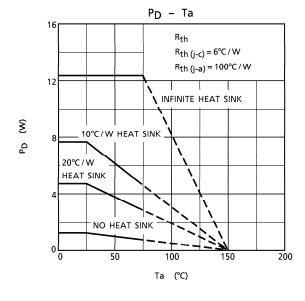
MAXIMUM RATINGS (Ta = 25°C)

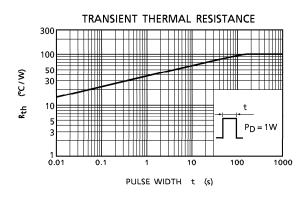
CHARACTERIST	TC TC	SYMBOL	RATING	UNIT	
Dook Supply Voltag	Peak	V _{CC} (MAX.)	25	V	
Peak Supply Voltage	Operate V _{CC} (opr.) 18		18	'	
Output Command	PEAK	IO (PEAK)	4.5	Α	
Output Current	AVE.	IO (AVE.)	1.5	A	
Power Dissipation		PD	12.5 (Note)	W	
Operating Temperatu	re	T _{opr}	– 30∼75	°C	
Storage Temperature		T _{stg}	- 55∼150	°C	

(Note) $Tc = 75^{\circ}C$

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current		lcc1	_	V _{CC} = 18V Output OFF stop mode	_	6.5	13	- mA
		I _{CC2}		V _{CC} = 18V Output OFF CW/CCW mode	_	10	20	
Saturation Voltage	Upper	V _{S1} U	_	V _{CC} = 18V, I _O = 0.1A	_	0.7	1.0	V
	Lower	V _{S1} L			_	0.6	0.9	
	Upper	V _{S2} U		V 10V L 11A	_	1.0	1.4	
	Lower	V _{S2} L		$V_{CC} = 18V, I_{O} = 1.1A$	_	0.9	1.3	
Output Transistor	Upper	IL U		V _S = 18V	_		100	μΑ
Leakage Current	Lower	IL L		\sqrt{5} = 18\sqrt{1}	_		100	
Input Voltage 1, 2		V _{IN} (H)		$T_j = 25$ °C, pin① and pin②	3.0	_	_	V
		V _{IN} (L)	_		_	_	0.8	
Diode Forward Voltage		V _F U		I= - 1 0 A	_	2.0	_	V
		V _F L		I _F = 1.0A		1.25	_	
Limiting Current		Isc	_	_	_	3.5	_	Α
Input Current		IN	_	_	_	1	10	μ A



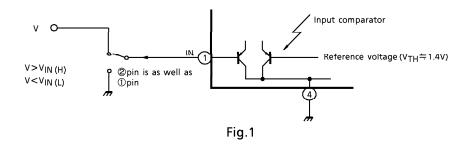


APPLICATION NOTE

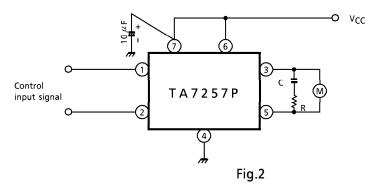
(1) Input circuit

Input circuit is shown in Fig.1. It's a "Low active" type voltage comparator that's one input connect to Input terminal (pin1, or 2) and the other to built-in temperature compensated voltage reference (V_{TH} = 1.4V Typ.)

If a voltage above $V_{IN\,(H)}$ fed into the Input Terminal that means "Logic 1" and less than $V_{IN\,(L)}$ or connect to GND means "Logic 0".



(2) Basic application circuit



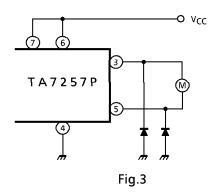
(Note) Fig.2 shows the basic application circuit.

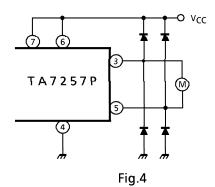
Optimum values of the C, R depend on the inherent constant of a motor and parasitic C, R values around the circuit.

Normally, recommended to use $0.1\mu F$ and 33Ω .

(3) Additional diode

- i) If the braking operation is so loose, connect a additional diode between each output to GND, (See Fig.3)
- ii) If the back electromotive pulse generated in output coil is so strong.
 Internally connected back electromotive suppression diode may be damaged by this pulse.
 In such a case connect a additional diode between each output to V_{CC}. (See Fig.4)

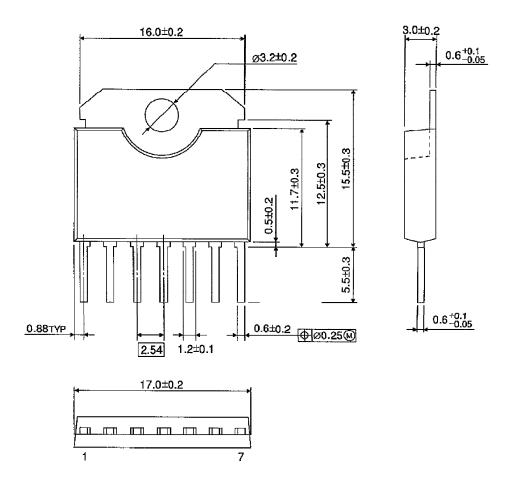




(Note) Utmost care is necessary in the design of the output line, V_S and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

OUTLINE DRAWING

HSIP7-P-2.54 Unit: mm



Weight: 1.88g (Typ.)