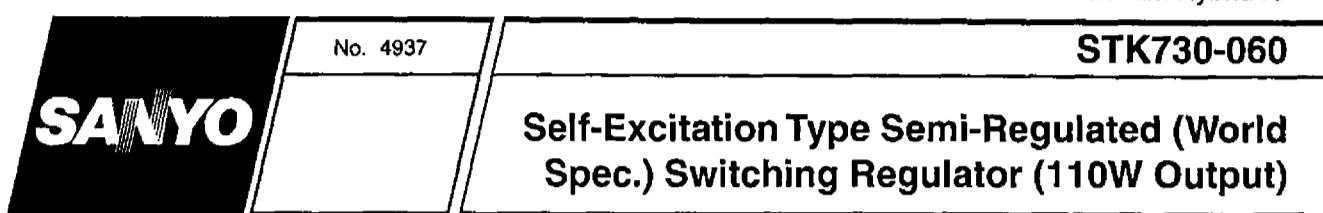


Ordering number: EN 4937

Thick Film Hybrid IC



## Overview

The STK730-060 incorporates on-chip all the power switching, amplifier, error detection and overcurrent protection circuits required in a self-excitation type semi-regulated off-line switching regulator. As a result, it can be used in the design of switching power supplies with minimal number of external components. Furthermore, the adoption of MOSFET power switching elements supports a higher oscillator frequency than that possible with bipolar transistors. This allows smaller pulse transformers and capacitors to be used, making it possible to construct miniature power supply systems.

## Applications

- CRT/CTV power supplies
- Office automation equipment power supplies

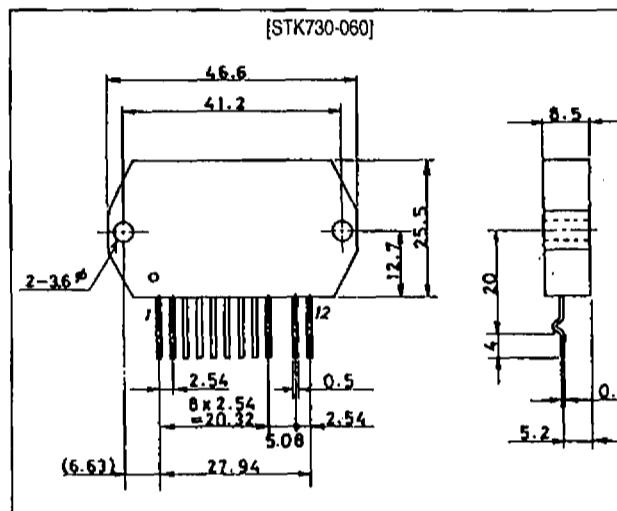
## Features

- Power MOSFET devices
- Ideal for semi-regulated control switching supplies
- Error detection circuit on-chip ( $40.5 \pm 0.5$ V set reference voltage)
- Overcurrent protection circuit on-chip
- Pin compatible with all other devices in the same series of devices with 110 to 280W power ratings
- Higher oscillator frequency allows the use of smaller pulse transformers
- IMST substrate acts as an electromagnetic shield, making low-noise designs possible

## Package Dimensions

unit: mm

4121



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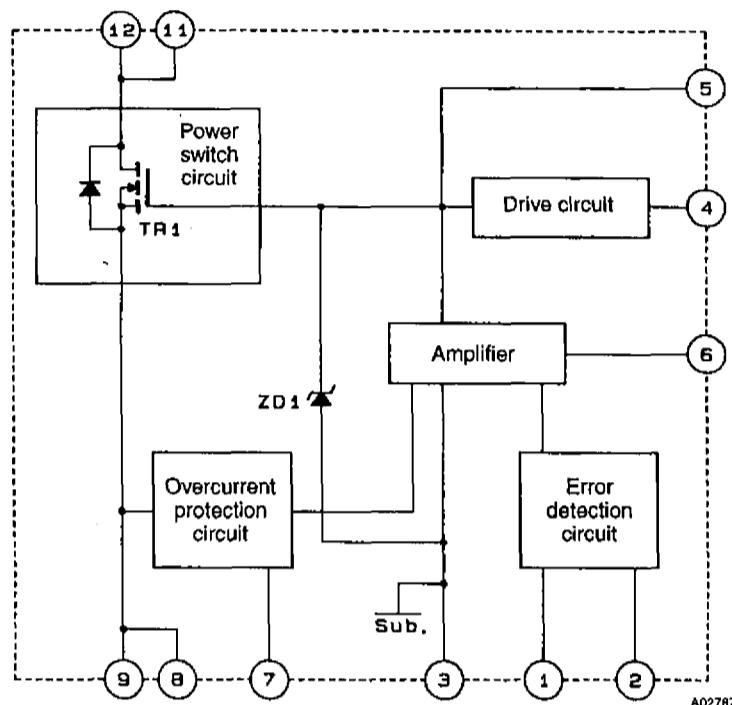
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- ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use;
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SANYO Electric Co., Ltd. Semiconductor Business Headquarters

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

**Block Diagram**

The back surface of the IC is not an insulator, and is effectively at pin 3 potential.

**Pin Functions**

Number	Function
1	$V_{rel}$ (40.5V typ) input
2	Error detection level
3	Ground
4	Drive voltage input
5	TR1 gate
6	Amplifier circuit control
7	OCP setting level input
8	TR1 source
9	
11	
12	TR1 drain

**Specifications**

**Maximum Ratings** at  $T_a = 25^\circ\text{C}$ ,  $T_c = 25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Conditions	Ratings	Unit
Operating substrate temperature	$T_c$ max	Recommended value is $105^\circ\text{C}$ .	115	$^\circ\text{C}$
AC input voltage	$V_{AC}$	Specified test circuit	280	Vrms
Operating temperature	$T_{opp}$		-10 to +85	$^\circ\text{C}$
Storage temperature	$T_{stg}$		-30 to +115	$^\circ\text{C}$
Maximum output power	$W_o$ max	Specified test circuit, $V_O = 135\text{V}$	110	W

## STK730-060

Parameter	Symbol	Conditions	Ratings	Unit
<b>[TR1]</b>				
Drain current	$I_D$		3	A
Pulse drain current	$I_{D(\text{pulse})}$	Refer to ASO characteristics for overcurrent condition.	7	A
Drain reverse current	$I_{DR}$		3	A
Gate-source voltage	$V_{GSS}$		$\pm 30$	V
Allowable power dissipation	$P_D$		78.1	W
Chip junction temperature	$T_j \text{ max}$		150	$^{\circ}\text{C}$
Thermal resistance	$\theta_{j-c}$		1.6	$^{\circ}\text{C}/\text{W}$
<b>[ZD1]</b>				
Allowable power dissipation	$P_{ZD1}$		500	mW
Chip junction temperature	$T_j(ZD1) \text{ max}$		125	$^{\circ}\text{C}$
Thermal resistance	$\theta_{j-c}(ZD1)$		0.2	$^{\circ}\text{C}/\text{mW}$

**Allowable Operating Ranges** at  $T_a = 25^{\circ}\text{C}$

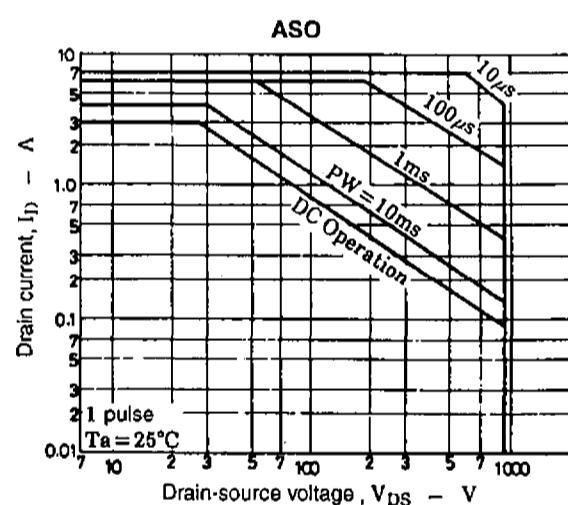
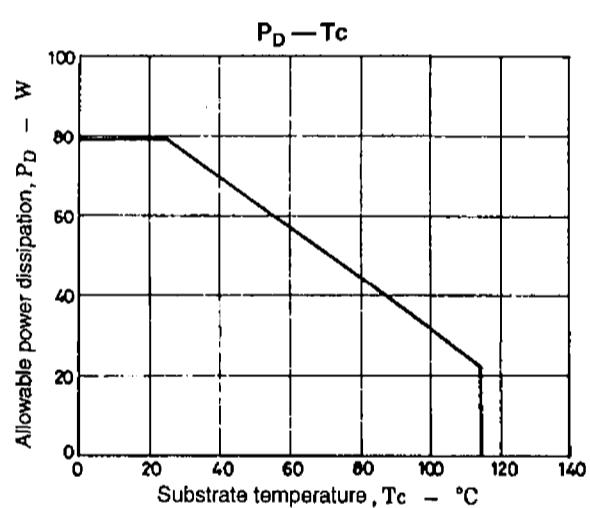
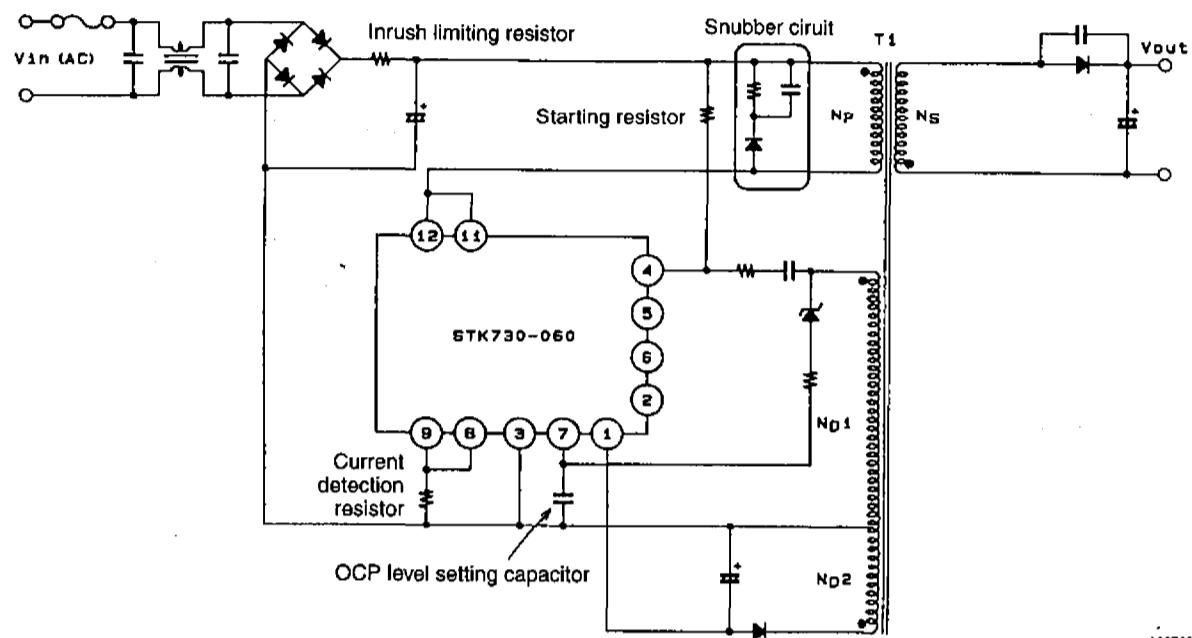
Parameter	Symbol	Conditions	Ratings	Unit
Pin 4 input voltage	$V_4$		$\pm 8 \text{ to } \pm 24$	V
Oscillator frequency	$f_{osc}$		20 to 120	kHz

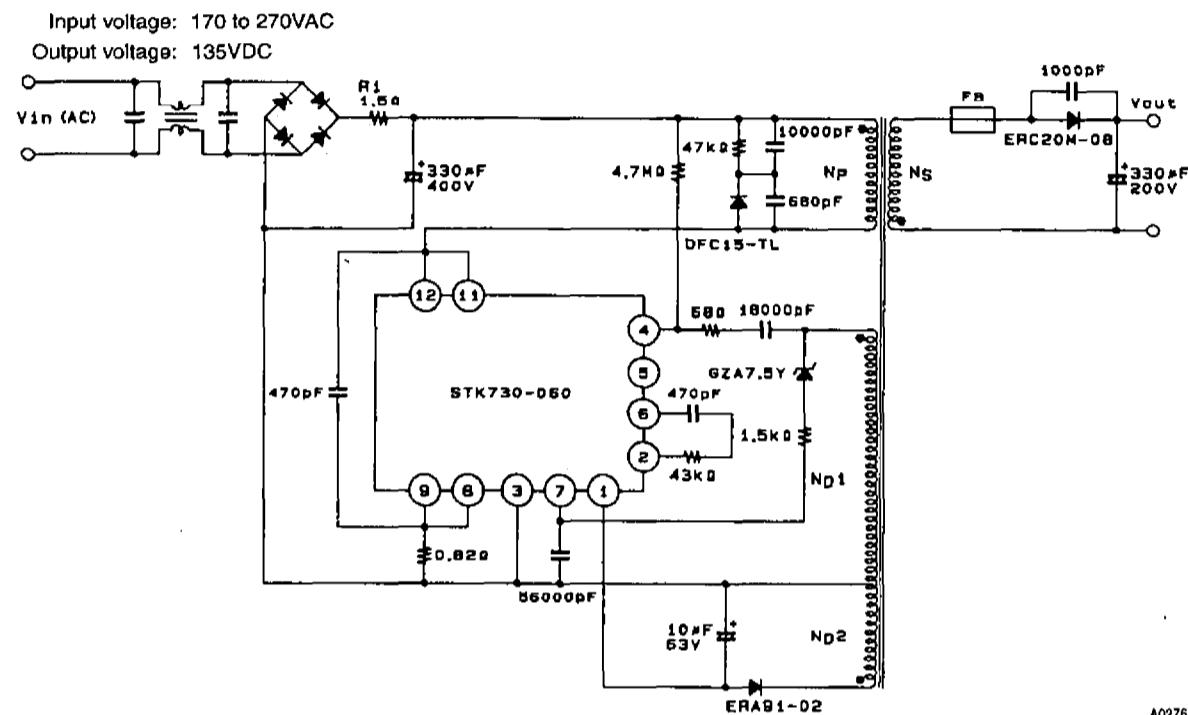
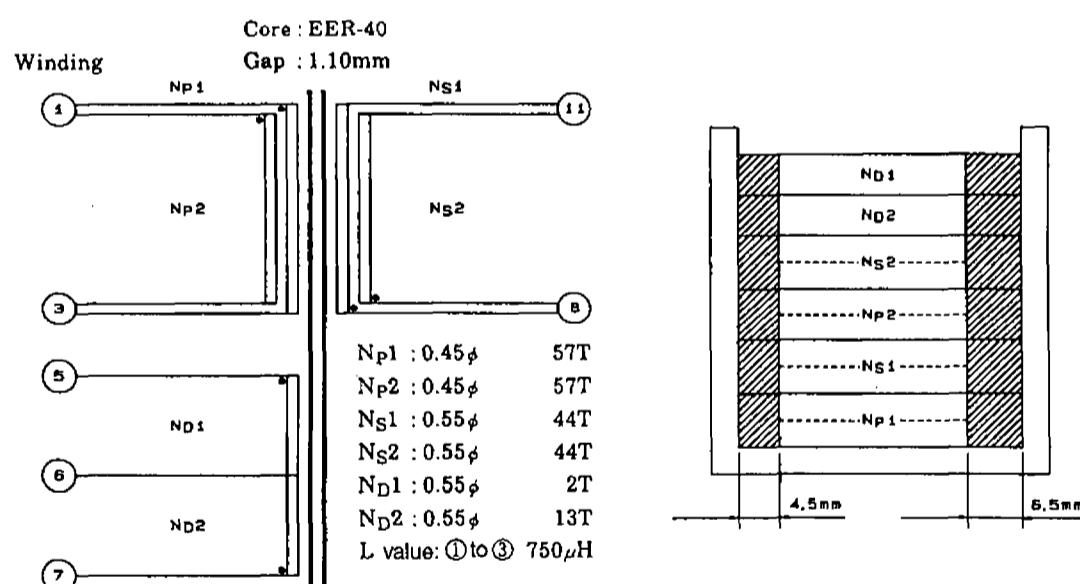
**Operating Characteristics** at  $T_a = 25^{\circ}\text{C}$ ,  $T_c = 25^{\circ}\text{C}$  unless otherwise specified, specified test circuit

Parameter	Symbol	Conditions	min	typ	max	Unit
Output voltage setting		$I_{IN} = 8\text{mA}$	40.0	40.5	41.0	V
Output voltage temperature coefficient		$T_c = 0 \text{ to } 105^{\circ}\text{C}$ , $I_{IN} = 8\text{mA}$	-	7	-	$\text{mV}/^{\circ}\text{C}$
<b>[TR1]</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = 10\text{mA}$ , $V_{GS} = 0\text{V}$	900	-	-	V
Gate-source cutoff voltage	$V_{GS(\text{off})}$	$I_D \approx 1\text{mA}$ , $V_{DS} = 10\text{V}$	2.0	-	3.0	V
ON resistance	$R_{DS(on)}$	$I_D = 1.5\text{A}$ , $V_{GS} = 10\text{V}$	-	5.0	7.0	$\Omega$
Input capacitance	$C_{iss}$	$V_{DS} = 10\text{V}$ , $V_{GS} = 0\text{V}$ , $f = 1\text{MHz}$	-	500	-	$\text{pF}$
<b>[ZD1]</b>						
Zener voltage	$V_z$	$I_z = 5\text{mA}$	23.7	-	26.3	V

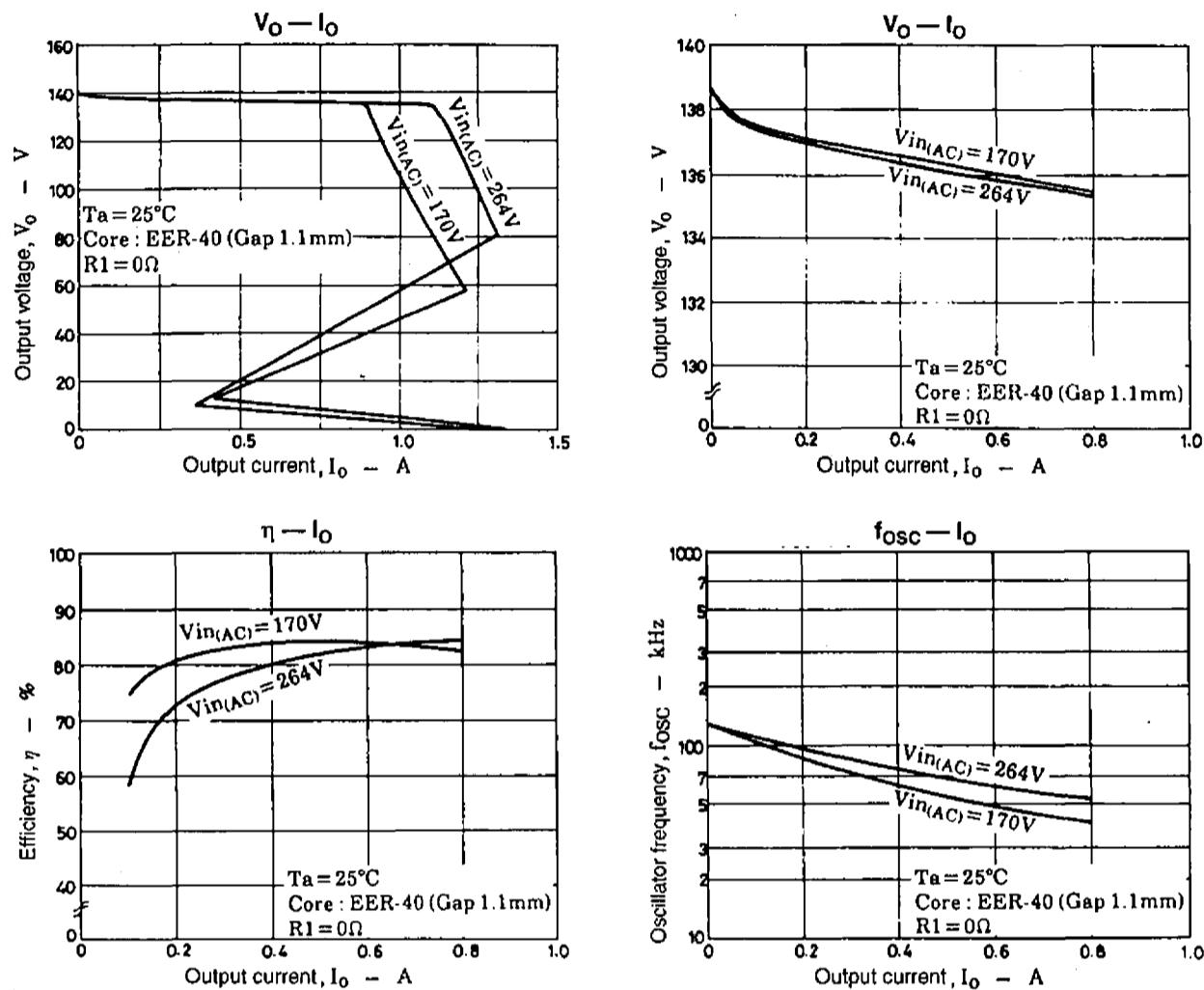
**STK730-060**

**Circuit Function Diagram**

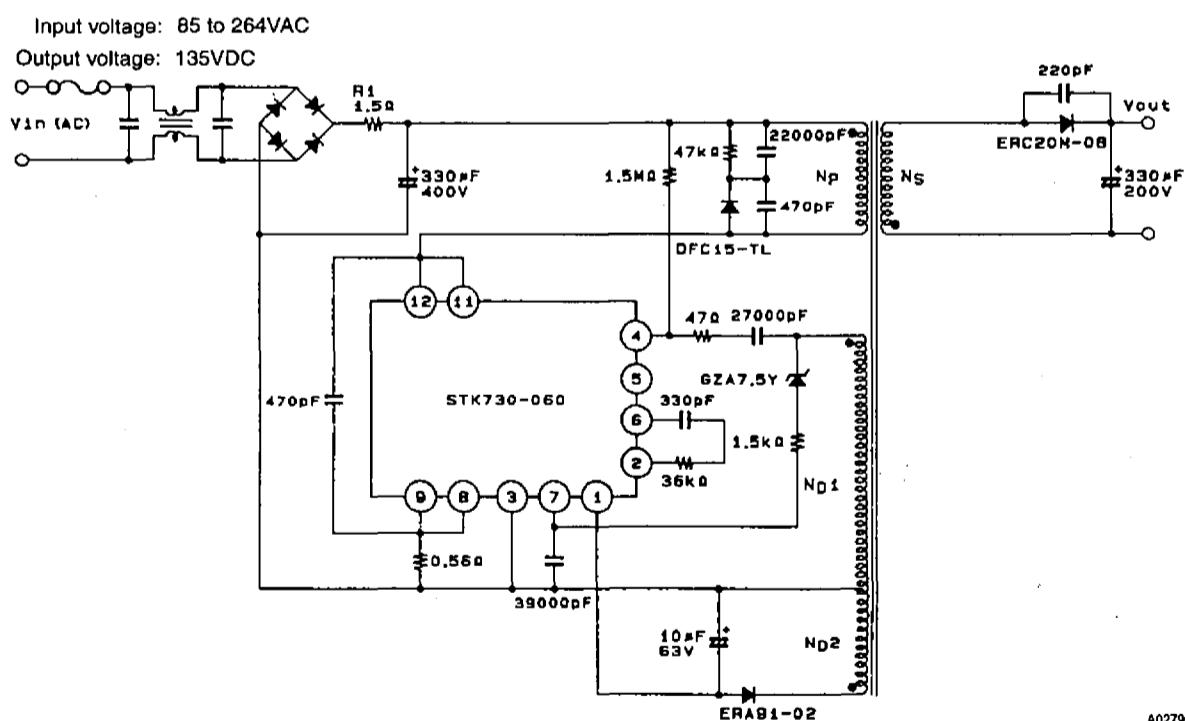


**Sample Application Circuit (200V System)****Pulse Transformer Specifications**

## STK730-060



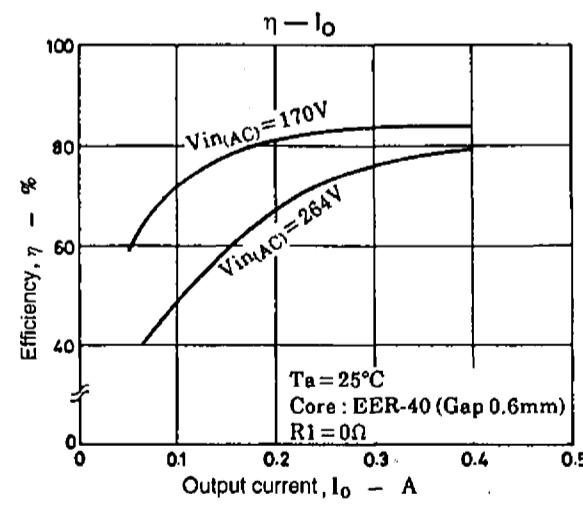
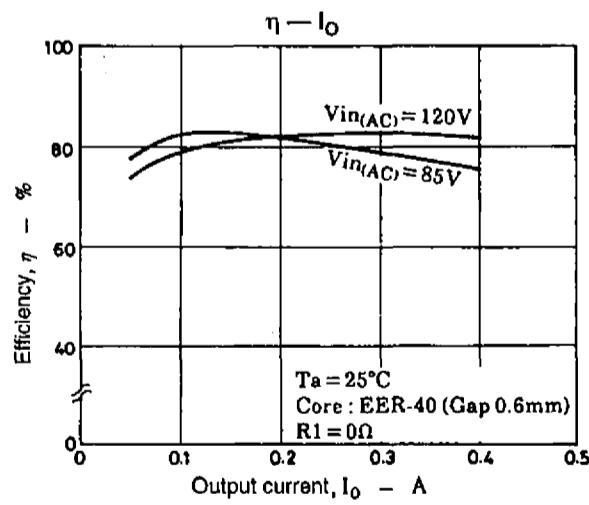
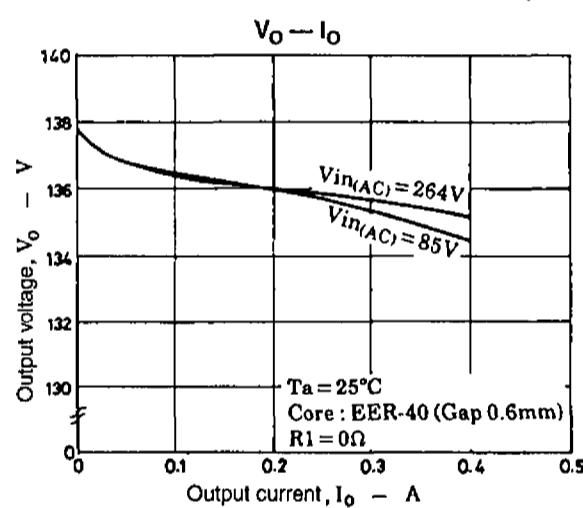
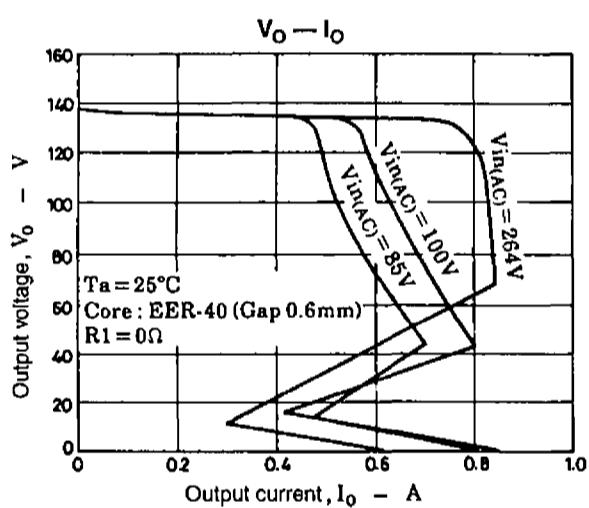
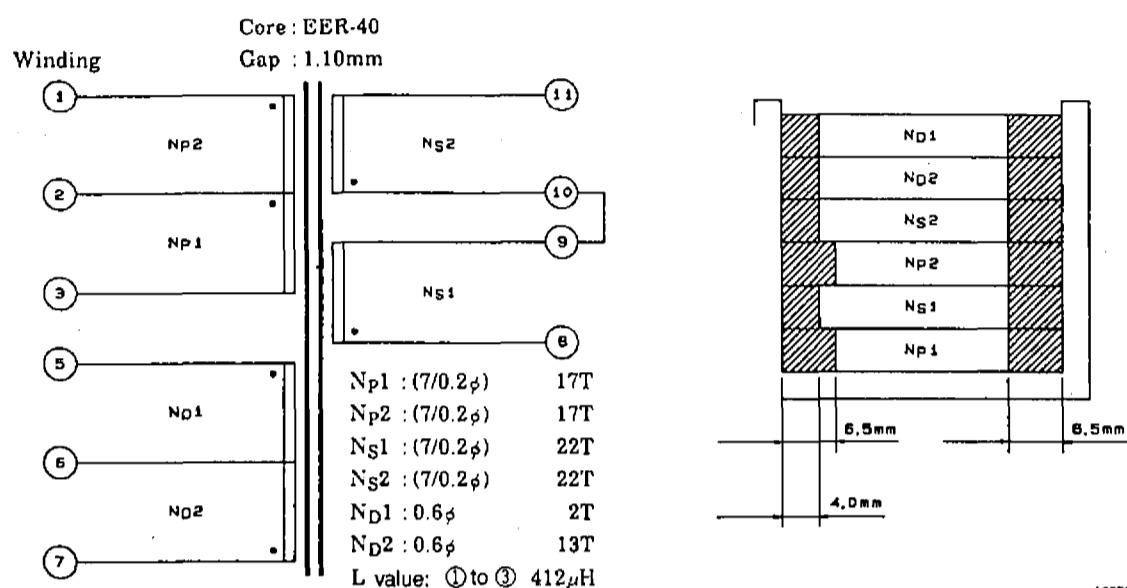
### Sample Application Circuit (World Input System)



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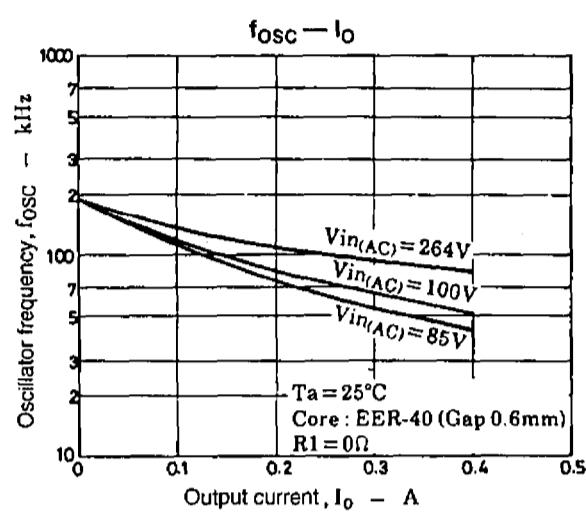
No. 4937-6/8

## Pulse Transformer Specifications



## STK730-060

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### Series Organization

These devices form a series with varying output power ratings.

Device	Maximum ratings					Operating characteristics		
	V <sub>DSS</sub> [V]	T <sub>stg</sub> [°C]	T <sub>c max</sub> [°C]	T <sub>j max</sub> [°C]	I <sub>p</sub> [A]	Input voltage [V]	Output power [W]	ON resistance [Ω]
STK730-010	500	-30 to +115	+115	+150	6.0	85 to 132	110	1.4
STK730-020					8.0		145	0.8
STK730-030					10.0		180	0.7
STK730-040					12.0		210	0.55
STK730-050					15.0		280	0.3
STK730-060	900				3.0	170 to 264	110	5.0
STK730-070					5.0		180	3.0
STK730-080					6.0		210	2.0
STK730-090					8.0		280	1.2