- Buffer Version of 'ALS20B
- Package Options include Plastic Small Outline Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

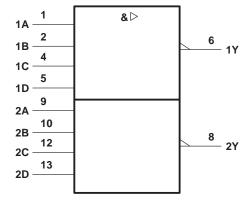
These devices contain two independent 4-input NAND buffers. They perform the Boolean functions $Y = \overline{A \bullet B \bullet C \bullet B}$ or $Y = \overline{A + B + \overline{C} + \overline{D}}$ positive logic.

The SN54ALS1020A is characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS1020A is characterized for operation from 0°C to 70°C.

FUNCTION TABLE (each gate)

	INP	OUTPUT		
Α	В	С	D	Υ
Н	Н	Н	Н	L
L	Χ	Χ	Χ	Н
Х	L	Χ	Χ	Н
Х	X	L	Χ	Н
Х	X	X	L	Н

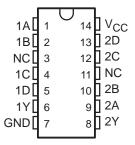
logic symbol†



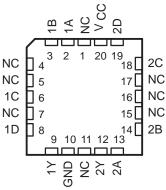
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

SN54ALS1020A . . . J PACKAGE SN74ALS1020A . . . D OR N PACKAGE (TOP VIEW)

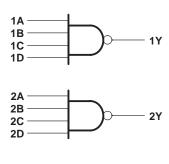


SN54ALS1020A . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

logic diagram (positive logic)



SN54ALS1020A, SN74ALS1020A DUAL 4-INPUT POSITIVE-NAND BUFFERS

SDAS 242 - D2661, APRIL 1982 - REVISED MAY 1986

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC}		 7 V
Input voltage		 7 V
Operating free-air temperature range:		
	SN74ALS1020A	 0°C to 70°C
Storage temperature range		 -65°C to 150°C

recommended operating conditions

		SN54ALS1020A		SN74ALS1020A			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
٧ _{IH}	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
IOH	High-level output current			-1			-2.6	mA
lOL	Low-level output current			12			24	mA
TA	Operating free-air temperature	-55	•	125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN54	SN54ALS1020A			SN74ALS1020A		
			MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	UNIT
V_{IK}	$V_{CC} = 4.5 \text{ V},$	$I_{I} = -18 \text{ mA}$			-1.5			-1.5	V
	$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2			V _{CC} -2			
VOH	V _{CC} = 4.5 V,	I _{OH} = -1 mA	2.4	3.3					V
	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -2.6 \text{ mA}$				2.4	3.3		
	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 12 \text{ mA}$		0.25	0.4		0.25	0.4	V
V _{OL}	V _{CC} = 4.5 V,	$I_{OL} = 24 \text{ mA}$					0.35	0.5	٧
ΙΙ	V _{CC} = 5.5 V,	V _I = 7 V			0.1			0.1	mA
lіН	V _{CC} = 5.5 V,	V _I = 2.7 V			20			20	μΑ
IIL	V _{CC} = 5.5 V,	V _I = 0.4 V			-0.1			-0.1	mA
IO [‡]	V _{CC} = 5.5 V,	V _O = 2.25 V	-30		-112	-30		-112	mA
ICCH	V _{CC} = 5.5 V,	V _I = 0		0.5	0.8		0.5	0.8	mA
^I CCL	$V_{CC} = 5.5 \text{ V},$	V _I = 4.5 V		2.4	3.9		2.4	3.9	mA

[†] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	$V_{CC} = 5 \text{ V},$ $C_L = 50 \text{ pF},$ $R_L = 500 \Omega,$ $T_A = 25^{\circ}\text{C}$ 'ALS1020A TYP	SN54AL MIN	C _L = 50 R _L = 50 T _A = M	-		UNIT
^t PLH	Any		5	2	10	2	8	ns
t _{PHL}	Ally	1	5	2	10	2	7	115

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.



[‡] The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, los.

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