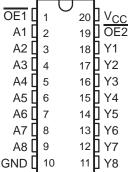
SCBS031B - FEBRUARY 1989 - REVISED SEPTEMBER 1994

- BiCMOS Design Significantly Reduces Standby Current
- 3-State True Outputs Drive Bus Lines or Buffer Memory-Address Registers
- High-Impedance State During Power Up and Power Down
- P-N-P Inputs Reduce DC Loading
- Data Flow-Through Pinout (All Inputs on Opposite Side From Outputs)
- ESD Protection Exceeds 2000 V Per MIL-STD-883C. Method 3015
- Package Options Include Plastic Small-Outline (DW) Packages and Standard Plastic (N) 300-mil DIPs

DW OR N PACKAGE (TOP VIEW)



description

The SN64BCT541A octal buffer and line driver is ideal for driving bus lines or buffering memory-address registers. The device features inputs and outputs on opposite sides of the package to facilitate printed-circuit-board layout.

The 3-state control gate is a 2-input AND gate with active-low inputs so that if either output-enable (OE1 or OE2) input is high, all eight outputs are in the high-impedance state. The outputs are in a high-impedance state during power up and power down while the supply voltage is less than approximately 3 V.

The SN64BCT541A is characterized for operation from -40°C to 85°C and from 0°C to 70°C.

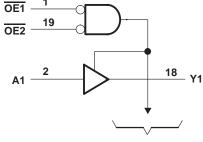
FUNCTION TABLE

	INPUTS	OUTPUT	
OE1	OE2	Α	Υ
L	L	L	L
L	L	Н	Н
Н	X	Χ	Z
Х	Н	Χ	Z

logic symbol†

OE1 ΕN 19 OE2 2 18 ∇ 1⊳ **Y1 A1** 3 17 **Y2 A2** 4 16 **Y3** A3 5 15 **Y4** Α4 6 14 Α5 Y5 13 A₆ **Y6** 8 12 Α7 **Y7** 9 11 **A8** Y8

logic diagram (positive logic)



To Seven Other Channels

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V _{CC}	– 0.5 V to 7 V
Input voltage range, V _I (see Note 1)	–0.5 V to 7 V
Voltage range applied to any output in the disabled or power-off state, VO	0.5 V to 5.5 V
Voltage range applied to any output in the high state, VO	–0.5 V to V _{CC}
Current into any output in the low state	128 mA
Operating free-air temperature range, T _A	–40°C to 85°C
Storage temperature range	. −65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input negative voltage rating may be exceeded if the input clamp current rating is observed.

recommended operating conditions

		MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			V
V _{IL}	Low-level input voltage			8.0	V
liK	Input clamp current			-18	mA
lOH	High-level output current			-15	mA
lOL	Low-level output current			64	mA
TA	Operating free-air temperature	-40		85	°C

[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

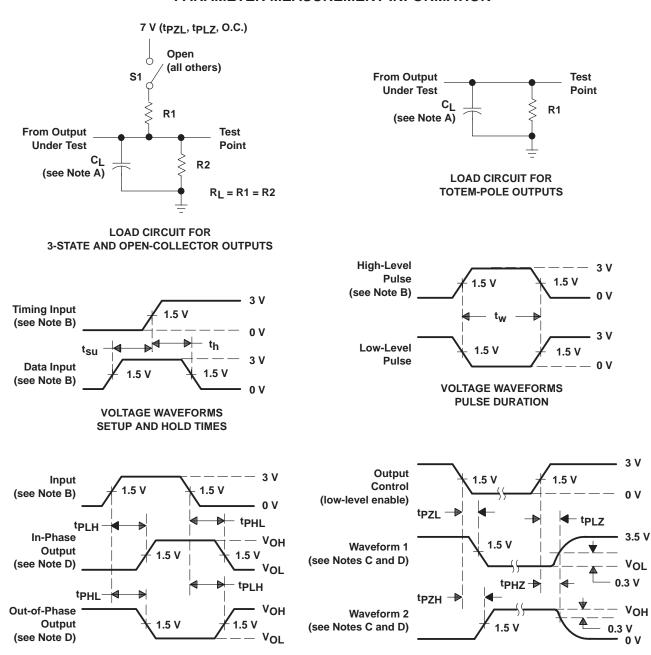
PARAMETER	TES	MIN	TYP [†]	MAX	UNIT		
VIK	V _{CC} = 4.5 V,	$I_I = -18 \text{ mA}$				-1.2	V
V	V 45V	$I_{OH} = -3 \text{ mA}$		2.4	3.3		.,
VOH	V _{CC} = 4.5 V	$I_{OH} = -15 \text{ mA}$		2	3.1		V
V _{OL}	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = 64 \text{ mA}$			0.42	0.55	V
lozh	$V_{CC} = 5.5 \text{ V},$	$V_0 = 2.7 \text{ V}$				50	μΑ
lozL	$V_{CC} = 5.5 \text{ V},$	$V_0 = 0.5 V$				-50	μΑ
1	V _{CC} = 0 to 2.3 V (power up)	V- 27V - 25V	OE at 0.8 V			± 50	^
loz	V _{CC} = 1.8 V to 0 (power down)	$V_0 = 2.7 \text{ V or } 0.5 \text{ V},$			± 50	μΑ	
lį	$V_{CC} = 5.5 \text{ V},$	V _I = 7 V				0.1	mA
lіН	$V_{CC} = 5.5 \text{ V},$	V _I = 2.7 V				20	μΑ
I _{ΙL}	$V_{CC} = 5.5 \text{ V},$	$V_{I} = 0.5 V$				-0.6	mA
los [‡]	$V_{CC} = 5.5 \text{ V},$	VO = 0		-100		-225	mA
ICCL	V _{CC} = 5.5 V				47	72	mA
Іссн	V _{CC} = 5.5 V				27	40	mA
Iccz	V _{CC} = 5.5 V				5	7	mA
Ci	V _{CC} = 5 V,	$V_1 = 2.5 \text{ V or } 0.5 \text{ V}$			5		pF
Co	V _{CC} = 5 V,	$V_0 = 2.5 \text{ V or } 0.5 \text{ V}$			10		pF

switching characteristics (see Figure 1)

PARAMETER	FROM TO (INPUT) (OUTPUT)		V _{CC} = 5 V, C _L = 50 pF, R1 = 500 Ω, R2 = 500 Ω,			V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R1 = 500 Ω, R2 = 500 Ω T_A = -40°C T_A = 0°C				UNIT
			T _A = 25°C		to 85°C		to 70°C			
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	А	Υ	2.1	3.7	5.3	1.7	6.3	1.7	6	
t _{PHL}	A	Ť	3.7	5.5	7.5	3.2	8.7	3.4	8.2	ns
^t PZH	ŌĒ	V	4.5	7.2	9.3	4.4	11	3.9	10.7	
tpZL	OE	Υ	5	8	10.4	5.4	12.4	4.4	11.5	ns
t _{PHZ}	ŌĒ	Y	3.5	5.6	7.6	3	9.1	3	8.6	20
t _{PLZ}	OE .	Y		5.2	7.2	3	9.4	3	8.6	ns

[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. ‡ Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

VOLTAGE WAVEFORMS

PROPAGATION DELAY TIMES (see Note D)

B. All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $t_r = t_f \leq$ 2.5 ns, duty cycle = 50%.

VOLTAGE WAVEFORMS

ENABLE AND DISABLE TIMES, 3-STATE OUTPUTS

- C. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- D. The outputs are measured one at a time with one transition per measurement.
- E. When measuring propagation delay times of 3-state outputs, switch S1 is open.

Figure 1. Load Circuits and Voltage Waveforms





PACKAGE OPTION ADDENDUM

7-Jun-2010

PACKAGING INFORMATION

www.ti.com

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
SN64BCT541ADW	OBSOLETE	SOIC	DW	20		TBD	Call TI	Call TI	Samples Not Available
SN64BCT541ADWR	OBSOLETE	SOIC	DW	20		TBD	Call TI	Call TI	Samples Not Available
SN64BCT541AN	OBSOLETE	PDIP	N	20		TBD	Call TI	Call TI	Samples Not Available

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

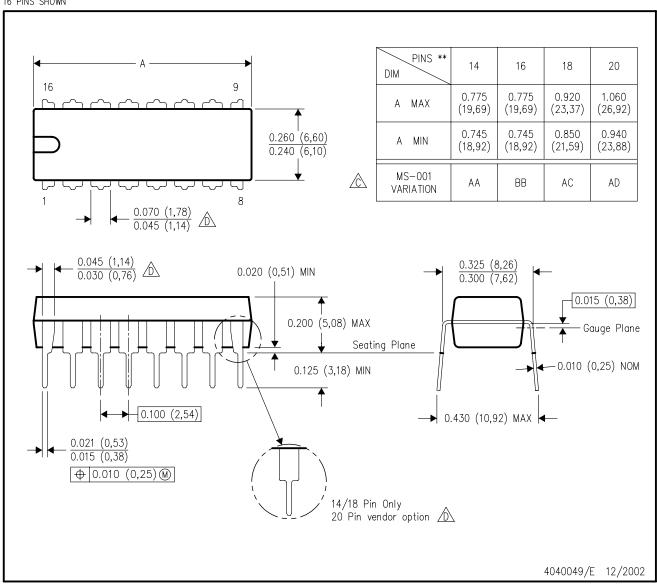
Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

N (R-PDIP-T**)

PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



DW (R-PDSO-G20)

PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in inches (millimeters). Dimensioning and tolerancing per ASME Y14.5M-1994.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
- D. Falls within JEDEC MS-013 variation AC.



IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Audio	www.ti.com/audio	Communications and Telecom	www.ti.com/communications
Amplifiers	amplifier.ti.com	Computers and Peripherals	www.ti.com/computers
Data Converters	dataconverter.ti.com	Consumer Electronics	www.ti.com/consumer-apps
DLP® Products	www.dlp.com	Energy and Lighting	www.ti.com/energy
DSP	dsp.ti.com	Industrial	www.ti.com/industrial
Clocks and Timers	www.ti.com/clocks	Medical	www.ti.com/medical
Interface	interface.ti.com	Security	www.ti.com/security
Logic	logic.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense
Power Mgmt	power.ti.com	Transportation and Automotive	www.ti.com/automotive
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video
RFID	www.ti-rfid.com	Wireless	www.ti.com/wireless-apps
RF/IF and ZigBee® Solutions	www.ti.com/lprf		

TI E2E Community Home Page

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2011, Texas Instruments Incorporated

e2e.ti.com