

2560 Bit

MOS Read-Only Memory Character Generator

Read Only
Memories

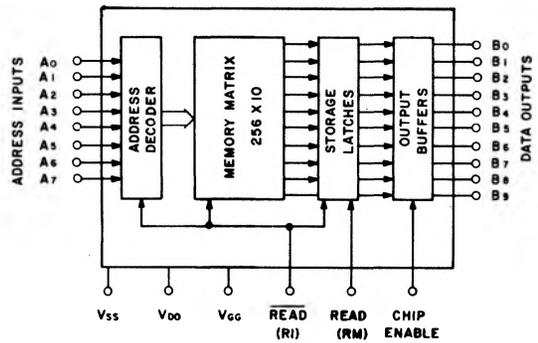
DESCRIPTION

The MK 2408 P is a pre-programmed member of the MK 2400 P Series. It is programmed as a dot-matrix character generator (64 characters) with ASCII encoded inputs and row (5-bit) outputs. The MK 2408 P outputs two rows at the same time. Row 1 is available at outputs B9 (left), B8, B7, B6, and B5 (right) while row 2 is available at outputs B4 (left), B3, B2, B1, and B0 (right). Row 3 is available at B9 through B5 while row 4 is available at B4 through B0. Row 5 and row 6 are available at B9 through B5 and B4 through B0. Row 5 and row 6 are available at B9 through B5 and B4 through B0. Row selection is determined by the address combination of bits A0 and A1.

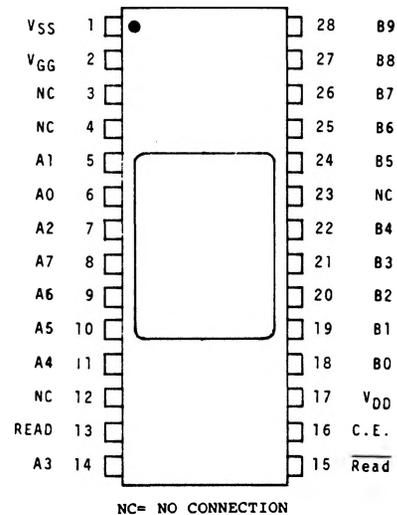
The MK 2408 P meets and operates by the specifications outlined in the MK 2400 P Series data sheet (DS-24001270-2)

The example in Figure 1 demonstrates the correspondence of the device outputs and row select sequence to the 7 x 5 dot-matrix font. The complete character font patterns (truth table) are illustrated on the back. A logic 1 or a DOT represents an input or output voltage equal to V_{SS} (+5V) and a logic 0 or a blank represents a voltage equal to V_{DD} (0V). The eighth row outputs (B4 through B0 when inputs A1 and A0 equal logic 1) are not illustrated since in each case they are equal to all 0's.

FUNCTIONAL DIAGRAM



PIN CONNECTIONS



A1	A0	B9	B8	B7	B6	B5	
		B4	B3	B2	B1	B0	
0	0	1	0	0	0	1	B9-B5
0	1	1	1	0	1	1	B4-B0
1	0	1	0	1	0	1	B9-B5
1	1	1	0	0	0	1	B4-B0
		0	0	0	0	0	B9-B5
							B4-B0

A7 = 0
 A6 = 0
 A5 = 1
 A4 = 1
 A3 = 0
 A2 = 1

FIGURE 1

