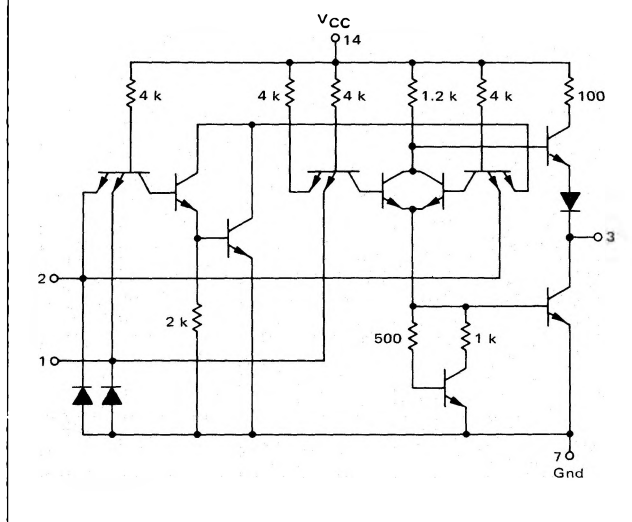


QUAD 2-INPUT
EXCLUSIVE "NOR" GATE

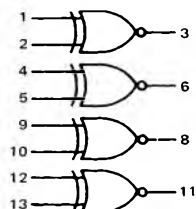
MC3100/MC3000 series

MC3122F • MC3022F
MC3122L • MC3022L,P

CIRCUIT SCHEMATIC
1/4 OF CIRCUIT SHOWN



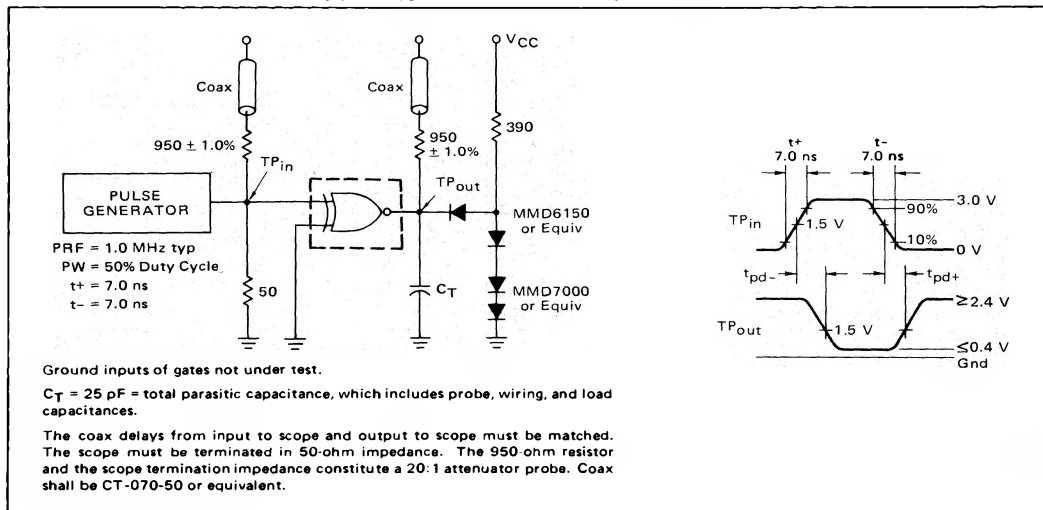
This device consists of four 2-input Exclusive NOR gates. They can be used to build parity checking/generating functions. Up/down counters can be built using these gates and J-K flip-flops.



Positive Logic: $3 = \bar{1} \cdot \bar{2} + 1 \cdot 2$

Input Loading Factor = 1.6
Output Loading Factor = 8
Total Power Dissipation = 85 mW typ/pkg
Propagation Delay Time = 14 ns typ

SWITCHING TIME TEST CIRCUIT AND WAVEFORMS



See General Information section for packaging.

ELECTRICAL CHARACTERISTICS