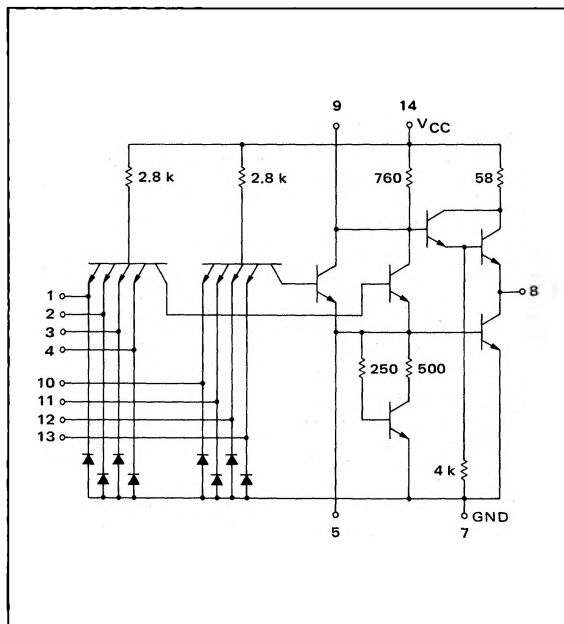


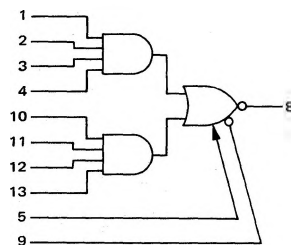
EXPANDABLE 2-WIDE 4-INPUT
"AND-OR-INVERT" GATE

MC3100/MC3000 series

MC3134F • MC3034F
MC3134L • MC3034L,P
(54H55J) (74H55J, N)



This device consists of two 4-input AND gates ORed together and inverted. The emitter and collector nodes of the OR stage are brought out to provide expansion capability to a 6-wide AOI gate using the MC3130/3030 or MC3118/3018 expanders.



Positive Logic:

$$8 = (1 \bullet 2 \bullet 3 \bullet 4) + (10 \bullet 11 \bullet 12 \bullet 13) + (\text{Expanders})$$

Negative Logic:

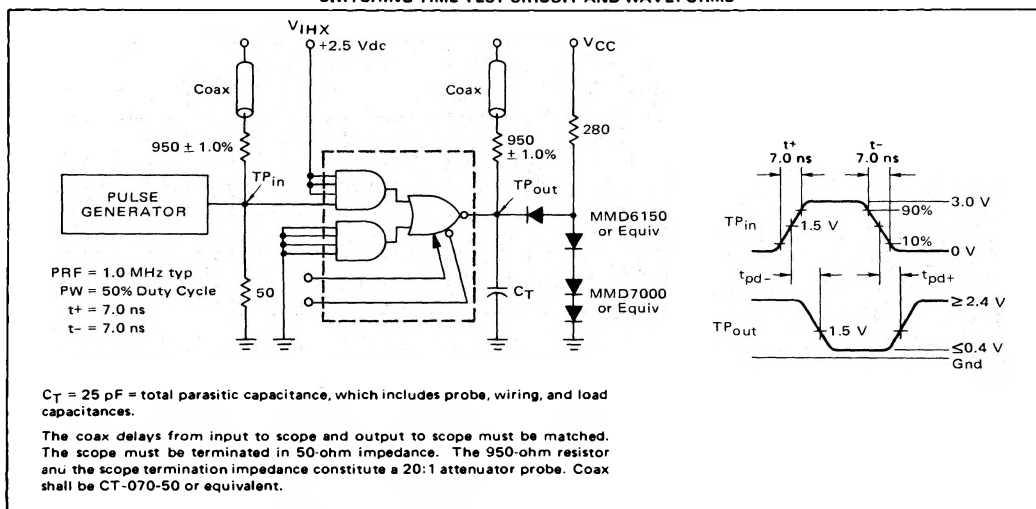
$$8 = (1 + 2 + 3 + 4) \bullet (10 + 11 + 12 + 13) \bullet (\text{Expanders})$$

Input Loading Factor = 1
Output Loading Factor = 10
Total Power Dissipation = 30 mW typ/pkg
Propagation Delay Time = 7.0 ns typ

Pin numbers for the 54H55F/74H55F device are shown in the chart. These devices are available on special request.

DEVICE	PIN NUMBERS													
MC3134F,L/3034F,L,P	1	2	3	4	5	6	7	8	9	10	11	12	13	14
54H55F/74H55F	14	1	2	3	9	10	11	12	13	5	6	7	8	4

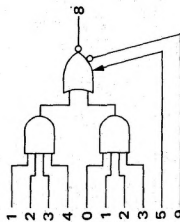
SWITCHING TIME TEST CIRCUIT AND WAVEFORMS



See General Information section for packaging.

ELECTRICAL CHARACTERISTICS

Test procedures are shown for only one input of the gate under test. To complete testing, sequence through remaining inputs.

[illegible]

**Tested only at 25°C

① See Figure 1.

(2) See Figure 2.

(3) See Figure 3.