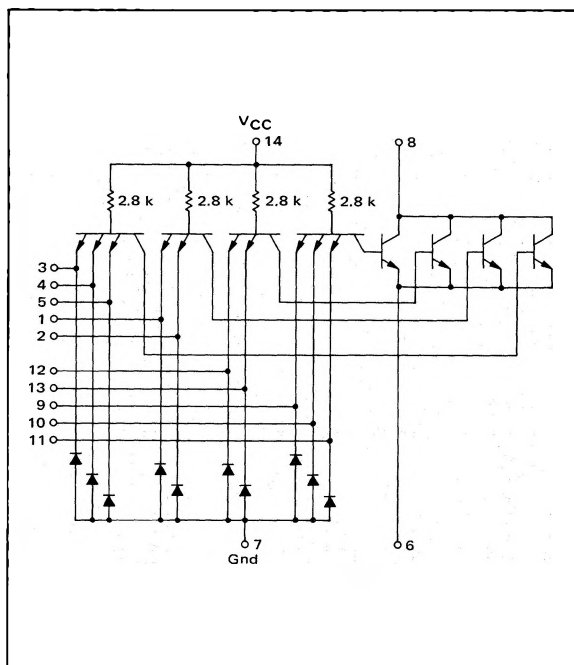


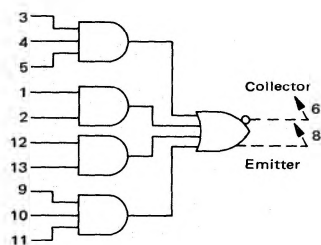
4-WIDE
3-2-2-3 INPUT EXPANDER FOR
"AND-OR-INVERT" GATES

MC3100/MC3000 series

MC3118F • MC3018F
MC3118L • MC3018L,P
(54H62J) (74H62J,N)



This expander is designed to expand the AND-OR-INVERT capabilities of the MC3032 and MC3034. One expander can be connected to each expander input of the expandable gate.



Input Loading Factor = 1

Full output loading factor of the expandable gate is maintained.

Total Power Dissipation = 40 mW typ/pkg

Propagation Delay Time:

$\Delta t_{pd1} = +0.4 \text{ ns typ}$

$\Delta t_{pd0} = 0.05 \text{ ns typ}$

When added to the expandable "AND-OR-INVERT" gate.

$\Delta t_{pd1/pF} = +0.3 \text{ ns/pF typ}$

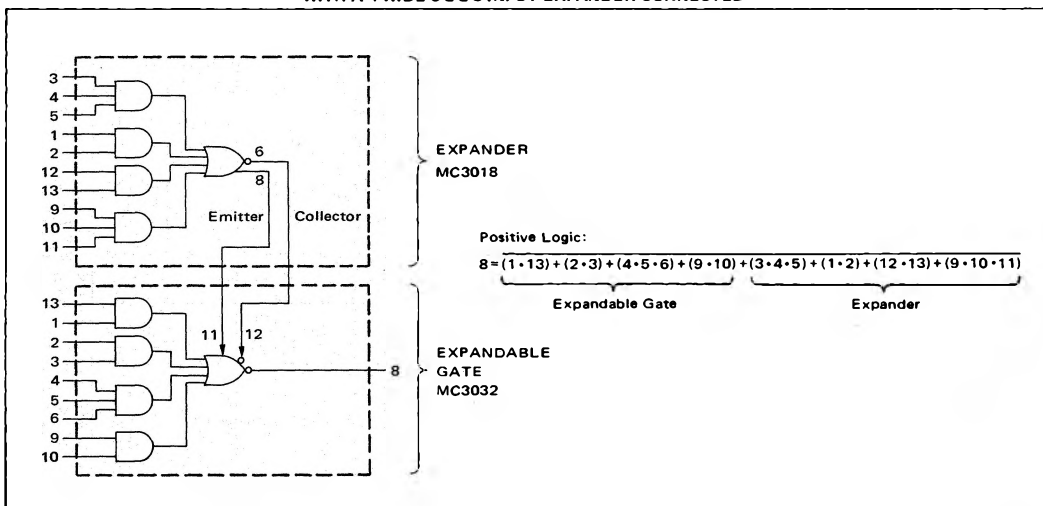
$\Delta t_{pd0/pF} = +0.04 \text{ ns/pF typ}$

Caused by additional capacitance at expansion points.

Pin numbers for the 54H62F/74H62F device are shown in the chart. These devices are available on special request.

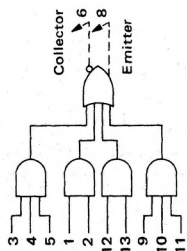
DEVICE	PIN NUMBERS													
MC3118F,L/3018F,L,P	1	2	3	4	5	6	7	8	9	10	11	12	13	14
54H62F/74H62F	2	3	13	14	1	10	11	12	7	8	9	5	6	4

APPLICATION: EXPANDABLE 4-WIDE 2-2-3-2-INPUT AND-OR-INVERT GATE
WITH A 4 WIDE 3-2-2-3 INPUT EXPANDER CONNECTED



See General Information section for packaging.

Test procedures are shown for only one input of the expander being tested. To complete testing, sequence through remaining inputs.



*Ground inputs to gates not under test.
† Apply to pin 14: @ Low temp. V_{CCL} ; @ 25°C V_{CC} ; @ Hi temp V_{CCH}
†† Connect 575 Ω resistor from Pin 8 to ground.
**This test is a measure of potential difference between pins 6 & 8.