



$V_{CC1} = 1, 15$

$V_{CC2} = 16$

$V_{EE} = 8$

$P_D = 160 \text{ mW typ/pkg (No Load)}$

$t_{pd} = 1.5 \text{ ns typ (All Outputs Loaded)}$

High Speed Dual 3-input 3-output OR/NOR Gate

The MC10212 is designed to drive up to six transmission lines simultaneously. The multiple outputs of this device also allow the wire "OR"-ing of several levels of gating for minimization of gate and package count.

The ability to control three parallel lines with minimum propagation delay from a single point makes the MC10212 particularly useful in clock distribution applications where minimum clock skew is desired.