



V<sub>CC1</sub> = Pin 1  
 V<sub>CC2</sub> = Pin 16  
 V<sub>EE</sub> = Pin 8

Function Select		POSITIVE LOGIC	
		Logic Function M is High F	Arithmetic Operation M is Low F
S <sub>1</sub>	S <sub>0</sub>	F = A ⊕ B F = A ⊕ B F = A + B F = A + B	F = A plus B plus Carry F = A plus B plus Carry F = A plus B plus Carry F = A + B + 2
L	L		
L	H		
H	L		
H	H		

P<sub>D</sub> = 575 mW typ/pkg (No Load)  
 t<sub>pd</sub> (typ): A1 to F = 7.5 ns  
 C<sub>n</sub> to C<sub>n-2</sub> = 2.7 ns  
 A1 to PG = 6.5 ns  
 A1 to GG = 5.5 ns  
 A1 to C<sub>n-2</sub> = 7.0 ns

## 2-Bit Arithmetic Logic Unit/Function Generator

The MC10182 is a high-speed arithmetic logic unit capable of performing 4 logic operations and 4 arithmetic operations on two 2-bit words. Full internal carry is incorporated for arithmetic operation.

Arithmetic logic operations are selected by applying the appropriate binary word to the select inputs (S<sub>0</sub> and S<sub>1</sub>) as indicated in the tables of arithmetic/logic functions. Group carry propagate (PG) and carry generate (GG) are provided for a second order look ahead carry using the MC10179. The internal carry is enabled by applying a low level voltage to the mode control input (M).

The MC10182 provides an alternate to the MC10181 four-bit ALU for applications not requiring the extended functions of the MC10181 or for applications requiring a 16-pin package. The MC10182 also differs from the MC10181 in that Word A and B are treated equally for addition and subtraction(A plus B, A minus B, B minus A). (Continued in next page.)

MC10182

ARITHMETIC FUNCTIONS

## TRUTH TABLE

These authors are not normally using this journal's submission system.