



V_{CC1} = Pin 1

V_{CC2} = Pin 16

V_{EE} = Pin 8

P_D = 310 mW typ/pkg (No Load)

t_{pd} : G to Q = 2 ns typ

D to Q = 3 ns typ

C to Q = 4 ns typ

TRUTH TABLE

C	D	Q _{n+1}
0	0	Q _n
0	1	Q _n
1	0	0
1	1	1

Quad Latch

The MC10168 is a Quad Latch with common clocking to all four latches. Separate output enabling gates are provided for each latch, allowing direct wiring to a bus. When the clock is high, outputs will follow the D inputs. Information is latched on the negative-going transition of the clock.