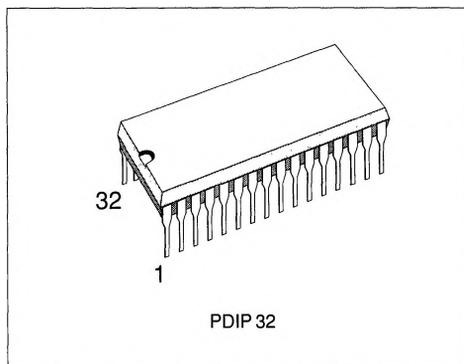


4096K (512K x 8) CMOS ROM
ADVANCE DATA

- **VERY FAST ACCESS TIME** : 120 ns.
(Chip select or address access time)
- **LOW POWER "CMOS" CONSUMPTION** :
 - Operating current 50 mA
 - Stand by current 20 μ A
- **SINGLE + 5 V \pm 10 % POWER SUPPLY.**
- **STATIC OPERATION.**
- **INPUTS AND OUTPUTS TTL COMPATIBLE.**
- **THREE STATE OUTPUTS.**
- **MASK PROGRAMMABLE ACTIVE LOW/HIGH CE.**
- **AUTOMATIC POWER DOWN.**


DESCRIPTION

The M23C4001 is a 4,194,304 CMOS Masked Read Only Memory (ROM), organized as 524,288 x 8 bits. It is manufactured in 0.8 micron CMOS technology : Very fast access time of 120ns makes it ideal for EPROM replacement on high performance, high volume running applications. Chip select line (CE) is active low or active high by mask programming, as per user's choice. When not active, it brings the device in standby mode, suitable on battery operated systems. Output Enable is to be used for Outputs control. After cycle completion and 50 ns without input change, the M23C4001 automatically goes in power-down ($I_{cc1} = 1$ mA), the data remaining latched on the

PIN NAMES

A0-A18	ADDRESS INPUTS
O0-O7	DATA OUTPUTS
\overline{CE}/CE	CHIP ENABLE INPUT
\overline{OE}	OUTPUT ENABLE
Vcc	+ 5V POWER SUPPLY
GND	GROUND
NC	NON CONNECTED

PIN CONNECTION
