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October 2010

# FSAV331 — Dual-Channel, 4:1 Video Switch

## **Features**

- Wide Bandwidth: 300MHz
- -73dB Non-Adjacent Channel Crosstalk at 10MHz
- -56dB Off Isolation at 10MHz
- On Resistance: 3Ω (Typical)
- Low Power Consumption: 3µA (Maximum)
- Control Inputs Compatible with TTL Level

# **Applications**

 Y/C Video or CVBS Video Switch in LCD, Plasma, and Projector Displays

# **Description**

The Fairchild video switch FSAV331 is a dual 4:1 high speed video switch which can be configured as either multiplexer or demultiplexer. Low on-resistance allows inputs to be connected to outputs without adding propagation delay or generating additional ground bounce noise.

When the OE Pin is LOW,  $S_0$  and  $S_1$  connect the A Port to the selected B Port output. When the OE Pin is HIGH, the switch is OPEN and a HIGH-Impedance state exists between the two ports.

# **Ordering Information**

Part Number	Operating Temperature Range	Package	Packing Method
FSAV331MTCX	-40 to +85°C	16-Lead, Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide	Tape and Reel

# **Pin Configurations**

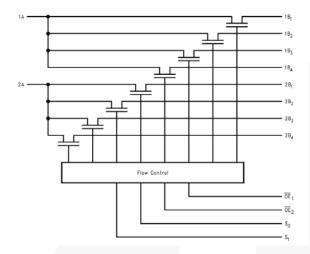


Figure 1. Logic Diagram

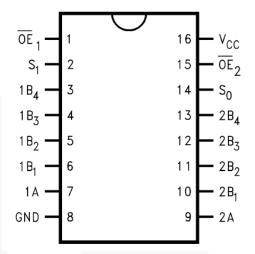


Figure 2. Pin Assignments

# **Pin Descriptions**

Pin #	Name	Description
1, 15	/OE <sub>1</sub> , /OE <sub>2</sub>	Port Enable
2, 14	S <sub>1</sub> , S <sub>0</sub>	Select Input
3, 4, 5, 6	1B <sub>4</sub> , 1B <sub>3</sub> , 1B <sub>2</sub> , 1B <sub>1</sub>	B-Ports (Channel 1)
7, 9	1A, 2A	A-Ports
8	GND	Ground
10, 11, 12, 13	2B <sub>1</sub> , 2B <sub>2</sub> , 2B <sub>3</sub> , 2B <sub>4</sub>	B-Ports (Channel 2)
16	Vcc	Supply Voltage

# **Truth Table**

S <sub>1</sub>	S <sub>0</sub>	/OE <sub>1</sub>	/OE <sub>2</sub>	Function
Don't Care	Don't Care	HIGH	Don't Care	Disconnect 1A
Don't Care	Don't Care	Don't Care	Care HIGH Disconne	
LOW	LOW	LOW	LOW A=	
LOW	HIGH	LOW	LOW A=E	
HIGH	LOW	LOW	LOW A=I	
HIGH	HIGH	LOW	LOW A=E	

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage	-0.5	+7.0	V
Vs	DC Switch Voltage	-0.5	V <sub>CC</sub> +0.5	V
V <sub>IN</sub>	DC Input Voltage <sup>(1)</sup>	-0.5	+7.0	V
I <sub>IK</sub>	DC Input Diode Current	-50		mA
I <sub>OUT</sub>	DC Output Current		128	mA
T <sub>STG</sub>	Storage Temperature Range	-65	+150	°C
ESD	Human Body Model, JESD22-A114		2000	V

#### Note:

# **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Max.	Unit
V <sub>CC</sub>	Power Supply	4.75	5.25	V
$V_{IN}$	Control Input Voltage	0	V <sub>CC</sub>	V
V <sub>SW</sub>	Switch Input Voltage	0	V <sub>CC</sub>	V
T <sub>A</sub>	Operating Temperature, Free Air	-40	+85	°C
$\Theta_{JA}$	Thermal Resistance		100	°C/W

#### Note:

2. Unused control inputs must be held HIGH or LOW; they may not float.

## **DC Electrical Characteristics**

Typical values are at  $V_{CC}=5.0V$  and  $T_{A}=+25$ °C.

Cumbal	Parameter	Conditions	V 00	$T_A = -40 \text{ to } +85^{\circ}\text{C}$			Units
Symbol	ymbol Parameter Conditions V <sub>CC</sub> (V)	Min.	Тур.	Max.			
V <sub>ANALOG</sub>	Analog Signal Range		4.75 to 5.25	0		2	V
V <sub>IK</sub>	Clamp Diode Voltage	I <sub>IN</sub> =-18mA	4.75			-1.2	V
V <sub>IH</sub>	High-Level Input Voltage		4.75 to 5.25	2.0			V
$V_{IL}$	Low-Level Input Voltage		4.75 to 5.25	a 92		0.8	V
I <sub>IN</sub>	Control Input Leakage	V <sub>IN</sub> =0V to V <sub>CC</sub>	5.25			±1.0	μΑ
loz	Off-State Leakage Current	$0 \le A, B \le V_{CC}$	5.25			±1.0	μΑ
В	Switch On Resistance <sup>(3)</sup>	V <sub>IN</sub> =1.0V, I <sub>ON</sub> =13mA	4.75		3	7	0
Ron	Switch On Resistance	V <sub>IN</sub> =2.0V, I <sub>ON</sub> =26mA	4.75		5	10	Ω
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> =0V or V <sub>CC</sub> , I <sub>OUT</sub> =0	5.25			3	μΑ

#### Note:

3. Measured by the voltage drop between the A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the A or B pins.

The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

# **AC Electrical Characteristics**

Typical values are at  $T_A$ =+25°C and  $V_{CC}$ =5.0V

Symbol	Parameter	Conditions V <sub>CC</sub> (V)		T <sub>A</sub> = -40 to +85°C			Units	Figure
Syllibol	Parameter	Conditions	V <sub>cc</sub> (V)	Min.	Тур.	Max.	Ullits	riguie
	Turn On Time; S to B	V <sub>I</sub> =7V for t <sub>PZL</sub>	1.0		5.3		Figure 3,	
t <sub>ON</sub>	Output Enable Time OE to A or B	V <sub>I</sub> =Open for t <sub>PZH</sub>	4.75 to 5.25	1.0		5.3	ns	Figure 4
	Turn Off Time; S to B	$V_{I}=7V$ for $t_{PLZ}$		1.0		5.8		Figure 3,
t <sub>OFF</sub>	Output Disable Time OE to A or B	V <sub>I</sub> =Open for t <sub>PHZ</sub> 4.75 to 5.25	1.0		5.5	ns	Figure 4	
4 4	Propagation Delay <sup>(4)</sup>	V-Open	Open 4.75 to 5.25			0.1	20	
t <sub>PLH</sub> , t <sub>PHL</sub>	Select to A Delay	Vi=Open				5.0	ns	
D <sub>G</sub>	Differential Gain <sup>(5)</sup>	$R_L=150\Omega$ , $f=3.58MHz$	4.75 to 5.25		0.26		%	
D <sub>P</sub>	Differential Phase <sup>(5)</sup>	$R_L=150\Omega$ , $f=3.58MHz$	4.75 to 5.25		0.23		0	
O <sub>IRR</sub>	Non Adjacent Off Isolation	$R_L=150\Omega$ , $f=10MHz$	4.75 to 5.25		-56.0		dB	Figure 5
X <sub>TALK</sub>	Non Adjacent Channel Crosstalk <sup>(5)</sup>	$R_L=150\Omega$ , $f=10MHz$	4.75 to 5.25		-73.0		dB	Figure 6
B <sub>W</sub>	-3dB Bandwidth <sup>(5)</sup>	R <sub>L</sub> =50Ω	4.75 to 5.25		300		MHz	Figure 7

## Note:

- This parameter is guaranteed by design.

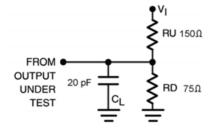
  This parameter is guaranteed by device characterization, not production tested.

# Capacitance

Typical values are at T<sub>A</sub>=+25°C.

Sy	Symbol Parameter		Conditions	Тур.	Units
C <sub>IN</sub> Control Pin Input Capacitance V <sub>CC</sub> =5.0		V <sub>CC</sub> =5.0V	2	pF	
	Con	A/B On Capacitance	V <sub>CC</sub> =5.0V, /OE=0V	39	pF
	A Port	O# Conscitores	V /05 5 0V	13	V
C <sub>OFF</sub>	B Port	Off Capacitance	V <sub>CC</sub> , /OE=5.0V	5	pF

# **AC Loadings and Waveforms**



#### Notes:

- 6. Input drive by  $50\Omega$  source terminated in  $50\Omega$ .
- 7. C<sub>L</sub> includes load and stray capacitance.
- 8. Input PRR=1.0MHz, t<sub>W</sub>=500ns.

Figure 3. AC Test Circuit

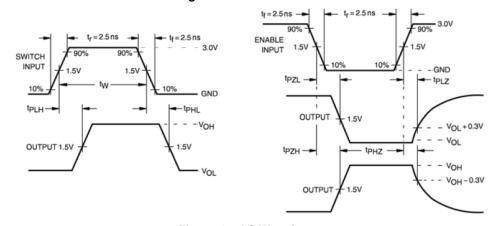


Figure 4. AC Waveforms

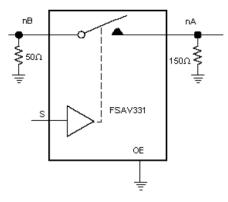


Figure 5. Off Isolation

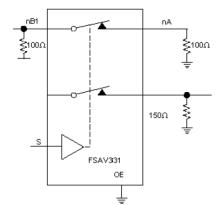


Figure 6. Crosstalk

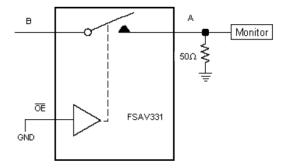


Figure 7. Bandwidth

# **Physical Dimensions** 5.00±0.10 4.55 5.90 4.45 7.35 В 6.4 0.65 $4.4 \pm 0.1$ 1.45 3.2 ALL LEAD TIPS 5.00 PIN #1 IDENT. LAND PATTERN RECOMMENDATION (F) 0.11 SEE DETAIL A ALL LEAD TIPS 1.1 MAX (0.90)○ 0.1 C 0.09-0.20 -C-0.10±0.05 0.65 0.19 - 0.30 TOP AND BOTTOM ⊕ 0.10M A BS CS **GAGE PLANE** NOTES: 0.25 0°-8° A. CONFORMS TO JEDEC REGISTRATION MO-153, VARIATION AB, B. DIMENSIONS ARE IN MILLIMETERS C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS 0.6±0.1 SEATING PLANE D. DIMENSIONING AND TOLERANCES PER ANSI Y14.5M, 1994 E. DRAWING FILE NAME: MTC16REV4 **DETAIL** A F. LAND PATTERN RECOMMENDATION PER IPC7351 - ID# TSOP65P640X110-16N

MTC16rev4

Figure 8. 16-Lead, Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide

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