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December 2013

FPDB40PH60B PFC SPM[®] 3 Series for 2-Phase Bridgeless PFC

Features

- UL Certified No. E209204 (UL1557)
- 600 V 40 A 2-Phase Bridgeless PFC with Integral Gate Driver and Protection
- Very Low Thermal Resistance Using Al₂O₃ DBC Substrate
- Built-in NTC Thermistor for Temperature Monitoring
- · Built-in Shunt Resistor for Current Sensing
- Optimized for 20kHz Switching Frequency
- Isolation Rating: 2500 Vrms/min.

Applications

• 2-Phase Bridgeless PFC Converter

Related Source

 <u>AN-9041 - Bridgeless PFC SPM 3 Series Design</u> <u>Guide</u>

General Description

The FPDB40PH60B is an advanced PFC SPM[®] 3 module providing a fully-featured, high-performance Bridgeless PFC (Power Factor Correction) input power stage for consumer, medical, and industrial applications. These modules integrate optimized gate drive of the built-in IGBTs to minimize EMI and losses, while also providing multiple on-module protection features including under-voltage lockout, over-current shutdown, thermal monitoring, and fault reporting. These modules also feature high-performance output diodes and shunt resistor for additional space savings and mounting convenience.

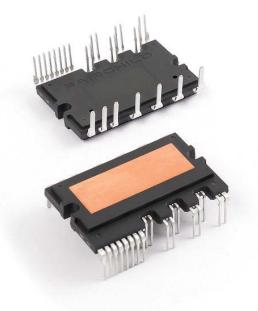


Figure 1. Package Overview

Package Marking & Ordering Information

Device	Device Marking	Package	Packing Type	Quantity
FPDB40PH60B	FPDB40PH60B	SPMGC-027	Rail	10

Integrated Drive, Protection and System Control Functions

- For IGBTs: gate drive circuit, Over-Current Protection (OCP), control supply circuit Under-Voltage Lock-Out (UVLO) Protection
- Fault signal: corresponding to OC and UV fault
- Built-in thermistor: temperature monitoring
- Input interface: active-HIGH interface, works with 3.3 / 5 V logic, Schmitt-trigger input

Pin Configuration

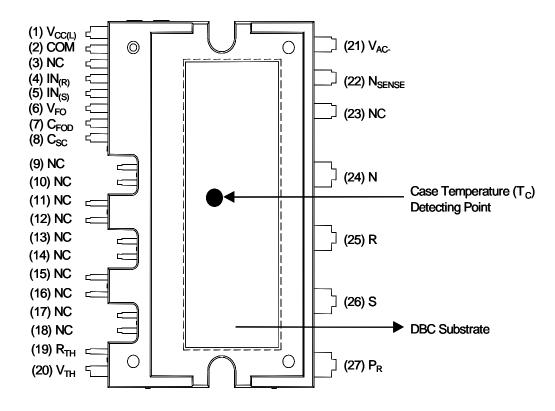


Figure 2. Top View

Pin Number	Pin Name	Pin Description
1	V _{CC}	Common Bias Voltage for IC and IGBTs Driving
2	СОМ	Common Supply Ground
4	IN _(R)	Signal Input for Low-Side R-Phase IGBT
5	IN _(S)	Signal Input for Low-Side S-Phase IGBT
6	V _{FO}	Fault Output
7	C _{FOD}	Capacitor for Fault Output Duration Selection
8	C _{SC}	Capacitor(Low-Pass Filter) for Over-Current Detection
19	R _(TH)	Series Resistor for The Use of Thermistor
20	V _(TH)	Thermistor Bias Voltage
21	V _{AC-}	Current Sensing Terminal
22	N _{SENSE}	Current Sensing Reference Terminal
24	Ν	Negative Rail of DC-Link
25	R	Output for R-Phase
26	S	Output for S-Phase
27	P _R	Positive Rail of DC-Link
3, 9~18, 23	NC	No Connection

Internal Equivalent Circuit

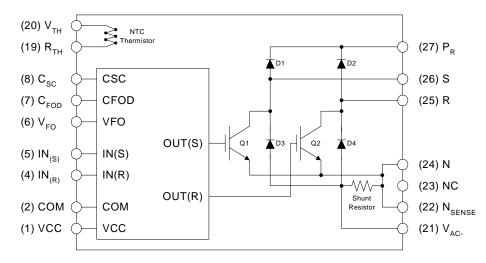


Figure 3. Internal Block Diagram

Notes:

1. Converter is composed of two IGBTs including four diodes and one IC which has gate driving and protection functions.

FPDB40PH60B PFC SPM® 3 Series for 2-Phase Bridgeless PFC

Absolute Maximum Ratings ($T_J = 25^{\circ}C$, unless otherwise specified.) Converter Part

Symbol	Item	Condition	Rating	Unit
V _i	Supply Voltage	Applied between R - S	264	V _{rms}
V _{i(Surge)}	Supply Voltage (Surge)	Applied between R - S	500	V
V _{PN}	Output Voltage	Applied between P - N	450	V
V _{PN(Surge)}	Output Voltage (Surge)	Applied between P - N	500	V
V _{CES}	Collector - Emitter Voltage		600	V
$\pm I_{C}$	Each IGBT Collector Current	$T_{C} = 25^{\circ}C$	40	А
$\pm I_{CP}$	Each IGBT Collector Current (Peak)	T _C = 25°C, Under 1 ms Pulse Width	70	А
P _C	Collector Dissipation	T _C = 25°C per IGBT	113	W
V _{RRM}	Repetitive Peak Reverse Voltage		600	V
I _{FSM}	Peak Forward Surge Current	Single Half Sine-Wave	350	А
P _{RSH}	Power Rating of Shunt Resistor	T _C < 125°C	2	W
Τ _J	Operating Junction Temperature	(Note 2)	-40 ~ 150	°C

Notes:

2. The maximum junction temperature rating of the power chips integrated within the PFC SPM[®] product is $150^{\circ}C(@T_{C} \le 100^{\circ}C)$.

Control Part

Symbol	Item	Condition	Rating	Unit
V _{CC}	Control Supply Voltage	Applied between V _{CC} - COM	20	V
V _{IN}	Input Signal Voltage	Applied between IN - COM	-0.3 ~ 17.0	V
V _{FO}	Fault Output Supply Voltage	Applied between V _{FO} - COM	-0.3 ~ V _{CC} +0.3	V
I _{FO}	Fault Output Current	Sink Current at V _{FO} Pin	5	mA
V _{SC}	Current Sensing Input Voltage	Applied between C _{SC} - COM	-0.3~V _{CC} +0.3	V

Total System

Symbol	Item	Condition	Rating	Unit
т _с	Module Case Operation Temperature		-20 ~ 100	°C
T _{STG}	Storage Temperature		-40 ~ 150	°C
V _{ISO}	Isolation Voltage	60 Hz, Sinusoidal, AC 1 Minute, Connect Pins to Heat-Sink Plate	2500	V _{rms}

Thermal Resistance

Symbol	ltem	Condition	Min.	Тур.	Max.	Unit
$R_{\theta(j\text{-}c)Q}$	Junction to Case Thermal Resistance	IGBT	-	-	1.1	°C/W
$R_{\theta(j\text{-}c)HD}$	(Referenced to PKG Center)	High-Side Diode	-	-	1.9	°C/W
$R_{\theta(j\text{-}c)LD}$		Low-Side Diode	-	-	1.4	°C/W

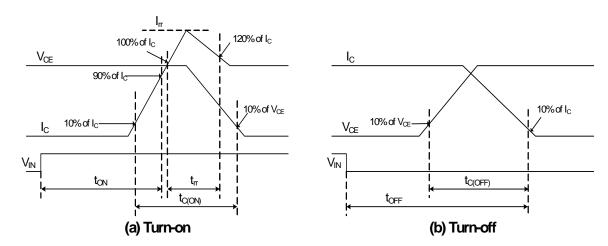
Notes:

3. For the measurement point of case temperature(T_C), please refer to Figure 2.

Symbol	Item	Condition	Min.	Тур.	Max.	Unit
V _{CE(SAT)}	IGBT Saturation Voltage	$V_{CC} = 15 \text{ V}, \text{ V}_{IN} = 5 \text{ V}, \text{ I}_{C} = 40 \text{ A}$	-	1.8	2.3	V
V_{FH}	High-Side Diode Voltage	I _F = 40 A	-	2.2	2.7	V
V _{FL}	Low-Side Diode Voltage	I _F = 40 A	-	1.15	1.55	V
t _{ON}	Switching Times	$V_{PN} = 400 \text{ V}, V_{CC} = 15 \text{ V}, I_{C} = 40 \text{ A}$	-	500	-	ns
t _{C(ON)}		$V_{IN} = 0 V \leftrightarrow 5 V$, Inductive Load (Note 4)	-	180	-	ns
t _{OFF}			-	500	-	ns
t _{C(OFF)}			-	90	-	ns
t _{rr}			-	43	-	ns
I _{rr}			-	6	-	А
R _{SENSE}	Current-Sensing resistor		1.8	2.0	2.2	mΩ
I _{CES}	Collector - Emitter Leakage Current	$V_{CE} = V_{CES}$	-	-	250	μΑ

Electrical Characteristics ($T_J = 25^{\circ}C$, unless otherwise specified.)

Notes: 4. t_{ON} and t_{OFF} include the propagation delay of the internal drive IC. t_{C(ON)} and t_{C(OFF)} are the switching time of IGBT itself under the given gate driving condition internally. For the detailed information, please see Figure 4.

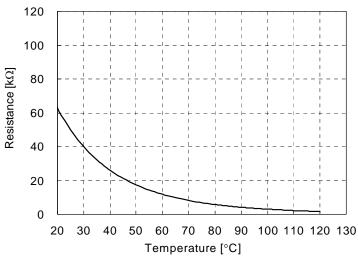


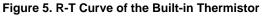


Symbol	Item	Con	dition	Min.	Тур.	Max.	Unit
IQCCL	Quiescent V _{CC} Supply Current	V _{CC} = 15 V, IN = 0 V	V _{CC} - COM	-	-	26	mA
V _{FOH}	Fault Output Voltage	V _{SC} = 0 V, V _{FO} Circuit	: 4.7 k Ω to 5 V Pull-up	4.5	-	-	V
V _{FOL}		V _{SC} = 1 V, V _{FO} Circuit	: 4.7 k Ω to 5 V Pull-up	-	-	0.8	V
V _{SC(ref)}	Over-Current Trip Level	V _{CC} = 15 V		0.45	0.50	0.55	V
UV _{CCD}	Supply Circuit Under-Voltage	Detection Level		10.7	11.9	13.0	V
UV _{CCR}	Protection	Reset Level		11.2	12.4	13.2	V
t _{FOD}	Fault-Out Pulse Width	C _{FOD} = 33 nF (Note 5)	1.4	1.8	2.0	ms
V _{IN(ON)}	ON Threshold Voltage	Applied between IN -	СОМ	3.0	-	-	V
V _{IN(OFF)}	OFF Threshold Voltage]		-	-	0.8	V
R _{TH}	Resistance of Thermistor	at $T_C = 25^{\circ}C$ (See Fig	ure 5)	-	50	-	kΩ
		at T _C = 80°C (See Fig	ure 5)	-	5.76	-	kΩ

Notes: 5. The fault-out pulse width t_{FOD} depends on the capacitance value of C_{FOD} according to the following approximate equation : $C_{FOD} = 18.3 \times 10^{-6} \times t_{FOD}[F]$

R-T Graph





Recommended Operating conditions

Symbol	Item	Condition	Min.	Тур.	Max.	Unit
VI	Input Supply Voltage	Applied between R - S	180	-	264	V _{rms}
V _{PN}	Output Voltage	Applied between P - N	-	280	400	V
V _{CC}	Control Supply Voltage	Applied between V _{CC} - COM	13.5	15	16.5	V
dV _{CC} /dt	Control Supply Variation	Applied between IN - COM	-1	-	1	V/µs
f _{PWM}	PWM Input Signal	$T_C \le 100^{\circ}C, T_J \le 125^{\circ}C, per IGBT$	-	20	-	kHz

Mechanical C	haracteristics and	Ratings				
ltem		Condition	Min.	Тур.	Max.	Units
Mounting Torque	Mounting Screw: M3	Recommended 0.62 N•m	0.51	0.62	0.72	N∙m
Device Flatness	See Figure 6		0	-	+120	μm
Weight			-	15.00	-	g

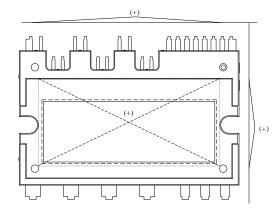
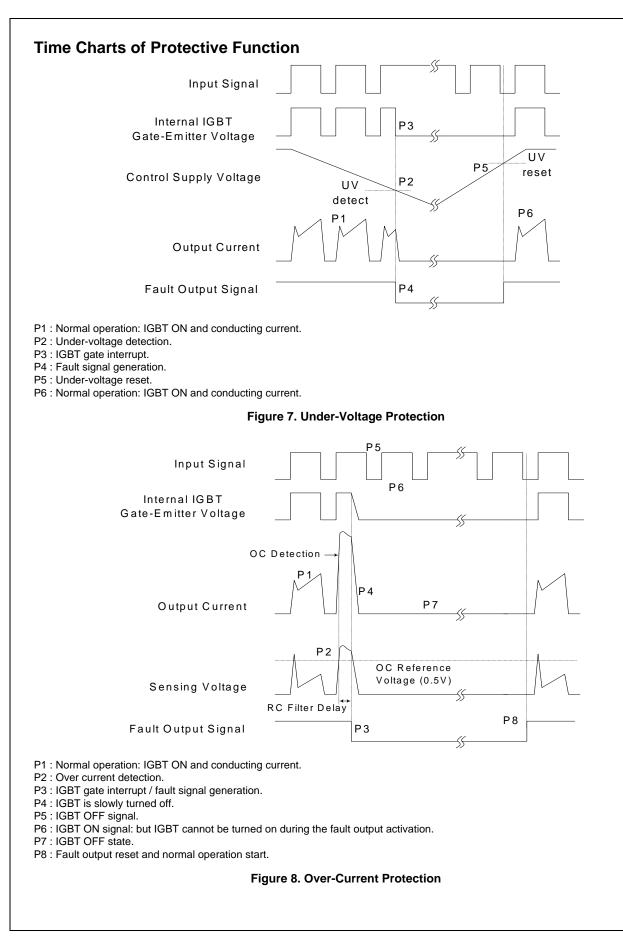


Figure 6. Flatness Measurement Position



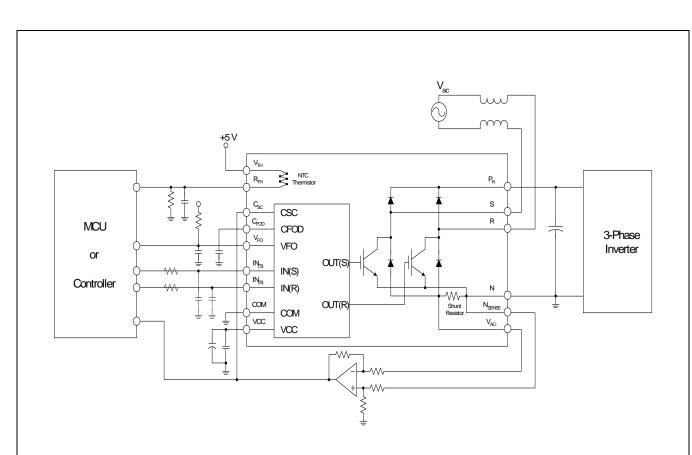
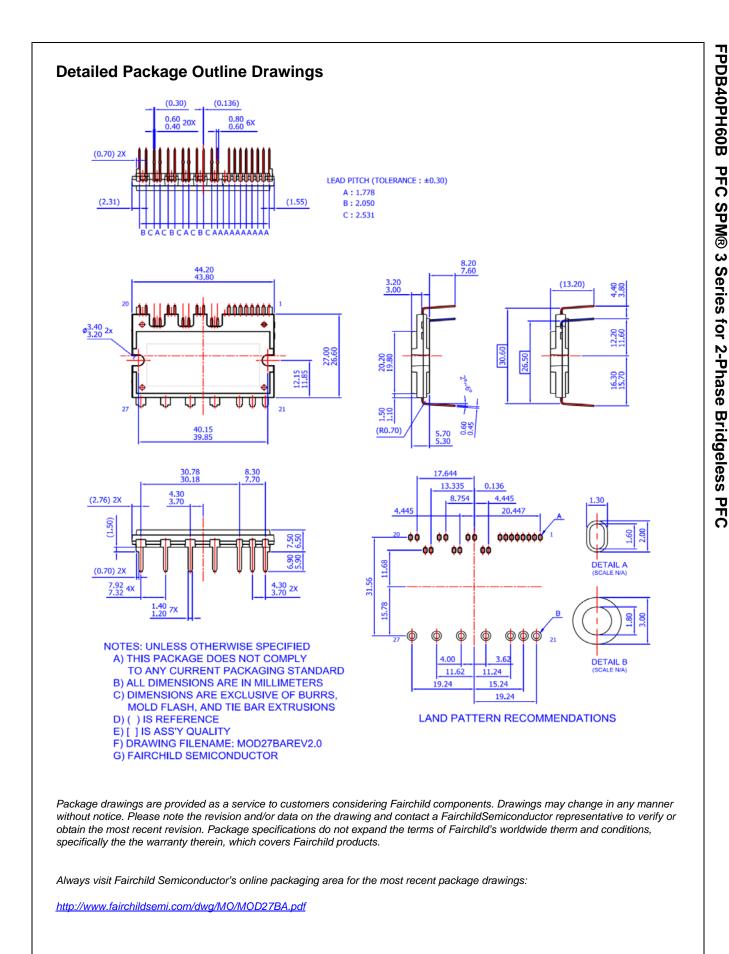


Figure 9. Application Example

Notes: 6. For the over-current protection, please set time constant in the range $3 \sim 4 \ \mu s$.





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