

May 2010

FJD5555 NPN Silicon Transistor

Features

- · High Voltage Switch Mode Application
- Fast Speed Switching
- Wide Safe Operating Area
- · Suitable for Electronic Ballast Application



1. Base 2. Collector 3. Emitter

Absolute Maximum Ratings* T_a =25°C unless otherwise noted

Symbol	Parameter	Value	Units
BV _{CBO}	Collector-Base Voltage	1050	V
BV _{CEO}	Collector-Emitter Voltage	400	V
BV _{EBO}	Emitter-Base Voltage	14	V
I _C	Collector Current (DC)	5	Α
I _{CP}	Collector Current (Pulse)	10	Α
I _B	Base Current (DC)	2	Α
I _{BP}	Collector Current (Pulse)	4	Α
P _C	Collector Dissipation	1.34	W
T_J	T _J Junction Temperature		°C
T _{STG}	Storage Junction Temperature Range	- 55 to 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
$R_{\theta ja}$	Thermal Resistance, Junction to Ambient	95	°C/W	

^{*} Device mounted on minimum pad size

Package Marking and Ordering Information

Part Number	Marking	Package	Packing Method	Remarks
FJD5555TM	J5555	D-PAK	Tape & Reel	

Electrical Characteristics* T_a=25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =500μA, I _E =0	1050			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I_C =5mA, I_B =0	400			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E =500μA, I _C =0	14			V
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =10mA	10			
		V _{CE} =3V, I _C =0.8A	20		40	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =1A, I _B =0.2A		0.17	0.5	V
		I _C =3.5A, I _B =1.0A			1.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C =3.5A, I _B =1.0A			1.2	V
C _{ob}	Output Capacitance	V _{CB} =10V, f=1MHz		45		pF
t _{ON}	Turn On Time	V _{CC} =125V, I _C =0.5A			1.0	μS
t _{STG}	Storage Time	I _{B1} =45mA, I _{B2} =-0.5A			1.2	μS
t _F	Fall Time	$R_L=250\Omega$		0.3		μS
t _{ON}	Turn On Time	V _{CC} =250V, I _C =2.5A			2.0	μS
t _{STG}	Storage Time	I _{B1} =0.5A, I _{B2} =-1.0A			2.5	μS
t _F	Fall Time	$R_L=100\Omega$			0.3	μS
EAS	Avalanche Energy	L=2mH	6			mJ

^{*} Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Characteristics

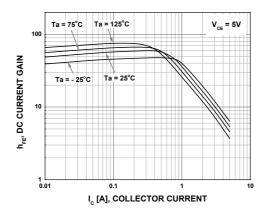


Figure 1. DC Current Gain

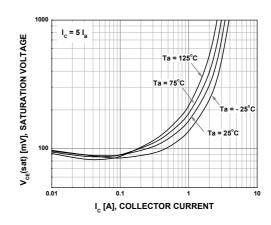


Figure 2. Saturation Voltage

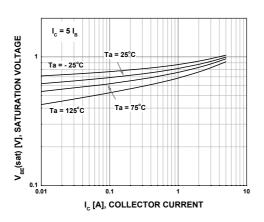


Figure 3. Saturation Voltage

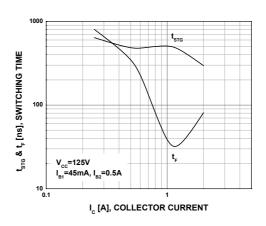


Figure 4. Resistive Load Switching

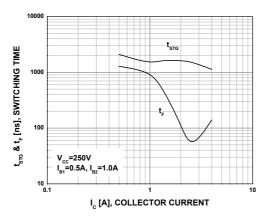


Figure 5. Resistive Load Switching

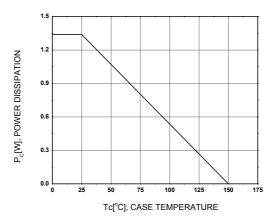


Figure 6. Power Derating

Typical Characteristics (Continued)

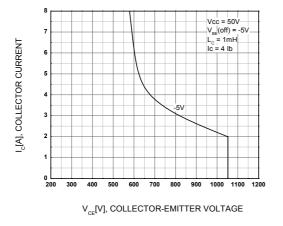


Figure 7. Reverse Bias Safe Operating

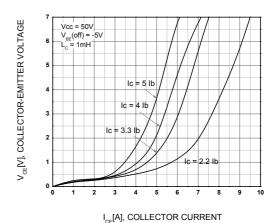
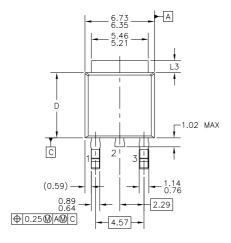
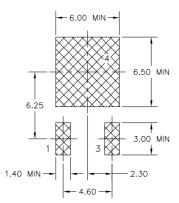


Figure 8. RBSOA Saturation

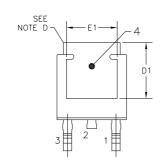
Physical Dimensions

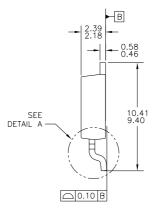
D-PAK

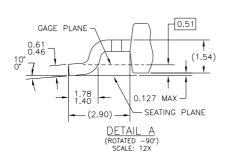




LAND PATTERN RECOMMENDATION







- NOTES: UNLESS OTHERWISE SPECIFIED

 A) ALL DIMENSIONS ARE IN MILLIMETERS.

 B) THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE C, VARIATION AA & AB, DATED NOV. 1999.

 C) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

 D) HEAT SINK TOP EDGE COULD BE IN CHAMFERED CORNERS OR EDGE PROTRUSION.

 E) DIMENSIONS LS,D,E1&D1 TABLE:

DIMEN	1210142 521	J, LIOLDI IA	L
	OPTION AA	OPTION AB	
L3	0.89-1.27	1.52-2.03	
D	5.97-6.22	5.33-5.59	
E1	4.32 MIN	3.81 MIN	
	E 01 MM	4 5 7 1 441	

F) PRESENCE OF TRIMMED CENTER LEAD IS OPTIONAL.

Dimensions in Millimeters



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