## **DS8881**

DS8881 Vacuum Fluorescent Display Driver



Literature Number: SNOSBN3A



# **DS8881 Vacuum Fluorescent Display Driver**

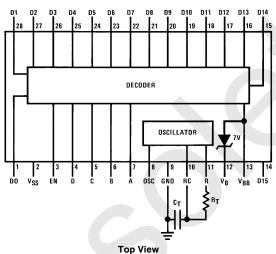
## **General Description**

The DS8881 vacuum fluorescent display driver will drive 16-digit grids of a vacuum fluorescent display. The decode inputs select one of the sixteen outputs to be pulled high. The device contains an oscillator for supplying clock signals to the MOS circuit, the filament bias zener and 50  $k\Omega$  pull-down resistors for each grid. Outputs will source up to 7 mA. The DS8881 is designed for 9V operation. If the enable input is pulled low, all outputs are disabled.

#### **Features**

- Oscillator frequency accuracy and stability allows maximum system speed
- Interdigit blanking with the enable input provides ghost-free display operation
- 50 k $\Omega$  pull-down resistors for each grid
- 7V filament bias zener

## **Connection Diagram**



Order Number DS8881N See NS Package Number N28B

Truth Table All outputs now shown high are off (low)

			_			_														
	lr	nputs										Digi	t Out	puts						
E <sub>N</sub>	D	C	В	Α	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
Н	L	L	L	L	Н															
Н	L	L	L	Н		Н														
Н	L	L	Н	L			Н													
Н	L	L	H	Н				Н												
Н	L	Н	L	L					Н											
Н	L	Н	L	Н						Н										
Н	L	Н	Н	L							Н									
Н	L	Н	Н	Н								Н								
Н	Н	L	L	L									Н							
Н	Н	L	L	Н										Н						
Н	Н	L	Н	L											Н					
Н	Н	L	Н	Н												Н				
Н	Н	Н	L	L													Н			
Н	Н	Н	L	Н														Н		
Н	Н	Н	Н	L															Н	
Н	Н	Н	Н	Н																Н
L	Х	Χ	Χ	Χ	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L

TL/F/5846-1

## **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{SS}-V_{BB}$ ) 38V Input Current 10 mA Output Current -20 mA Storage Temperature  $-65^{\circ}$ C to  $+150^{\circ}$ C

Maximum Power Dissipation\* at 25°C

Molded Package 2168 mW Lead Temperature (Soldering, 4 sec.) 260°C

\*Derate molded package 17.35 mW/°C above 25°C.

## **Operating Conditions**

Min	Max	Units
5.0	9.5	V
Gnd	-26	V
0	+70	°C
	5.0 Gnd	5.0 9.5 Gnd –26

## Electrical Characteristics (Notes 2 and 3)

Symbol	Parameter		Cone	Min	Тур	Max	Units		
V <sub>IH</sub>	Logical "1"	V <sub>SS</sub> = Max	Enable	I <sub>IN</sub> = 260 μA				5.1	V
	Input Voltage		A, B, C, D	I <sub>IN</sub> = 1400 μA				1.5	V
I <sub>IH</sub>	Logical "1" Input Current	V <sub>SS</sub> = Max	Enable A, B, C			260	μА		
V <sub>IL</sub>	Logical "0"	V <sub>SS</sub> = Max	Enable			1.0	٧		
	Input Voltage				0.3	٧			
I <sub>IL</sub>	Logical "0"	V <sub>SS</sub> = Max	Enable	$V_{IN} = 0V$				-1.0	μΑ
	Input Current		A, B, C, D	$V_{IN} = V_{IL(MAX)}$		25			μΑ
V <sub>OH</sub>	Logical "1" Output Voltage	Digit Output, I	$_{OH} = -7  \text{mA}$			V <sub>SS</sub> - 2.5			٧
I <sub>OH</sub>	Logical "1" Output Current	$V_{SS} = Max, C$	Osc. Output, V <sub>RC</sub>			50	μΑ		
los	Output Short-Circuit Current	V <sub>SS</sub> = Min, Pi	in R, V <sub>RC</sub> = 0.6	$V, V_R = 0V$		-150		-450	μΑ
Rout	Output Pull-Down Resistor	V <sub>SS</sub> = Min, D	igit Output			30	50	85	kΩ
V <sub>OL</sub>	Logical "0" Output Voltage	V <sub>SS</sub> = Min	Osc	$V_{RC} = 1.6V$	$I_{OL} = 6  mA$			0.5	٧
			Pin R		I <sub>OL</sub> = 60 μA			0.2	V
		V <sub>SS</sub> = Max	Digit Output	V <sub>ENABLE</sub> = 1V	$I_{OL} = 10 \mu A$			V <sub>BB</sub> + 1.4	V
I <sub>SS</sub>	Supply Current	$V_{SS} = 9.5V$ , $I_{OH} = 0$ $V_{ENABLE} = 5.1V$ $V_{ENABLE} = 1V$					9.0	12.5	mA
							5.0	9.0	mA
I <sub>BB</sub>	Supply Current	$V_{SS} = 9.5V, I_B = 0,$ $V_{ENABLE} = 1V$					-0.8	-1.5	mA
		$V_{BB} = -26V$ (Note 4)	$I_{IN} = 300 \mu\text{A}$	V <sub>ENABLE</sub> = 5.1	V		-3.0	-5.0	mA
V <sub>B</sub>	Filament Bias Voltage	$I_B = 10 \text{ mA}$		V <sub>BB</sub> + 6.4	V <sub>BB</sub> + 6.9	V <sub>BB</sub> + 7.4	٧		

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

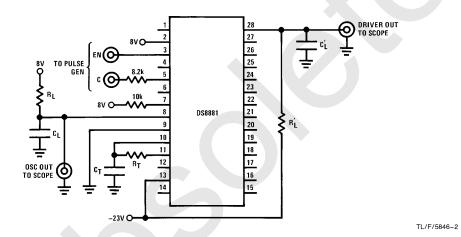
Note 2: Unless otherwise specified, min/max limits apply across the  $0^{\circ}\text{C}$  to  $+70^{\circ}\text{C}$  range. All typicals are given for  $T_{\text{A}}=25^{\circ}\text{C}$ .

Note 3: All currents into device pins shown as positive, out of device pins as negative, and all voltages referenced to ground unless otherwise noted. All values shown as max or min on absolute value basis.

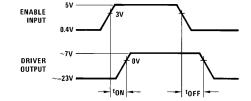
Note 4: Approximately 50% of input current on pins 4, 5, 6, 7 is shunted to V<sub>BB</sub>. If minimum I<sub>BB</sub> is desired, then I<sub>IN</sub> should be minimized by using resistors in series with the inputs.

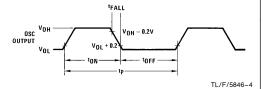
Switching Characteristics T <sub>A</sub> = 25°C unless otherwise specified											
Symbol	Parameter	Conditions	Min	Тур	Max	Units					
t <sub>pd0</sub>	Propagation Delay to a Logical "0" from Enable Input to Digit Output				1	μs					
t <sub>pd0</sub>	Propagation Delay to a Logical "0" A, B, C, D to Digit Output	$R_{I} = 4.7 \text{ k}\Omega$ , $C_{I} = 50 \text{ pF}$ , $V_{RR} = -23 \text{V}$ , $V_{SS} = 8 \text{V}$			1	μs					
t <sub>pd1</sub>	Propagation Delay to a Logical "1" from Enable Input to Digit Output	11, 4.7 M22, OL 30 P1, VBB 25V, V55			300	ns					
t <sub>pd1</sub>	Propagation Delay to a Logical "1" from A, B, C, D to Digit Output				500	ns					
t <sub>FALL</sub>	Oscillator Output Transition Time from 1 to 0	$V_{SS} = 9.5V$ , $R_L = 6k$ to $V_{SS}$ , $C_L = 25$ pF			50	ns					
fosc	Oscillator Frequency	7V < V <sub>SS</sub> < 9.5V, R <sub>T</sub> = 27 k $\Omega$ ±2%, R <sub>L</sub> = 1.3k,		360	400	kHz					
dc	Oscillator Duty Cycle	$C_T = 100 \text{ pF}, \pm 5\%, C_L = 50 \text{ pF}$	46	56	66	%					

## **AC Test Circuit**



# Switching Time Waveforms

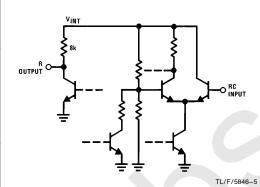


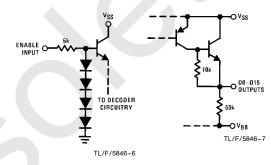


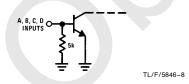
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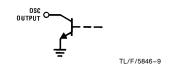
 $\begin{array}{ll} \mbox{Duty Cycle} & = \frac{t_{ON}}{t_p} \\ \\ \mbox{Frequency} & = \frac{1}{t_p} \end{array}$ 

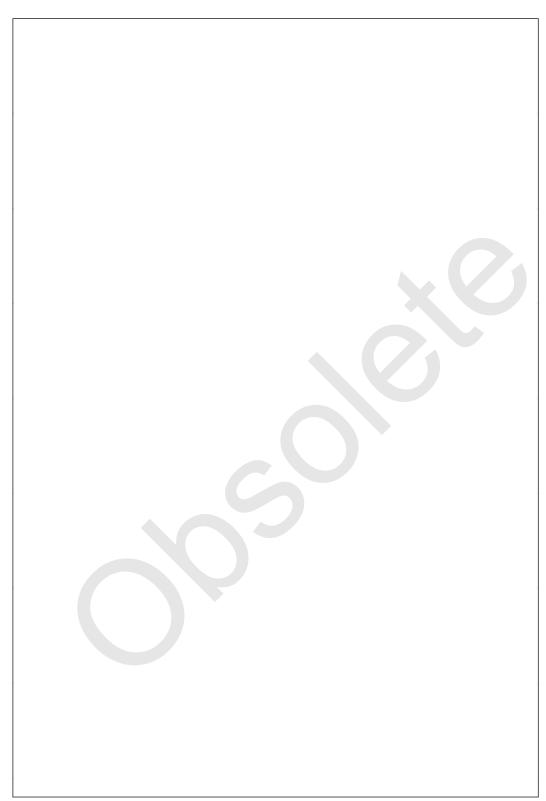
## **Input-Output Schematics**











#### Physical Dimensions inches (millimeters) 28 27 26 25 24 23 22 21 20 19 18 17 16 15 0.510 ± 0.005 (+) (+) $(12.95 \pm 0.127)$ 7 8 9 10 11 12 13 14 PIN NO. 1 IDENT 0.050 TYP 0.030 (0.762) $\frac{0.145 - 0.210}{(3.683 - 5.334)}$ 0.600 - 0.6200.125 - 0.165 (3.175 - 4.191) 0.020 MIN (15.24 - 15.75) (1.270) (0.508)0.009 - 0.015(0.229 - 0.381) 0.580 0.050 ± 0.015 (14.73) 0.125 - 0.145 $(1.270 \pm 0.381)$ (2 540 ± 0 254) (0.457±0.076) 0.625 + 0.025 - 0.015 $(15.88 + 0.635 \\ -0.381)$ Molded Dual-In-Line Package (N) **Order Number DS8881N** NS Package Number N28B

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