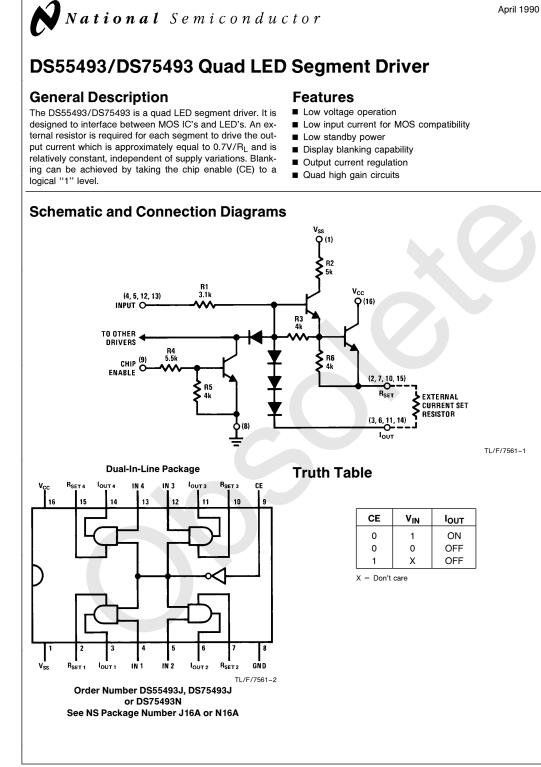
# DS55493,DS75493

DS55493 DS75493 Quad LED Segment Driver



Literature Number: SNOSBR6A



DS55493/DS75493 Quad LED Segment Driver

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RRD-B30M105/Printed in U. S. A.

### Absolute Maximum Ratings (Note 1)

Input Voltage

Output Voltage

Storage Temperature Range

Maximum Power Dissipation\* at 25°C

Lead Temperature (Soldering, 4 seconds)

Output Current (IOUT)

Cavity Package

Molded Package

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. Supply Voltage

## **Operating Conditions**

	Min	Max	Units
Supply Voltage			
V <sub>CC</sub>	3.2	8.8	V
V <sub>SS</sub>	6.5	8.8	V
Temperature T <sub>A</sub>			
DS75493	0	+70	°C
DS55493	-55	+ 125	°C

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\*Derate cavity package 9.14 mW/°C above 25°C; derate molded package 10.24 mW/°C above 25°C.

## Electrical Characteristics (V\_{SS} $\geq$ V\_{CC}) (Notes 2 and 3)

Symbol Parameter		Co	Conditions			Max	Units
I <sub>IN</sub>	Input Current	$V_{SS} = Max, V_{IN} = 8.8V, V_{CC} = Open, V_{CE} = 0V$				3.2	mA
		I <sub>OUT</sub> = R <sub>SET</sub> @ 0V, V <sub>CE</sub> = 8.8V				3.6	mA
I <sub>CE</sub>	Chip Enable Input Current	$V_{CC}$ = Max, $V_{SS}$ = Max, $V_{CE}$ = 8.8V, All Other Pins to GND				2.1	mA
lout	Output Current	$I_{OUT}$ @ 2.15V, $R_L = 50\Omega$	$\label{eq:VCC} \begin{split} V_{CC} &= \text{Min}, \text{V}_{SS} = 6.5\text{V},\\ \text{I}_{CE} &= 80 \; \mu\text{A}, \text{V}_{IN} = 6.5\text{V}\\ \text{Through } 1.0 \; \text{k}\Omega \end{split}$	-8	-13		mA
			$V_{CE} = 0V, V_{IN} = 8.8V$		-16	-20	mA
I <sub>OL</sub>	Output Leakage Current	$\begin{split} I_{OUT} &= R_{SET} @ 0V, \\ Measure Current to Gnd, \\ V_{SS} &= 8.8V \end{split}$	$\label{eq:VCC} \begin{array}{l} V_{CC} = \mbox{Min}, \mbox{V}_{CE} = \mbox{0V} \\ V_{IN} = \mbox{8.8V Through} \\ 100 \mbox{ k} \Omega \end{array}$			-200	μΑ
			$V_{CE} = 6.5V$ Though 1.0 k $\Omega$ , $V_{IN} = 8.8V$			-100	μA
ICC	Supply Current, V <sub>CC</sub>	$V_{CC} = Max, V_{SS} = Max$ , All Other Pins to Gnd				40	μA
I <sub>SS</sub>	Supply Current	$V_{CC} = 0V$ , All Other Pins to Gnd				40	μA
		$V_{CC} = Min, V_{SS} = 8.8V$	$\label{eq:lour_lour} \begin{array}{l} I_{OUT} @ 2.15 V, V_{CE} = 8.8 V \\ Through 100 k\Omega, \\ R_L = 50 \Omega \end{array}$		0.5	1.5	mA
			$I_{OUT} = Open, R_{SET} = Open, V_{CE} = 0V$			1.4	mA

10V

10V  $v_{\text{CC}}$ 

-25 mA

1371 mW

1280 mW

260°C

-65°C to +150°C

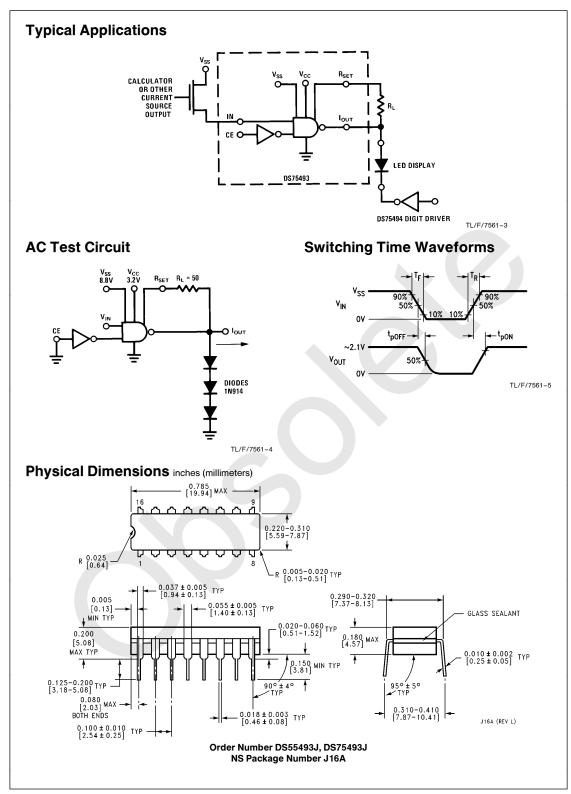
Switching Characteristics  $T_A = 25^{\circ}C$ , nominal power supplies unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t <sub>pd</sub> (OFF)	Propagation Delay to a Logical "0" From Input to Output	(See AC Test Circuit		170	300	ns
t <sub>pd(ON)</sub>	Propagation Delay to a Logical "1" From Input to Output	(See AC Test Circuit)		11	100	ns

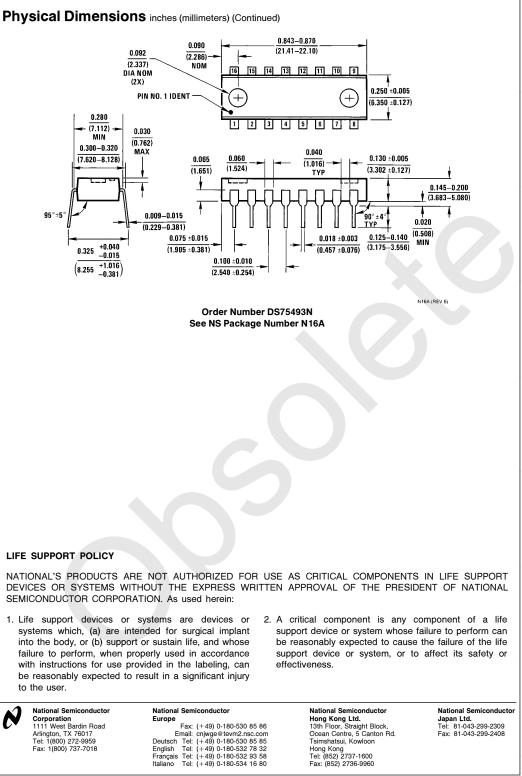
Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: Unless otherwise specified min/max limits apply across the 0°C to +70°C range for the DS75493 and across the -55°C to +125°C range for the DS55493.

Note 3: All currents into device pins shown as positive, out of device pins as negative, all voltages referenced to ground unless otherwise noted. All values shown as max or min on absolute value basis.







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