DS75110A

DS75110A Dual Line Drivers



Literature Number: SNOSC36A

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DS75110A Dual Line Drivers

General Description

The DS75110A is a dual line driver with independent channels, common supply and ground terminals featuring constant current outputs. These drivers are designed for optimum performance when used with the DS75107, DS75108 line receivers.

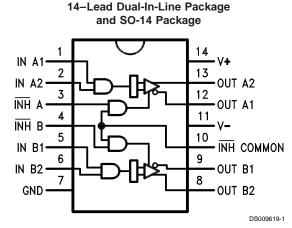
The output current of the DS75110A is nominally 12 mA and may be switched to either of two output terminals with the appropriate logic levels at the driver input.

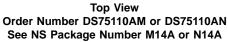
Separate or common control inputs are provided for increased logic versatility. These control or inhibit inputs allow the output current to be switched off (inhibited) by applying low logic levels to the control inputs. The output current in the inhibit mode, $I_{O(Off)}$, is specified so that minimum line loading is induced. This is highly desirable in system applications using party line data communications.

Features

- Improved stability over supply voltage and temperature ranges
- Constant current, high impedance outputs
- High speed: 15 ns max propagation delay
- Standard supply voltages
- Inhibitor available for driver selection
- High common mode output voltage range (-3.0V to 10V)
- TTL input compatibility

Connection Diagram





Function Table

| Inputs | | | | Outputs | | |
|--------|-----|-----------|-----|---------|-------|--|
| Lo | gic | Inhibitor | | 1 | | |
| 1 | 2 | A/B | ĪNH | A1/B1 | A2/B2 | |
| Х | Х | L | Х | Off | Off | |
| Х | Х | Х | L | Off | Off | |
| L | Х | Н | Н | Off | On | |
| Х | L | Н | Н | Off | On | |
| Н | Н | Н | Н | On | Off | |

H = High L = Low

X = Don't Care

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Absolute Maximum Ratings (Note 2)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

| Storage Temperature Range | |
|----------------------------------|-----------------|
| Ceramic DIP | –65°C to +175°C |
| Molded DIP and SO-14 | –65°C to +150°C |
| Lead Temperature | |
| Ceramic DIP (Soldering, 60 sec.) | 300°C |
| | |

| Molded DIP and SO-14 (Soldering, 10 sec.) Maximum Power Dissipation (Note 1) at 25°C | 265°C |
|--|---------------|
| Molded Package | 1040 mW |
| SO Package | 930 mW |
| Suppy Voltage | ±7.0V |
| Input Voltage (Any Input) | 5.5V |
| Output Voltage (Any Output) - | -5.0V to +12V |

Recommended Operating Conditions

| | | DS75110A | | |
|--|-------|----------|-------|----|
| | Min | Тур | Мах | |
| Positive Supply Voltage (V ⁺) | 4.75 | 5.0 | 5.25 | V |
| Negative Supply Voltage (V ⁻) | -4.75 | -5.0 | -5.25 | V |
| Positive Common Mode Voltage (V _{CM+}) | 0 | | 10 | V |
| Negative Common Mode Voltage (V _{CM}) | 0 | | -3.0 | V |
| Operating Temperature (T _A) | 0 | 25 | 70 | °C |

Note 1: Derate molded DIP package 8.3 mW/°C above 25°C, derate SO package 7.5 mW/°C above 25°C.

Electrical Characteristics (Notes 3, 4)

Over recommended operating temperature range, unless otherwise specified.

| Symbol | Paramet | er | Conditions | Min | Тур | Max | Units |
|---------------------|-------------------------|-----------|--|-----|------|------|-------|
| V _{IH} | Input Voltage HIGH | | | 2.0 | | | V |
| V _{IL} | Input Voltage LOW | | | | | 0.8 | V |
| V _{IC} | Input Clamp Voltage | | $V_{CC} = Min, I_1 = -12 mA$ | | -0.9 | -1.5 | V |
| I _{O(On)} | On-State | | $V_{\rm CC}$ = Max, $V_{\rm O}$ = 10V | | 12 | 15 | mA |
| . , | Output Current | | $V_{CC} = Min, V_O = -3.0V$ | 6.5 | 12 | | |
| I _{O(Off)} | Off-State Output Curre | nt | $V_{\rm CC}$ = Min, $V_{\rm O}$ = 10V | | | 100 | μA |
| | (Inhibited Only) | | | | | | |
| l _i | Input Current | A, B or C | $V_{CC} = Max, V_1 = 5.5V$ | | | 1.0 | |
| | At Maximum | Inputs | | | | | mA |
| | Input Voltage | D Input | - | | | 2.0 | |
| I _{IH} | Input Current HIGH | A, B or C | $V_{CC} = Max, V_1 = 2.4V$ | | | 40 | |
| | | Input | | | | | μA |
| | | D Input | | | | 80 | 1 |
| I _{IL} | Input Current LOW | A, B or C | $V_{\rm CC}$ = Max, $V_{\rm I}$ = 0.4V | | | -3.0 | |
| | | Input | | | | | mA |
| | | D Input | - | | | -6.0 | |
| l+ _(On) | Positive Supply Curren | t | V _{CC} = Max, | | 23 | 35 | mA |
| | with Driver Enabled | | A & B Inputs at 0.4V, | | | | |
| I- _(On) | Negative Supply Current | | C & D Inputs at 2.0V | | -34 | -50 | mA |
| | with Driver Enabled | | | | | | |
| I+ _(Off) | Positive Supply Curren | t | V _{CC} = Max, | | 21 | | mA |
| | with Driver Inhibited | | A, B, C & D Inputs | | | | |
| I- _(Off) | Negative Supply Curre | nt | at 0.4V | | -17 | | mA |
| | with Driver Inhibited | | | | | | |

Note 2: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

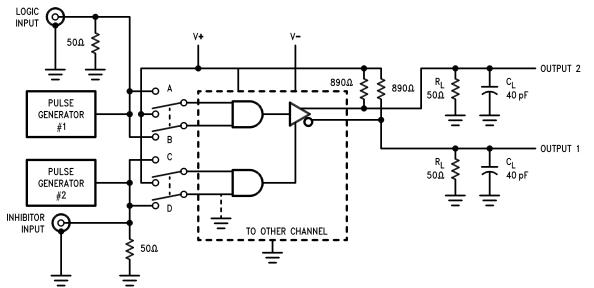
Note 3: Unless otherwise specified min/max limits apply across 0°C to +70°C range for the DS75110. All typicals are given for V_{CC} = 5V and T_A = 25°C.

Note 4: When using only one channel of the line drivers, the other channel should be inhibited and/or its outputs grounded.

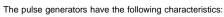
DS75110A

Switching Characteristics $V_{cc} = \pm 5V, T_A = 25^{\circ}C$

| Symbol | Parameter | Conditions | From | То | Min | Тур | Max | Units |
|------------------|-------------------------------------|-------------------------|---------|----------|-----|-----|-----|-------|
| | | | (Input) | (Output) | | | | |
| t _{PLH} | Propagation Delay Time, LOW to HIGH | C _L = 40 pF, | A or B | 1 or 2 | | 9.0 | 15 | ns |
| t _{PHL} | Propagation Delay Time, HIGH to LOW | $R_{L} = 50\Omega$ | | | | 9.0 | 15 | ns |
| t _{PLH} | Propagation Delay Time, LOW to HIGH | See Test Circuit | C or D | 1 or 2 | | 16 | 25 | ns |
| t _{PHL} | Propagation Delay Time, HIGH to LOW | | | | | 13 | 25 | ns |



DS009619-3



 $t_{r} = t_{f} = 10 \text{ ns } \pm 5.0 \text{ ns}, t_{w1} = 500 \text{ ns}, \text{PRR} = 1.0 \text{ MHz}, t_{W2} = 1.0 \text{ } \mu\text{s}, \text{PRR} = 500 \text{ } \text{kHz}, \text{ } \text{Z}_{O} = 50\Omega.$

C_L includes probe and jib capacitance.

For simplicity, only one channel and the inhibitor connections are shown.

FIGURE 1. AC Test Circuit

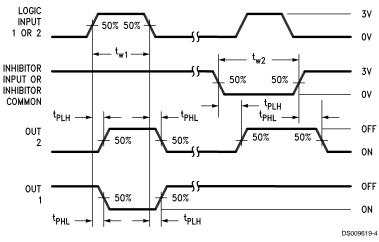
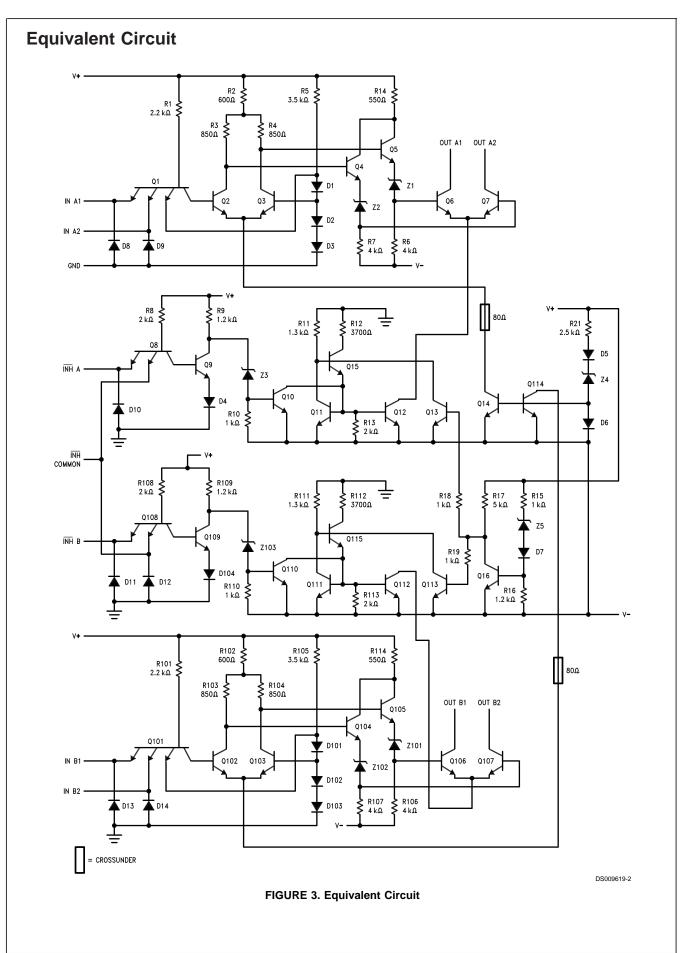


FIGURE 2. AC Waveforms

DS75110A



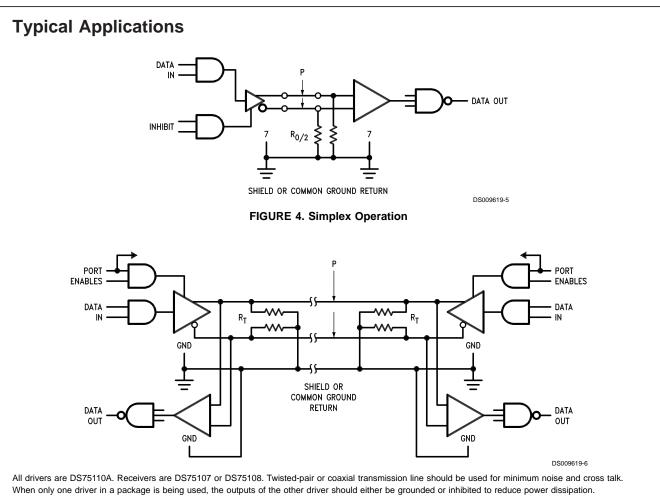
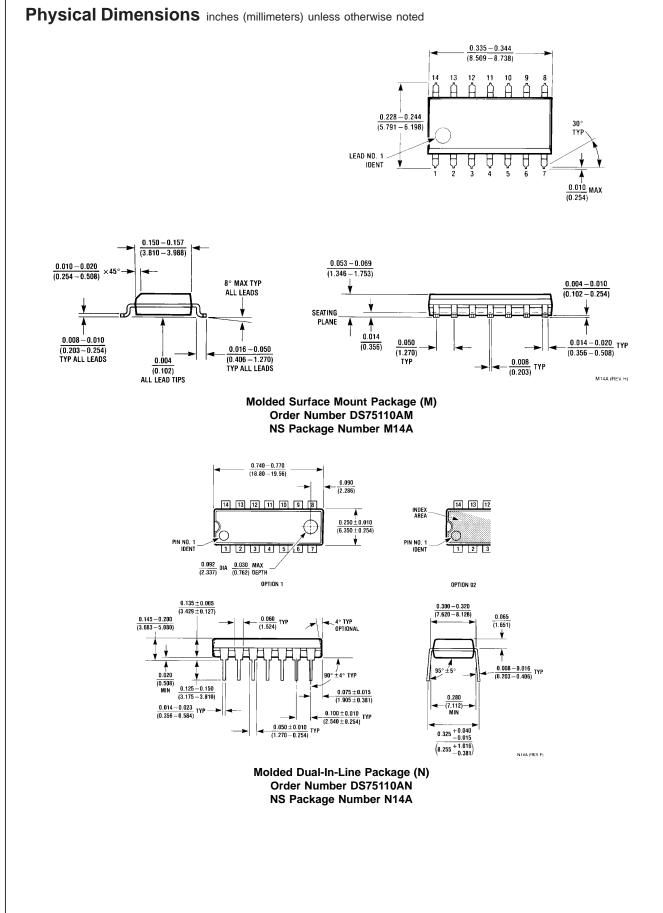


FIGURE 5. Half-Duplex Operation





Notes

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