DS3680 Quad Negative Voltage Relay Driver

General Description

The DS3680 is a quad high voltage negative relay driver designed to operate over wide ranges of supply voltage, common-mode voltage, and ambient temperature, with 50 mA sink capability. These drivers are intended for switching the ground end of loads which are directly connected to the negative supply, such as in telephone relay systems.

Since there may be considerable noise and IR drop between logic ground and negative supply ground in many applications, these drivers are designed to operate with a high common-mode range ($\pm 20V$ referenced to negative supply ground). Each driver has a common-mode range separate from the other drivers in the package, which pemits input signals from more than one element of the system.

With low differential input current requirements (typically 100 μ A), these drivers are compatible with TTL, LS and CMOS logic. Differential inputs permit either inverting or non-inverting operation.

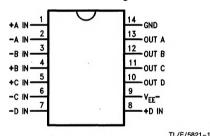
The driver outputs incorporate transient suppression clamp networks, which eliminate the need for external networks when used in applications of switching inductive loads. A fail-safe feature is incorporated to insure that, if the +IN input or both inputs are open, the driver will be OFF.

Features

- -10V to -60V operation
- Quad 50 mA sink capability
- TTL/LS/COMS or voltage comparator input
- High input common-mode voltage range
- Very low input current
- Fail-safe disconnect feature
- Built-in output clamp diode

Connection Diagram

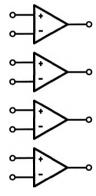
Dual-In-Line Package



Top View

Order Number DS3680J, DS3680M or DS3680N See NS Package Number J14A, M14A, N14A

Logic Diagram



TL/F/5821-2

Truth Table

Differential Inputs	Outputs		
$V_{ID} \ge 2V$	On		
$V_{ID} \leq 0.8V$	Off		
Open	Off		

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Office Distributors for availability and specifications.					
Supply Voltage (GND to V _{EE} -, and Any	Pin) -70V				
Positive Input Voltage (Input to GND)	20V				
Negative Input Voltage (Input to V _{EE} -)	-5V				
Differential Voltage (+IN to -IN)	±20V				
Inductive Load	L∟≤5h				
	I _L ≤50 mA				
Output Current	-100 mA				
Storage Temperature	-65°C to +150°C				

Maximum Power Dissipation* at 25°C

Cavity Package 1433 mW Molded Dip Package 1398 mW SO Package 1002 mW

Lead Temperature (Soldering, 4 seconds)

Recommended Operating Conditions

8.61-	Max	Units
MILL	Max	Units
10	-60	٧
-20	20	٧
2	20	٧
-20	0.8	٧
-25	+85	° C
	-20 2 -20	-10 -60 -20 20 2 20 -20 0.8

Electrical Characteristics (Notes 2 and 3)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
VIH	Logic "1" Input Voltage	0	2.0	1.3		٧
V _{IL}	Logic "0" Input Voltage			1.3	0.8	٧
INH	Logic "1" Input Current	V _{IN} = 2V V _{IN} = 7V		40 375	100 1000	μA μA
I _{INL}	Logic "0" Input Current	$V_{IN} = 0.4V$ $V_{IN} = -7V$		-0.01 -1	-5 -100	μΑ μΑ
V _{OL}	Output ON Voltage	I _{OL} = 50 mA		-1.6	-2.1	٧
OFF	Output Leakage	V _{OUT} = V _{EE} -		-2	-100	μΑ
I _{FS}	Fail-Safe Output Leakage	V _{OUT} = V _{EE} - (Inputs Open)		-2	-100	μА
l _{LC}	Output Clamp Leakage Current	V _{OUT} = GND		2	100	μΑ
v _C	Output Clamp Voltage	$I_{CLAMP} = -50 \text{ mA}$ Referenced to V_{EE}		-2	-1.2	٧
V _P	Positive Output Clamp Voltage	I _{CLAMP} = 50 mA Referenced to GND		0.9	1.2	٧
EE(ON)	ON Supply Current	All Drivers ON		-2	-4.4	mA
I _{EE(OFF)}	OFF Supply Current	All Drivers OFF		-1	-100	μΑ
t _{PD(ON)}	Propagation Delay to Driver ON	$L = 1h, R_L = 1k,$ $V_{IN} = 3V Pulse$		1	10	μs
t _{PD(OFF)}	Propagation Delay to Driver OFF	L = 1h, R _L = 1k, V _{IN} = 3V Pulse		1	10	μs

260°C

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range", they are not meant to imply that the device should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: Unless otherwise specified, the min/max limits of the table of "Electrical Characteristics" apply within the range of the table of "Operating Conditions". All typical values are given for V_{EE}⁻ = 52V, and T_A = 25°C.

Note 3: All current into device pins shown as positive, out of the device as negative. All voltages are referenced to ground unless otherwise noted.

Derate cavity package 9.6 mW/°C above 25°C; derate molded dip package 11.2 mW/°C above 25°C; derate SO package 8.02 mW/°C above 25°C.

Schematic Diagrams

