DS16149,DS16179,DS36149,DS36179

DS16149 DS36149 DS16179 DS36179 Hex MOS Drivers



Literature Number: SNOSBR3A



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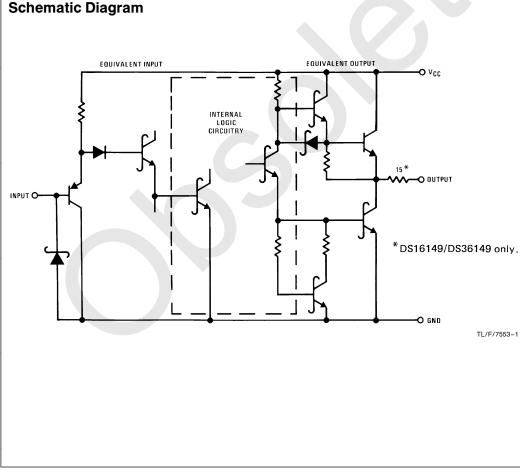
General Description

Features

- High speed capabilities
- Typ 9 ns driving 50 pF
- Typ 29 ns driving 500 pF
- **Built-in** 15 Ω damping resistor (DS16149/DS36149)
- Same pin-out as DM8096 and DM74366

The DS16149/DS36149 and DS16179/DS36179 are Hex MOS drivers with outputs designed to drive large capacitive loads up to 500 pF associated with MOS memory systems. PNP input transistors are employed to reduce input currents allowing the large fan-out to these drivers needed in memory systems. The circuit has Schottky-clamped transistor logic for minimum propagation delay, and a disable control that places the outputs in the logic "1" state (see truth table). This is especially useful in MOS RAM applications where a set of address lines has to be in the logic "1" state during refresh.

The DS16149/DS36149 has a 15 Ω resistor in series with the outputs to dampen transients caused by the fast-switching output. The DS16179/DS36179 has a direct low impedance output for use with or without an external resistor.



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Absolute Maximum	Ratings (Note 1)
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Logical "1" Input Voltage

Logical "0" Input Voltage

Cavity Package

Molded Package

Storage Temperature Range

Maximum Power Dissipation* at 25°C

Lead Temperature (Soldering 10 seconds)

If Military/Aerospace specified devices are required,
please contact the National Semiconductor SalesOffice/Distributors for availability and specifications.Supply Voltage7.0V

Operating Conditions

	Min	Max	Units
Supply Voltage (V _{CC})	4.5	5.5	V
Temperature (T _A)			
DS16149, DS16179	-55	+ 125	°C
DS36149, DS36179	0	+70	°C

DC Electrical Characteristics (Notes 2 and 3)

*Derate cavity package 9.1 mW/°C above 25°C; derate molded package 10.2 m/W°C above 25°C.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IN} (1)	Logical "1" Input Voltage			2.0			V
V _{IN} (0)	Logical "0" Input Voltage					0.8	V
I _{IN} (1)	Logical "1" Input Current	$V_{CC} = 5.5V, V_{IN} = 5.5V$			0.1	40	μA
I _{IN} (0)	Logical "0" Input Current	$V_{CC} = 5.5V, V_{IN} = 0.5V$			-50	-250	μA
V _{CLAMP}	Input Clamp Voltage	$V_{CC} = 4.5V, I_{IN} = -18 \text{ mA}$			-0.75	-1.2	V
V _{OH}	Logical "1" Output Voltage (No Load)	$V_{CC} = 4.5V, I_{OH} = -10 \mu A$	DS16149/DS16179	3.4	4.3		V
		ν _{CC} 4.5ν, ι _{OH} το μπ	DS36149/DS36179	3.5	4.3		V
V _{OL}	Logical "0" Output Voltage (No Load)	$V_{CC} = 4.5V, I_{OI} = 10 \mu A$	DS16149/DS16179		0.25	0.4	V
		ν ₍ , η) - το μπ	DS36149/DS36179		0.25	0.35	V
V _{OH}	Logical "1" Output Voltage (With Load)	$V_{CC} = 4.5V, I_{OH} = -1.0 \text{ mA}$	DS16149	2.4	3.5		V
			DS16179	2.5	3.5		V
			DS36149	2.6	3.5		V
			DS36179	2.7	3.5		V
V _{OL}	Logical "0" Output Voltage (With Load)		DS16149		0.6	1.1	V
		$V_{CC} = 4.5V, I_{OI} = 20 \text{ mA}$	DS16179		0.4	0.5	V
		V(() 4.0V, I() 20 III/	DS36149		0.6	1.0	V
		DS36179			0.4	0.5	V
I _{ID}	Logical "1" Drive Current	$V_{CC} = 4.5V, V_{OUT} = 0V$, (Note 4)			-250		mA
I _{OD}	Logical "0" Drive Current	V _{CC} = 4.5V, V _{OUT} = 4.5V, (Note 4)			150		mA
ICC	Power Supply Current	Power Supply Current V _{CC} = 5.5V Disable Inputs = All Other Inputs =			33	60	mA
		All Inputs = 0V		14		20	mA

7.0V

-1.5V

1371 mW

1280 mW

300°C

 $-65^{\circ}C$ to $+150^{\circ}C$

Switching Characteristics ($V_{CC} = 5V, T_A = 25^{\circ}C$) (Note 4)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
t _{S±}	Storage Delay Negative Edge	(Figure 1)	$C_L = 50 pF$		4.5	7	ns
		(, ,gui e ,)	$C_L = 500 pF$		7.5	12	ns
ts∓	Storage Delay Positive Edge	(Figure 1)	$C_L = 50 \text{ pF}$		5	8	ns
		(igure i)	$C_L = 500 pF$		8	13	ns
t⊨	Fall Time	(Figure 1)	$C_L = 50 pF$		5	8	ns
		(iguro i)	C _L = 500 pF		22	35	ns

Symbol	Parameter	Cor	nditions	Min	Тур	Max	Units
t _R	Rise Time	(Figure 1)	$C_L = 50 pF$		6	9	ns
			$C_L = 500 pF$		26	35	ns
t _{LH}	Delay from Disable Input to Logical ''1''	$\begin{array}{l} R_{L} = 2 \ k\Omega \ to \ Gnd, \\ C_{L} = 50 \ pF, (\textit{Figure 2}) \end{array}$			15	22	ns
t _{HL}	Delay from Disable Input to Logical ''0''	$R_{L} = 2 k\Omega \text{ to } V_{CC},$ $C_{L} = 50 \text{ pF}, (Figure 3)$			11	18	ns

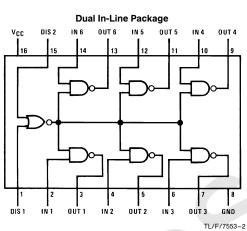
Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: Unless otherwise specified min/max limits apply across the -55° C to $+125^{\circ}$ C temperature range for the DS16149 and DS16179 and across the 0°C to $+70^{\circ}$ C range for the DS36149 and DS36179. All typical values are for T_A = 25^{\circ}C and V_{CC} = 5V.

Note 3: All currents into device pins shown as positive, out of device pins as negative, all voltages referenced to ground unless otherwise noted. All values shown as max or min on absolute value basis.

Note 4: When measuring output drive current and switching response for the DS16179 and DS36179 a 15 Ω resistor should be placed in series with each output. This resistor is internal to the DS16149/DS36149 and need not be added.

Connection Diagram

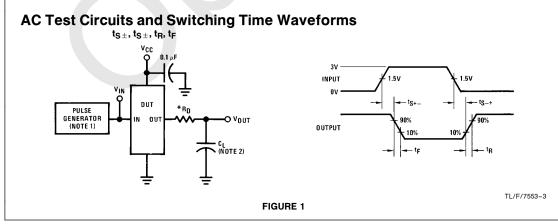


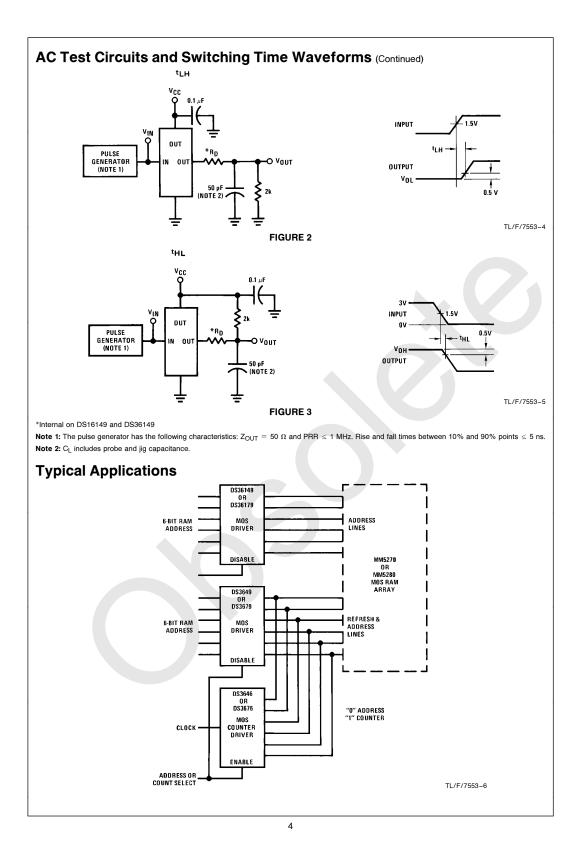
Top View Order Number DS16149J, DS36149J, DS16179J, DS36179J, DS36149N or DS36179N See NS Package Number J16A or N16A

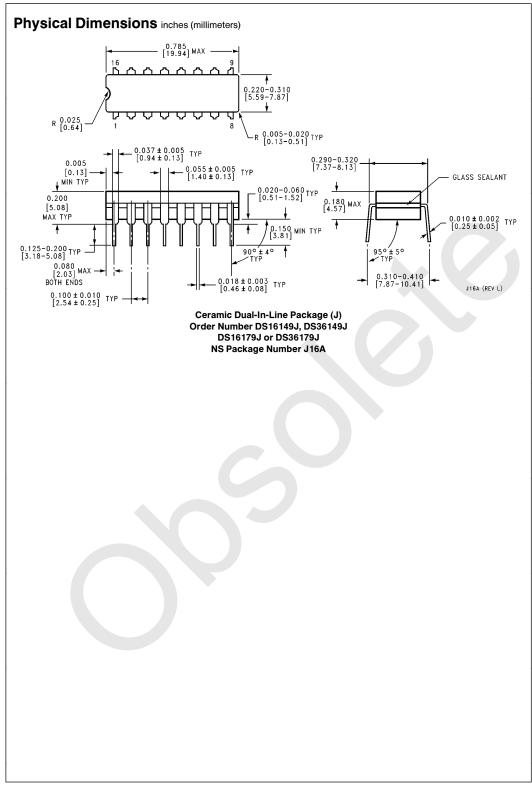
Truth Table

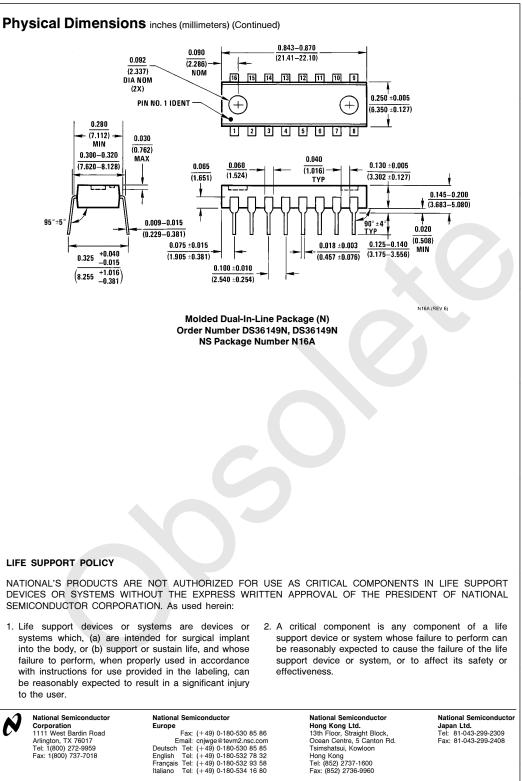
Disable Input		Input	Output
DIS 1	DIS2	mpar	output
0	0	0	1
0	0	1	0
0	1	Х	1
1	0	X	1
1	1	х	1

X = Don't care









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