

DS1688/DS1691 3 Volt/5 Volt Serialized Real Time Clock with NV RAM Control

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FEATURES

Incorporates industry standard DS1287 PC clock plus enhanced features:

- +3 or +5 V operation
- 64–bit Silicon serial number
- 64-bit customer specific ROM or additional serial number available
- Power control circuitry supports system power on from date/time alarm or key closure
- Automatic battery backup and write protection to external SRAM
- Crystal select bit allows RTC to operate with 6 pF or 12.5 pF crystal
- 114 bytes user NV RAM
- Auxiliary battery input
- RAM clear input
- Century register
- 32 kHz output for power management
- 32–bit V_{CC} powered elapsed time counter
- 32-bit V_{BAT} powered elapsed time counter
- 16–bit power cycle counter
- Compatible with existing BIOS for original DS1287 functions
- Available as IC (DS1688) or stand-alone module with embedded battery and crystal (DS1691)
- Timekeeping algorithm includes leap year compensation valid up to 2100

ORDERING INFORMATION

PART #	DESCRIPTION
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- DS1688S RTC IC, 28–pin SOIC
- DS1691 RTC Module; 28–pin DIP

PIN ASSIGNMENT

VIEW X CEE	, O	20 m m	VBAUK	1 28	CEI
X1 CBC	2	27 20 000		2 27	CED
×2 CEC	3	28 000 Voci	NC	3 26	Vcci
RCLA CEC	4	25 DE V000	ACLA .	4 25	Vcco
ADO CEL	8	24 000 SOW	ADO	5 24	SOW
AD1 CEC		23 DE VENT		6 23	NC
AD2 000	7	22 00 190	AD2	7 22	IBO
AD9 CE		21 DD PSEL		8 21	PSEL
AD4 CEC		20 10 10		9 20	RD
ADS CEL	10	19 0 OND	ADS	10 19 8	NC
AD6 CEC	11	18 🞞 Wit	AD6	11 18	WR
AD7 CEC	12	17 00 ALE	AD7	12 17	ALE
PWR CIC	13	16 DD CS	PWR	19 16	C8
GND EE	14	15 DO RB	GND	14 15	88
				10	no
			DS1691		VPSU-
	(330 M	68L)	LATED	PACKAGE (74	O MIL)

PIN DESCRIPTION

X1	- Crystal Input
X2	- Crystal Output
RCLR	- RAM Clear Input
AD0-AD7	- Mux'ed Address/Data Bus
PWR	- Power-on Interrupt Output
KS	- Kickstart Input
\overline{CS}	- RTC Chip Select Input
ALE	- RTC Address Strobe
WR	- RTC Write Data Strobe
RD	- RTC Read Data Strobe
V _{CCO}	- RAM Power Supply Output

- Interrupt Request Output IRQ SQW - Square Wave Output V_{CCI} - +3 or +5V Main Supply GND - Ground V_{BAT} - Battery + Supply - Auxiliary Battery Supply V_{BAUX} PSEL - +3 or +5V Power Select CEI - RAM Chip Enable In CEO - RAM Chip Enable Out

DESCRIPTION

The DS1688/DS1691 is a real time clock (RTC) designed as a successor to the industry standard DS1285, DS1385, DS1485, and DS1585 PC real time clocks. This device provides the industry standard DS1285 clock function with the new feature of either +3.0 or +5.0 volt operation and automatic backup and write protection to an external SRAM. The DS1688 also incorporates a number of enhanced features including a silicon serial number, power on/off control circuitry, 114 bytes of user NV SRAM, power on elapsed timer, and power cycle counter.

Each DS1688/DS1691 is individually manufactured with a unique 64–bit serial number as well as an additional 64–bit customer specific ROM or serial number. The serial number is programmed and tested at Dallas to insure that no two devices are alike. The serial number can be used to electronically identify a system for purposes such as establishment of a network node address or for maintenance tracking. Blocks of available numbers from Dallas Semiconductor can be reserved by the customer.

The serialized RTC's also incorporate power control circuitry, which allows the system to be powered on via an external stimulus, such as a keyboard or by a time and date (wake up) alarm. The \overrightarrow{PWR} output pin can be triggered by one or either of these events, and can be used to turn on an external power supply. The \overrightarrow{PWR} pin is under software control, so that when a task is complete, the system power can then be shut down.

The DS1688/DS1691 incorporates a power on elapsed time counter, a power on cycle counter, and a battery powered continuous counter. These three counters provide valuable information for maintenance and warranty requirements.

Automatic backup and write protection for an external SRAM is provided through the V_{CCO} and \overline{CEO} pins. The lithium energy source used to permanently power the real time clock is also used to retain RAM data in the absence of V_{CC} power through the V_{CCO} pin. The chip enable out-put to RAM (\overline{CEO}) is controlled during power transients to prevent data corruption.

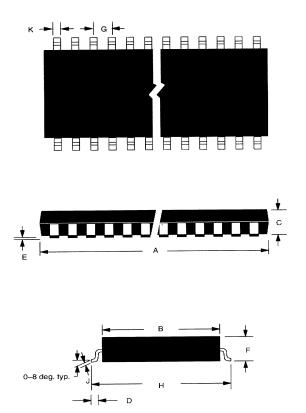
The DS1688 is a clock/calendar chip with the features described above. An external crystal and battery are the only components required to maintain time–of–day and memory status in the absence of power. The DS1691 incorporates the DS1688 chip, a 32.768 kHz crystal, and a lithium battery in a complete, self–contained timekeeping module. The entire unit is fully tested at Dallas Semiconductor such that a minimum of 10 years of timekeeping and data retention in the absence of V_{CC} is guaranteed.

For a complete description of operating conditions, electrical characteristics, bus timing, and pin descriptions other than the SQW output see the DS1689/DS1693 data sheet.

SIGNAL DESCRIPTION

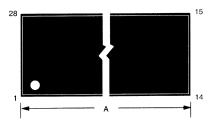
SQW (**Square Wave Output**) – The SQW output signal functions identical to the DS1689/DS1693 with an exception occurring at power–up. A 32 kHz square wave will be output on this pin, t_{REC} , after a power–up condition has been detected. This condition sets the following bits enabling the 32 kHz output; DV1=1, SQWE=1, and E32K=1. The square wave will be output on this pin if either SQWE=1 or E32K=1.

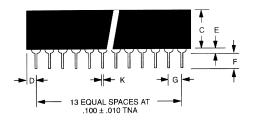
DS1688S 28-PIN SOIC

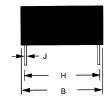


IAX .728 8.50 .350
.728 8.50
8.50
.350
8.90
.118
3.00
.050
1.27
.014
0.35
.120
3.05
BSC
.500
2.70
.013
0.32
.020
0.50

DS1691 28-PIN 740 MIL MODULE







PKG	28-PIN		
DIM	MIN	MAX	
A IN.	1.520	1.540	
MM	38.61	39.12	
B IN.	0.695	0.740	
MM	17.65	18.80	
C IN.	0.350	0.375	
MM	8.89	9.52	
D IN.	0.100	0.130	
MM	2.54	3.30	
E IN.	0.015	0.030	
MM	0.38	0.76	
F IN.	0.110	0.140	
MM	2.79	3.56	
G IN.	0.090	0.110	
MM	2.29	2.79	
H IN.	0.590	0.630	
MM	14.99	16.00	
J IN.	0.008	0.012	
MM	0.20	0.30	
K IN.	0.015	0.021	
MM	0.38	0.53	

Note: Pins 2, 3, 19 And 23 Are Missing By Design