

www.ti.com

#### SNLS081B-MAY 2004-REVISED SEPTEMBER 2004

# DS14C89A Quad CMOS Receiver

Check for Samples: DS14C89A

## **FEATURES**

- DESCRIPTION
- Meets EIA/TIA-232-E and CCITT V.28 Standards
- Failsafe Output High for Open Input
- **LOW Power Consumption**
- **On Chip Noise Filter**
- Available in SOIC Package

#### The DS14C89A, pin-for-pin compatible to the DS1489A/MC1489A, ia a quad receiver designed to interface data terminal equipment (DTE) with data circuit-terminating equipment (DCE). These devices translate levels conforming to EIA-232E and CCITT V.28 standards to TTL/CMOS logic levels.

The device is fabricated in low threshold CMOS metal gate technology. The device provides very low power consumption compared to their bipolar equivalents: 900 µA (DS14C89A) versus 26 mA (DS1489A).

The DS14C89A provides on chip noise filtering which eliminates the need for external response control filter capacitors. When replacing the DS1489A with the DS14C89A, the response control filter pins can be tied high, low, or not connected.

## **Connection Diagram**

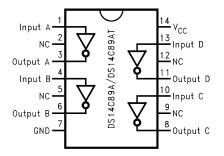


Figure 1. See Package Number D, NFF0014A



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet. All trademarks are the property of their respective owners.

# DS14C89A

SNLS081B-MAY 2004-REVISED SEPTEMBER 2004

TEXAS INSTRUMENTS

www.ti.com



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

## Absolute Maximum Ratings<sup>(1)(2)</sup>

V <sub>CC</sub>	+6V				
Input Voltage	-30V to +30V				
Receiver Output Voltage	(V <sub>CC</sub> ) +0.3V to GND-0.3V				
Junction Temperature	+150°C				
Continuous Power Dissipation @ +25°C <sup>(3)</sup>	NFF0014A Package	1513 mV			
	D Package	1063 mW			
Lead Temp.	+260°C				
Storage Temp. Range	-65°C to +150°C				
ESD Rating ≥ 1.8 kV, Typically ≥ 2 kV (HMB, 1.5 kΩ, 100 pF)					

(1) Absolute Maximum Ratings are those values beyond which the safety of the device cannot be ensured. They are not meant to imply that the devices should be operated at these limits. The tables of AC Electrical Characteristics specify conditions for device operation.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

(3) Derate NFF0014A Package 12.1 mW/°C, and D Package 8.5 mW/°C above +25°C.

### **Recommended Operating Conditions**

		Min	Max	Units
$V_{CC}$ (GND = 0V)		+4.5	+5.5	V
Operating Free Air Temp. (T <sub>A</sub> )	DS14C89A	0	+75	°C



SNLS081B-MAY 2004-REVISED SEPTEMBER 2004

www.ti.com

#### **Electrical Characteristics**

Over recommended operating conditions, unless otherwise specified

Symbol	Parameter		Conditions	Min	Тур	Max	Units
V <sub>TH</sub>	Input High Threshold			1.3		2.7	V
V <sub>TL</sub>	Input Low Threshold			0.5		1.9	V
V <sub>HY</sub>	Typical Input Hysteresis				1.0		V
I <sub>IN</sub> Input Current	Input Current	V <sub>IN</sub> = +25V	$V_{CC} = +4.5V$ to +5.5V	3.6		8.3	mA
		V <sub>IN</sub> = −25V		-3.6		-8.3	mA
		$V_{IN} = +3V$		0.43		1.0	mA
	$V_{IN} = -3V$		-0.43		-1.0	mA	
	V <sub>IN</sub> = +15V	$V_{IN} = +15V$ $V_{CC} = 0V (Power-Off)^{(1)}$			5.0	mA	
	V <sub>IN</sub> = −15V		-2.14		-5.0	mA	
	$V_{IN} = +3V$		0.43		1.0	mA	
		$V_{IN} = -3V$		-0.43		-1.0	mA
V <sub>OH</sub>	Output High Voltage	$V_{IN} = V_{TL}$ (min)	I <sub>OUT</sub> = −3.2 mA	2.8	4.0		V
			$I_{OUT} = -20\mu A$	3.5	4.7		V
V <sub>OL</sub> Output Low Voltage	Output Low Voltage	V <sub>IN</sub> = V <sub>TH</sub> (max)			0.45	0.4	V
		I <sub>OUT</sub> = +3.2 mA		0.15	0.4	V	
I <sub>CC</sub>	Supply Current	No Load, V <sub>IN</sub> = 2.7	V or 0.5V		0.5	900	μA

(1) Under the power-off supply conditions it is assumed that the power supply potential drops to zero (0V) and is replaced by a low impedance or short circuit to ground.

## AC Electrical Characteristics<sup>(1)</sup>

Over recommended operating conditions, unless otherwise specified,  $C_I = 50 \text{ pF}$ 

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t <sub>PLH</sub>	Propagation Delay Low to High	Input Pulse Width ≥ 10 µs		3.5	6.5	μs
t <sub>PHL</sub>	Propagation Delay High to Low	Input Pulse Width ≥ 10 µs		3.2	6.5	μs
t <sub>SK</sub>	Typical Propagation Delay Skew			400		ns
t <sub>r</sub>	Output Rise TIme			40	300	ns
t <sub>f</sub>	Output Fall Time			40	300	ns
t <sub>nw</sub>	Pulse Width assumed to be Noise				1.0	μs

(1) AC input waveforms for test purposes:  $t_r = t_f = 200$  ns,  $V_{IH} = +3V$ ,  $V_L = -3V$ , f = 20 KHz.



www.ti.com

SNLS081B-MAY 2004-REVISED SEPTEMBER 2004

#### **Parameter Measurement Information**

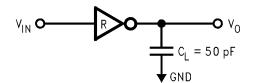


Figure 2. Receiver Load Circuit

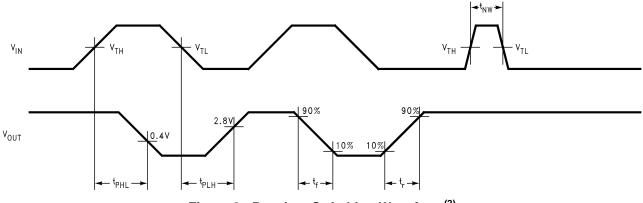


Figure 3. Receiver Switching Waveform<sup>(2)</sup>

(2) AC input waveforms for test purposes:  $t_r = t_f = 200$  ns,  $V_{IH} = +3V$ ,  $V_L = -3V$ , f = 20 KHz.

4 Submit Documentation Feedback

TTL

1/4 DS14C89A/DS14C89AT

1/4 DS14C88/DS14C88T

÷

DATA CIRCUIT TERMINATING EQUIPMENT (DCE)

## Figure 5. EIA-232D Data Transmission

SIGNAL GROUND

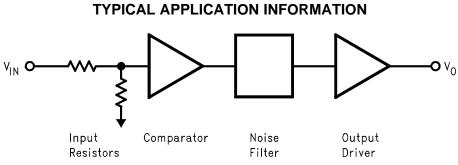


Figure 4. Receiver Block Diagram

INTERCONNECTING CABLE

TTL 1/4 DS14C88/DS14C88T

TTL 1/4 DS14C89A/DS14C89AT ⊞-∽J

Ē

K>

÷

Ξ

DATA TERMINAL EQUIPMENT (DTE)



www.ti.com



## PACKAGING INFORMATION

Orderable Device	Status	Package Type	•		Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
	(1)		Drawing			(2)		(3)		(4)	
DS14C89AM	ACTIVE	SOIC	D	14	55	TBD	Call TI	Call TI	0 to 70	DS14C89AM	Samples
DS14C89AM/NOPB	ACTIVE	SOIC	D	14	55	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	0 to 70	DS14C89AM	Samples
DS14C89AN	ACTIVE	PDIP	NFF	14	25	TBD	Call TI	Call TI	0 to 70	DS14C89AN	Samples
DS14C89AN/NOPB	ACTIVE	PDIP	NFF	14	25	Green (RoHS & no Sb/Br)	SN	Level-1-NA-UNLIM	0 to 70	DS14C89AN	Samples

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

<sup>(4)</sup> Only one of markings shown within the brackets will appear on the physical device.

**Important Information and Disclaimer:**The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

# **MECHANICAL DATA**

# NFF0014A





D (R-PDSO-G14)

PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AB.



#### **IMPORTANT NOTICE**

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have *not* been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products		Applications	
Audio	www.ti.com/audio	Automotive and Transportation	www.ti.com/automotive
Amplifiers	amplifier.ti.com	Communications and Telecom	www.ti.com/communications
Data Converters	dataconverter.ti.com	Computers and Peripherals	www.ti.com/computers
DLP® Products	www.dlp.com	Consumer Electronics	www.ti.com/consumer-apps
DSP	dsp.ti.com	Energy and Lighting	www.ti.com/energy
Clocks and Timers	www.ti.com/clocks	Industrial	www.ti.com/industrial
Interface	interface.ti.com	Medical	www.ti.com/medical
Logic	logic.ti.com	Security	www.ti.com/security
Power Mgmt	power.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video
RFID	www.ti-rfid.com		
OMAP Applications Processors	www.ti.com/omap	TI E2E Community	e2e.ti.com
Wireless Connectivity	www.ti.com/wirelessconne	ectivity	

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2013, Texas Instruments Incorporated