

# TTL MSI

## DM5483/DM7483(SN5483/SN7483) four-bit binary full adder and dual single-bit binary full adder

#### general description

The DM5483/DM7483 binary full adder adds two four-bit binary numbers. A carry input is included and four  $\Sigma$  outputs are provided along with the resultant carry. Since the carry-ripple-time is the limiting delay in the addition of a long word length, carry look-ahead circuitry has been included in the design to minimize this delay. Typical propagation delay from Carry-input to Carry output is 12 ns.

The device can also be used as a dual single-bit binary full adder. (See application.) In this application the  $\Sigma_2$  output is used as the CARRY output for BIT 1; and the  $A_3B_3$  inputs are used as the CARRY input for Bit 2.

It is completely compatible with other Series 54/74 devices.



## absolute maximum ratings

V <sub>cc</sub>		7V
Input Voltage		5.5V
Operating Temperature Range	DM7483	0°C to 70°C
	DM5483	55°C to +125°C
Storage Temperature Range		–65°C to +15 <b>0</b> °C
Lead Temperature (Soldering, 10 sec)		300°C

#### electrical characteristics (Note 1)

PARAMETER		CONDITION		ТҮР	МАХ	UNITS
Input Diode Clamp Voltage		$V_{CC} = 5.0V, T_A = 25^{\circ}C, I_{IN} = -12 \text{ mA}$			-1.5	v
Logical "1" Input Voltage	DM5483 DM7483	V <sub>CC</sub> = 4.5V V <sub>CC</sub> = 4.75V	2.0			v
Logical "O" Input Voltage	DM5483 DM7483	V <sub>CC</sub> = 4.5V V <sub>CC</sub> = 4.75V			0.8	v
Logical "1" Output Voltage	DM5483 DM7483	$V_{CC} = 4.5V$ $V_{IN} = 0.8V, I_{OUT} = -400 \mu\text{A}$ $V_{CC} = 4.75V$ (Note 3)	2.4			v
Logical "O" Output Voltage	DM5483 DM7483	$V_{CC} = 4.5V$ $V_{IN} = 2.0V, I_{OUT} = 16 \text{ mA}$ $V_{CC} = 4.75V$ (Note 3)			0.4	v
Logical "1" Input Current (all inputs)	DM5483 DM7483	$V_{cc} = 5.5V$ $V_{cc} = 5.25V$ $V_{IN} = 2.4V$			80	μA
Logical "1" Input Current	DM5483 DM7483	V <sub>CC</sub> = 5.5V V <sub>CC</sub> = 5.25V V <sub>IN</sub> = 5.5V			1	mA
Logical "0" Input Current (all inputs)	DM5483 DM7483	$V_{\rm CC} = 5.5V$ $V_{\rm CC} = 5.25V$ $V_{\rm IN} = 0.4V$			-3.2	mA
Output Short Circuit Current (Note 2) (except C <sub>4</sub> )	DM5483 DM7483	V <sub>cc</sub> = 5.5V V <sub>cc</sub> = 5.25V	-20 -18		-55	mA
Output Short Circuit Current (for C <sub>4</sub> )	DM5483 DM7483	V <sub>CC</sub> = 5.5V V <sub>CC</sub> = 5.25V	-27		-70	mA
Supply Current	DM5483 DM7483	V <sub>cc</sub> = 5.5V V <sub>cc</sub> = 5.25V		58	79	mA

# switching characteristics $v_{cc}$ = 5V, $T_A$ = 25°C

PARAMETER	FROM (INPUT)	TO (OUTPUT)	CONDITION	MIN	түр	MAX	UNITS
t <sub>pd 1</sub>	C	2	N = 10, C = 50 pF		23	34	ns
t <sub>pd0</sub>	UIN	21	N = 10, C = 50 pF		20	34	ns
t <sub>pd 1</sub>		6	N = 10, C = 50 pF		24	35	ns
t <sub>pd0</sub>	CIN	$\mathcal{L}_{2}$	N = 10, C = 50 pF		22	35	ns
t <sub>pd 1</sub>	<u> </u>	2	N = 10, C = 50 pF		30	50	ns
t <sub>pd0</sub>	CIN	23	N = 10, C = 50 pF		24	40	ns
t <sub>pd 1</sub>	0	Σ4	N = 10, C = 50 pF		30	50	ns
t <sub>pd0</sub>	CIN		N = 10, C = 50 pF		28	50	ns
t <sub>pd 1</sub>	<u> </u>	C₄	N = 5, C = 50 pF		12	20	ns
t <sub>od 0</sub>	CIN		N = 5, C = 50 pF		12	20	ns
t <sub>pd 1</sub>		2 Σ2	N = 10, C = 50 pF			40	ns
t <sub>pd0</sub>			N = 10, C = 50 pF			35	ns
t <sub>pd 1</sub>	A	7	N = 10, C = 50 pF			40	ns
t <sub>pd</sub> 0		<i>2</i> 4	N = 10, C = 50 pF			35	ns

Note 1: Min/Max limits apply across the guaranteed temperature range of 0°C to 70°C for the DM7483 and -55°C to +125°C for the DM5483 unless otherwise specified. All typicals are given for  $V_{CC}$  = 5.0V and  $T_A$  = 25°C

Note 2: Only one output at a time should be short circuited.

Note 3: For C<sub>4</sub> output,  $I_{OUT(1)} = -200 \ \mu A$ ,  $I_{OUT(0)} = 8 \ m A$ .

#### typical application

Connect the DM5483/DM7483 in the following manner to implement a dual single-bit full adder.



connection diagram



truth table (See Note 1)

