

## TTL MSI

# DM7210/DM8210 eight channel digital switch DM7211/DM8211 eight channel digital switch

### general description

The DM7210/DM8210 and DM7211/DM8211 are digital bipolar integrated circuits employing TTL, used to multiplex eight INPUT channels to a single OUTPUT. Depending upon the 3-bit binary number applied to the SELECT lines, the digital bit on the unique INPUT selected appears on the output.

The DM7211/DM8211 provides a strobe input which when taken to a logical "1" level places the output in the logical "1" state.

The circuit can also be used to convert parallel input information to serial output information. If

eight bits of parallel information are applied to the inputs, and if the binary numbers 000 through 111 are sequenced on the select lines, the output will provide a serial presentation of the input bits. Key features include:

- TTL Circuitry
- Input Clamping Diodes
- 1 Volt Typical Noise Immunity
- 400 mV Guaranteed Noise Immunity

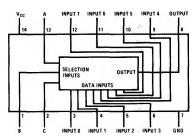
The devices are completely compatible with Series 54/74 circuits.

#### logic table

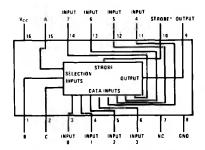
SELECTION INPUTS			STROBE	DATA INPUTS								
С	В	A	(DM 7211/DM8211 ONLY)	0	1	2	3	4	5	6	7	OUTPUT
0	0	0	0	0	×	Х	×	X	×	х	х	0
0	0	0	0	1	X	X	X	X	×	X	X	1
0	0	1	0	×	0	х	х	x	` X	х	х	0
0	0	1	0	×	1	×	X	×	х	х	X	1
0	1	0	0	×	х	0	х	х	×	х	x	0
0	1	0	0	×	x	1	×	х	×	X	X	1
0	1	1	0	×	х	х	0	х	×	х	x	0
0	1	1	0	X	х	х	1	X	X	х	X	1
1	0	0	0	×	×	X	×	0	×	х	X	0
1	0	0	0	X	X	X	х	1	×	х	×	1
1	0	1	0	х	×	×	×	×	0	×	x	0
1	0	1	0	х	X	X	X	X	1	X	X	1
1	1	0	0	×	х	х	х	X	×	0	х	0
1	1	0	0	X	×	X	×	X	×	1	X	1
1	1	1	0	х	х	Х	×	Х	×	х	0	0
1	1	1	0	×	х	X	X	Х	X	X	1	1
X	X	X	1	X	х	×	X	х	х	×	X	1

X = "Don't Care" Condition

#### connection diagrams



DM7210/DM8210

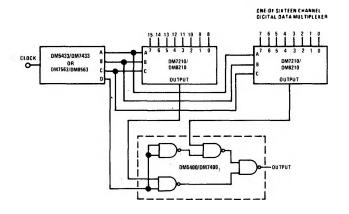


"A Logical 1 on the strobe input causes the autgut to go the Logical 1 state.

A Logical 0 on the strobe input allows information to be routed through the device

#### DM7211/DM8211

## typical application



### absolute maximum ratings

Supply Voltage Input Voltage Fanout 7V 5.5V 10

Storage Temperature Range Operating Temperature Range

DM7210, DM7211 DM8210, DM8211 -65°C to +150°C -55°C to +125°C 0°C to +70°C

Lead Temperature (soldering, 10 sec)

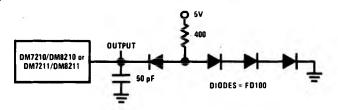
300°C

#### electrical characteristics (Note 1)

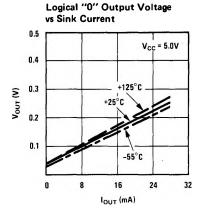
PARAMETER		CONDITION	MIN	TYP	MAX	UNITS
Logical "1" Input Voltage	DM7210/DM7211 DM8210/DM8211	V <sub>CC</sub> = 4.5V V <sub>CC</sub> = 4.75V	2.0			V
Logical "0" Input Voltage	DM7210/DM7211 DM8210/DM8211	V <sub>CC</sub> = 4.5V V <sub>CC</sub> = 4.75V			0.8	٧
Logical "1" Output Voltage	DM7210/DM7211 DM8210/DM8211	$V_{CC} = 4.5V$ $V_{CC} = 4.75V$ $I_{OUT} = -400 \mu\text{A}$	2.4			٧
Logical "0" Output Voltage	DM7210/DM7211 DM8210/DM8211	$V_{CC} = 4.5V$ $V_{CC} = 4.75V$ $I_{OUT} = 16 \text{ mA}$			0.4	٧
Logical "1" Input Current (All Inputs)	DM7210/DM7211 DM8210/DM8211	$\frac{V_{CC} = 5.5V}{V_{CC} = 5.25V}$ $V_{IN} = 2.4V$			40	μΑ
Logical "1" Input Current (All Inputs)	DM7210/DM7211 DM8210/DM8211	$V_{CC} = 5.5V$ $V_{CC} = 5.25V$ $V_{IN} = 5.5V$			1	mA
Logical "0" Input Current (All Inputs)	DM7210/DM7211 DM8210/DM8211	$V_{CC} = 5.5V$ $V_{CC} = 5.25V$ $V_{IN} = 0.4V$		-1.0	-1.6	mA
Input Clamp Diode (All Inputs)	DM7210/DM7211 DM8210/DM8211	$V_{CC} = 5.5V$ $V_{CC} = 5.25V$ $I_{IN} = -12 \text{ mA, } T_A = 25^{\circ}C$		-1.0	-1.5	V
Output Short Circuit Current	DM7210/DM7211 DM8210/DM8211	$V_{CC} = 5.5V$ $V_{CC} = 5.25V$ $V_{OUT} = 0$	-20 -18		-55	mA
Power Supply Current (All Inputs GND)	DM7210/DM7211 DM8210/DM8211	V <sub>CC</sub> = 5.5V V <sub>CC</sub> = 5.25V		20	33	mA
Propagation Delay to a Logica From Data Input to Output, t		V <sub>CC</sub> = 5.0V, T <sub>A</sub> = 25°C	10	21	30	ns
Propagation Delay to a Logica From Strobe Input to Output		V <sub>CC</sub> = 5.0V, T <sub>A</sub> = 25°C	10	19	27	ns
Propagation Delay to a Logica From Data Input to Output, t		$V_{CC} = 5.0V, T_A = 25^{\circ}C$	10	23	32	ns
Propagation Delay to a Logica From Strobe Input to Output		V <sub>CC</sub> = 5.0V, T <sub>A</sub> = 25°C	10	21	30	ns
Data Selection Settling Time F $0 \rightarrow 1$ Transition on A, B, C ( $t_s$		V <sub>CC</sub> = 5.0V, T <sub>A</sub> = 25°C	15	31	43	ns
Data Selection Settling Time F $1\rightarrow 0$ Transition on A, B, C ( $t_s$ )		V <sub>CC</sub> = 5.0V, T <sub>A</sub> = 25°C	15	31	42	ns

Note 1: Unless otherwise specified the min-max limits apply across the  $-55^{\circ}$ C to  $+125^{\circ}$ C temperature range for the DM7210 and DM7211 and across the  $0^{\circ}$ C to  $70^{\circ}$ C temperature range for the DM8210 and DM8211. Typicals are given for  $V_{CC} = 5.0V$  and  $25^{\circ}$ C.

#### ac test circuit

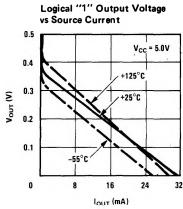


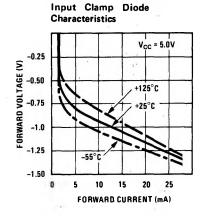
#### typical performance characteristics



Transition Time to a Logical

"1" from Strobe Input to Out-

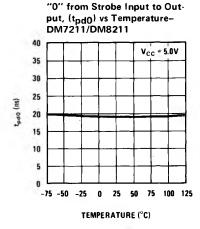




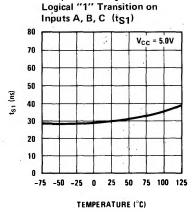
put, (tpd1) vs Temperature –
DM7211/DM8211

40
35
30
25
20
-75 -50 -25 0 25 50 75 100 125

TEMPERATURE (°C)

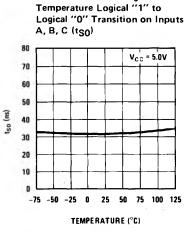


Transition Time to a Logical

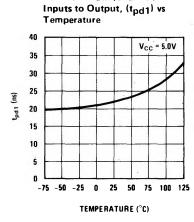


Data Selection Settling Time vs

Temperature Logical "0" to

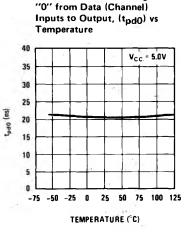


**Data Selection Settling Time vs** 



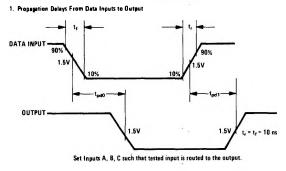
Transition Time to a Logical

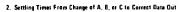
"1" from Data (Channel)

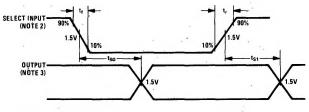


Transition Time to a Logical

## switching time waveforms







Data Inputs: Connected in any logic configuration

Note 2: When the select inputs are taken to opposite logical levels simultaneously, the one (ones) making the Logical "1" to Logical "0" transition growide the worst-case path.

Note 3: Transition times specified are independent of the direction of the output waveform.