

June 2012

D45H11 PNP Power Amplifier

- This device is designed for power amplifier, regulator and switching circuits where speed is important.
- · Sourced from process 5Q.



1. Base 2. Collector 3. Emitter

Absolute Maximum Ratings

Symbol	Parameter	Value	Units	
V_{CEO}	Collector-Emitter Voltage	-80	V	
I _C	Collector Current - Continuous	-10	А	
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150 °C		

Electrical Characteristics $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Character	istics		•	•	
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	$I_C = -100 \text{mA}, I_B = 0$	-80		V
I _{CBO}	Collector-Cutoff Current	$V_{CB} = -80V, I_{E} = 0$		-10	μΑ
I _{EBO}	Emitter-Cutoff Current	$V_{EB} = -5V, I_{C} = 0$		-100	μΑ
On Character	istics *				
h _{FE}	DC Current Gain	V _{CE} = -1V, I _C = -2A V _{CE} = -1V, I _C = -4A	60 40		
V _{CE (sat)}	Collector-Emitter Saturation Voltage	$I_C = -8A, I_B = -0.4A$		-1.0	V
V _{BE (sat)}	Base-Emitter Saturation Voltage	$I_C = -8A, I_B = -0.8A$		-1.5	V
V _{BE (on)}	Base-Emitter On Voltage	$V_{CE} = -2V, I_{C} = -10mA$	-0.54	-0.65	V
	Characteristics		•		
f _T	Current Gain Bandwidth Product	$I_C = -500 \text{mA}, V_{CE} = -10 \text{V}$	40		MHZ

Thermal Characteristics $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation Derate above 25°C	60 480	W mW/°C
$R_{ heta JC}$	Thermal Resistance, Junction to Case	2.1	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient	62.5	°C/W

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^{*} Note) Device mounted on FR-\$ PCB 36mm*18mm*1.5mm: Mounting pad for the collector lead min. 6cm2.



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Definition of Terms				
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